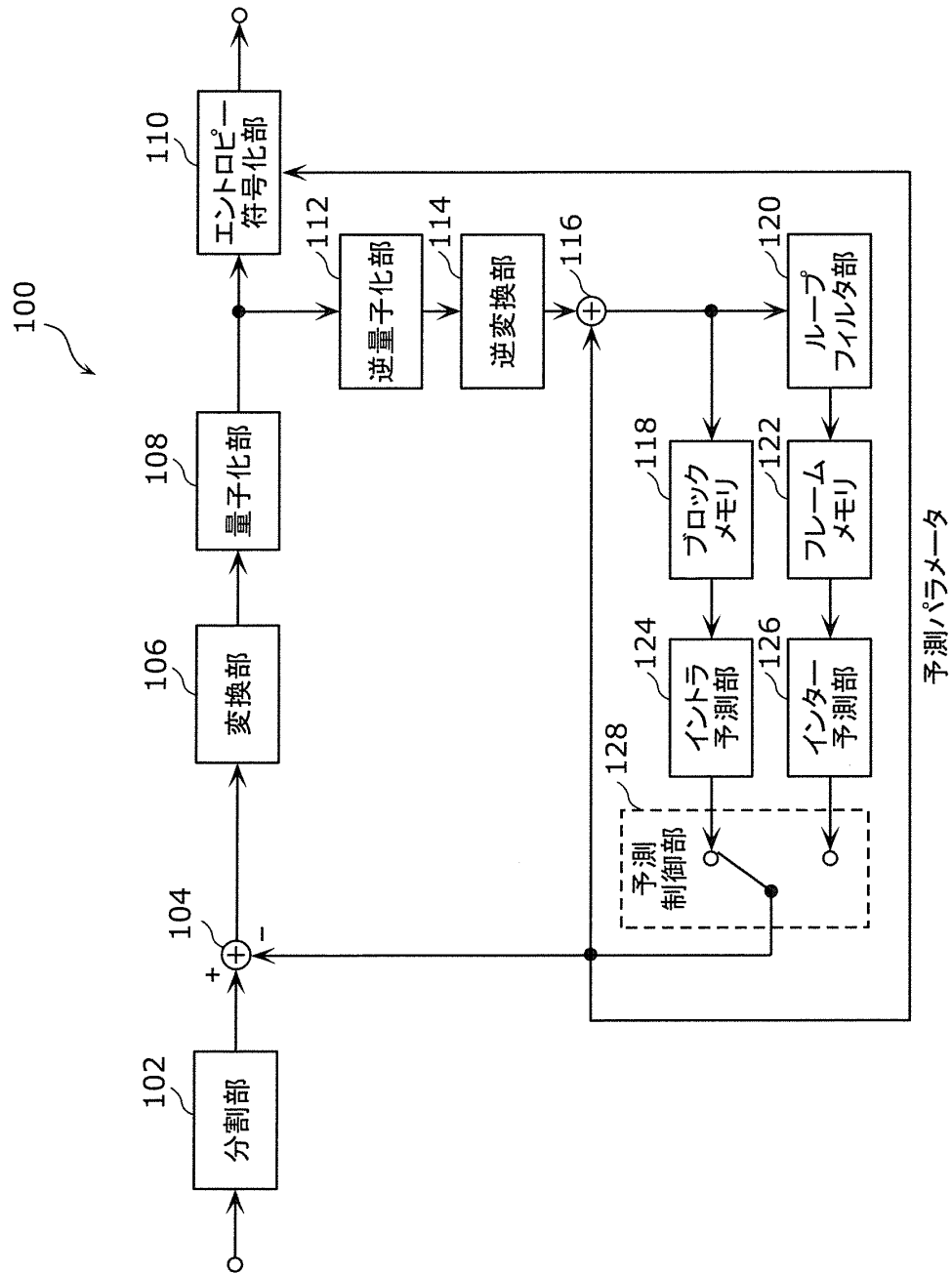
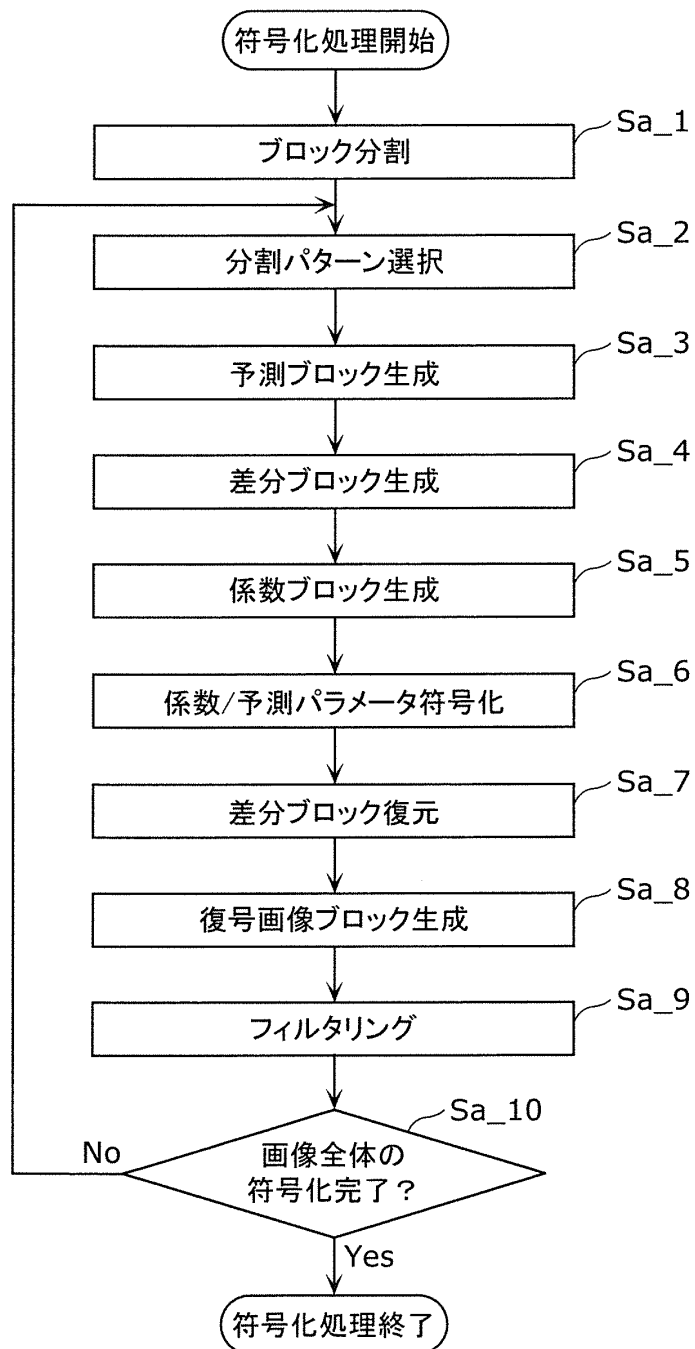


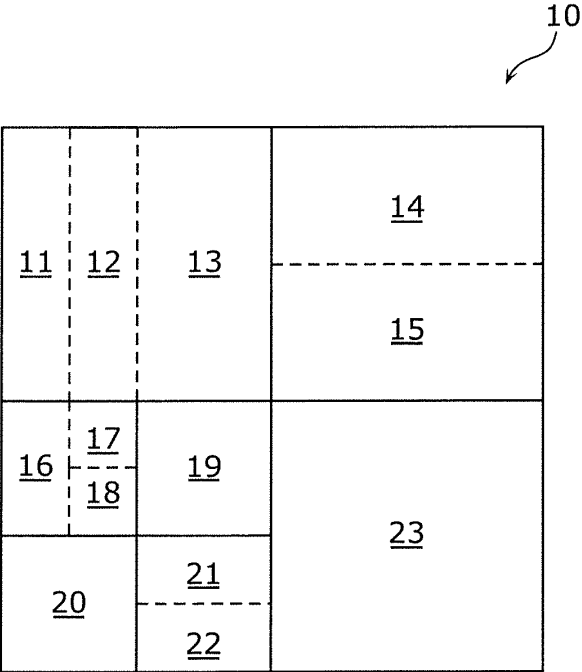
[図1]



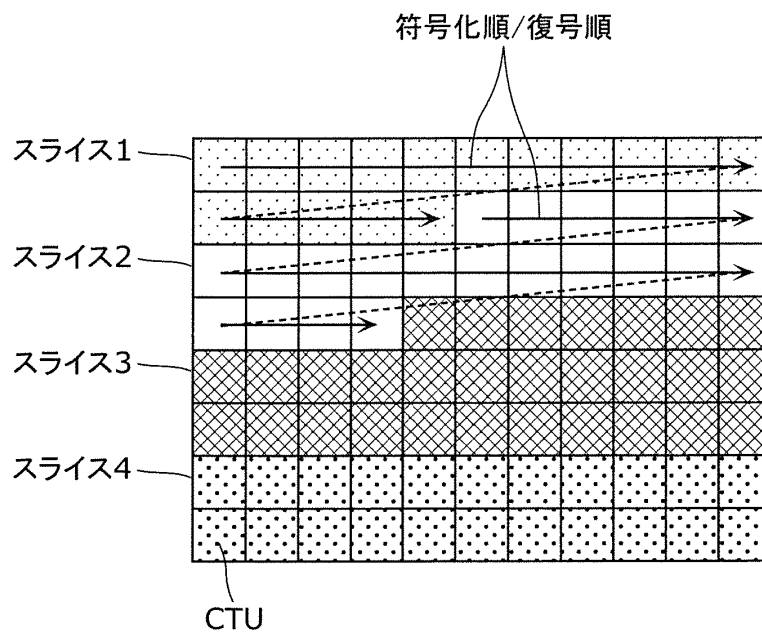
[図2]



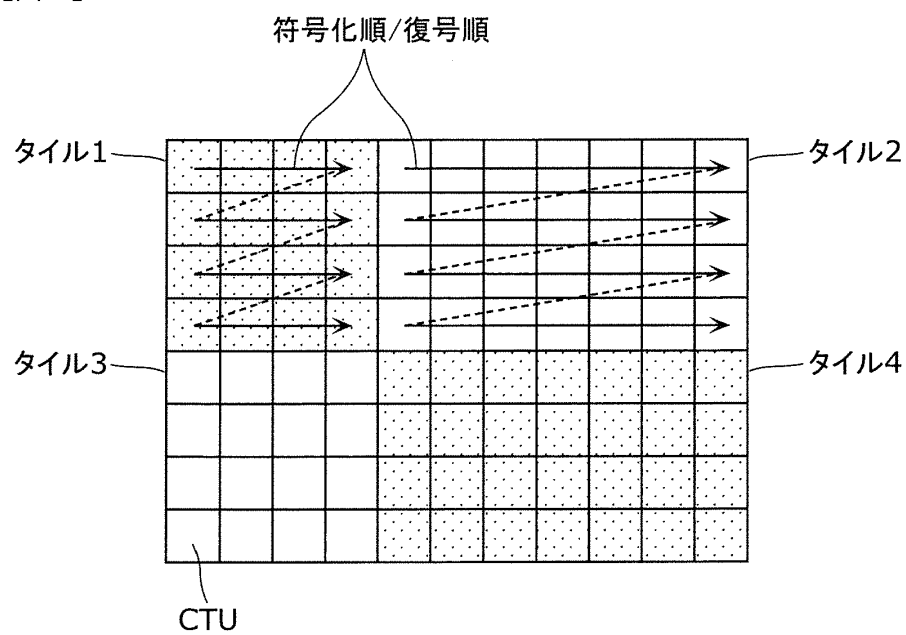
[図3]



[図4A]



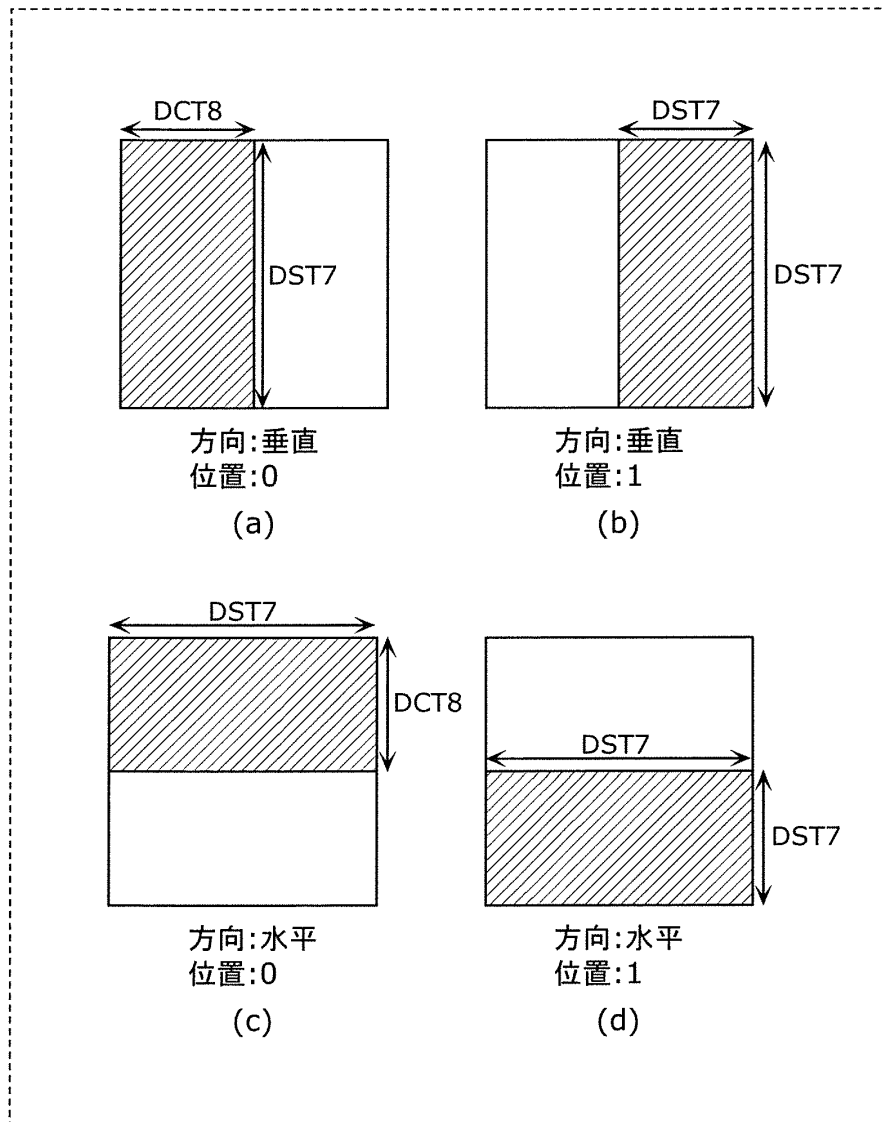
[図4B]



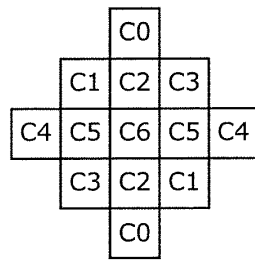
[図5A]

変換タイプ	基底関数 $T_i(j)$, $i, j=0, 1, \dots, N-1$
DCT-II	$T_i(j) = \omega_0 \cdot \sqrt{\frac{2}{N}} \cdot \cos\left(\frac{\pi \cdot i \cdot (2j + 1)}{2N}\right)$ $\text{where } \omega_0 = \begin{cases} \sqrt{\frac{2}{N}} & i = 0 \\ 1 & i \neq 0 \end{cases}$
DCT-V	$T_i(j) = \omega_0 \cdot \omega_1 \cdot \sqrt{\frac{2}{2N-1}} \cdot \cos\left(\frac{2\pi \cdot i \cdot j}{2N-1}\right)$ $\text{where } \omega_0 = \begin{cases} \sqrt{\frac{2}{N}} & i = 0 \\ 1 & i \neq 0 \end{cases}, \omega_1 = \begin{cases} \sqrt{\frac{2}{N}} & j = 0 \\ 1 & j \neq 0 \end{cases}$
DCT-VIII	$T_i(j) = \sqrt{\frac{4}{2N+1}} \cdot \cos\left(\frac{\pi \cdot (2i+1) \cdot (2j+1)}{4N+2}\right)$
DST-I	$T_i(j) = \sqrt{\frac{2}{N+1}} \cdot \sin\left(\frac{\pi \cdot (i+1) \cdot (j+1)}{N+1}\right)$
DST-VII	$T_i(j) = \sqrt{\frac{4}{2N+1}} \cdot \sin\left(\frac{\pi \cdot (2i+1) \cdot (j+1)}{2N+1}\right)$

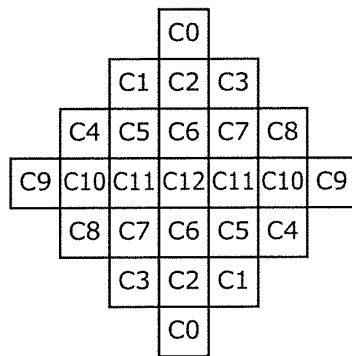
[図5B]



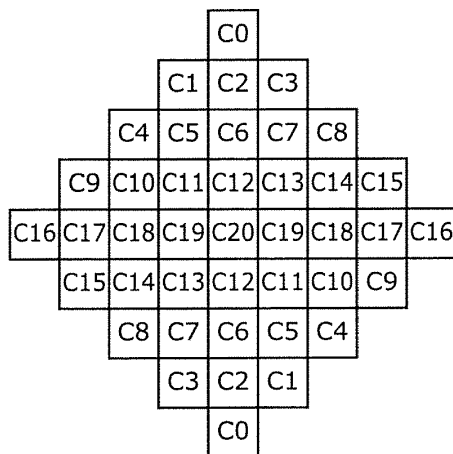
[図6A]



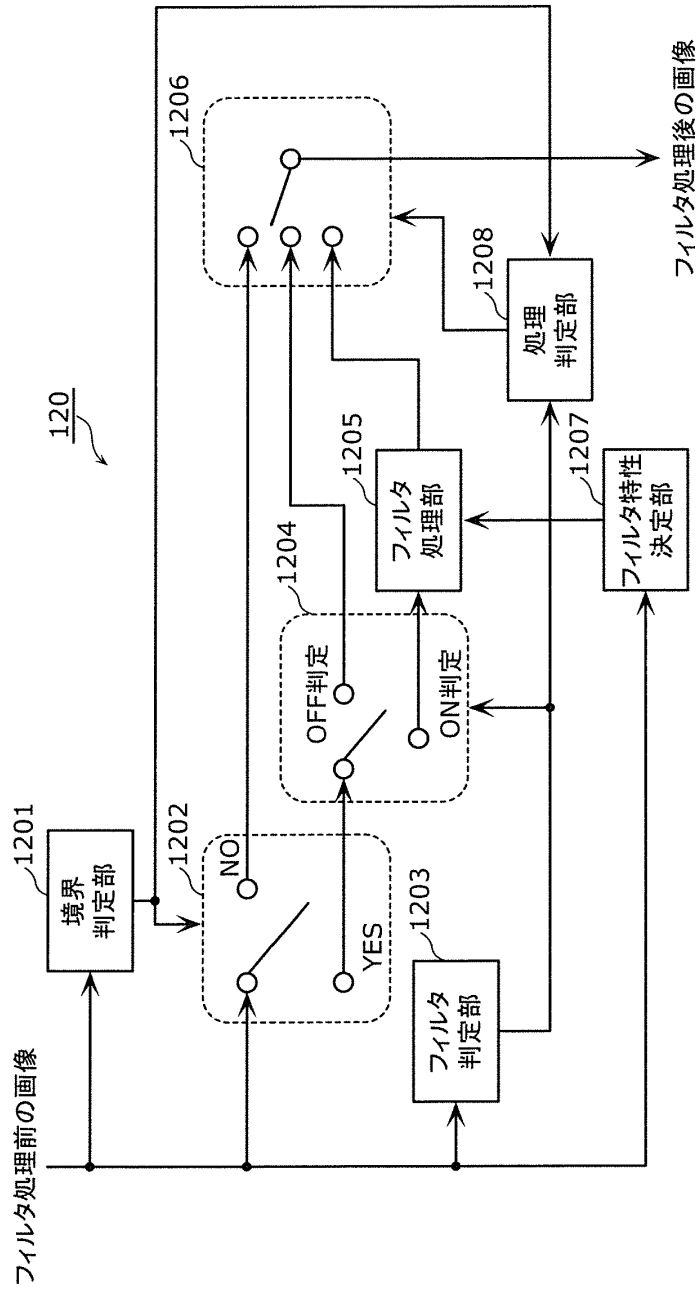
[図6B]



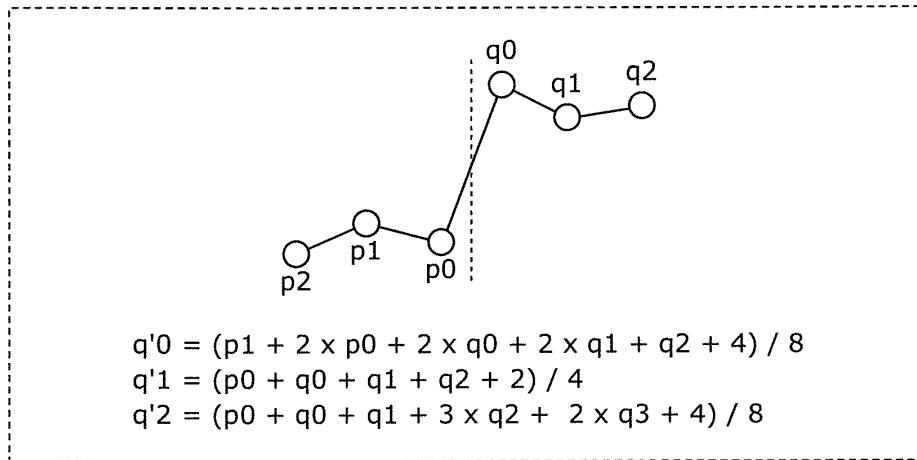
[図6C]



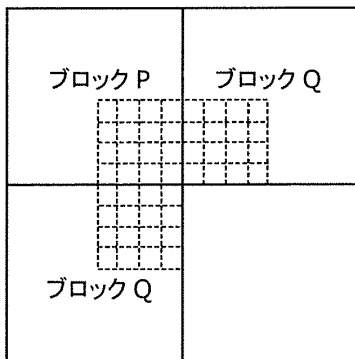
[図7]



[図8]



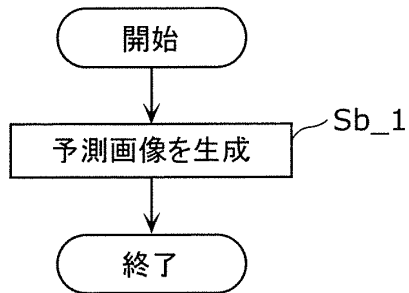
[図9]



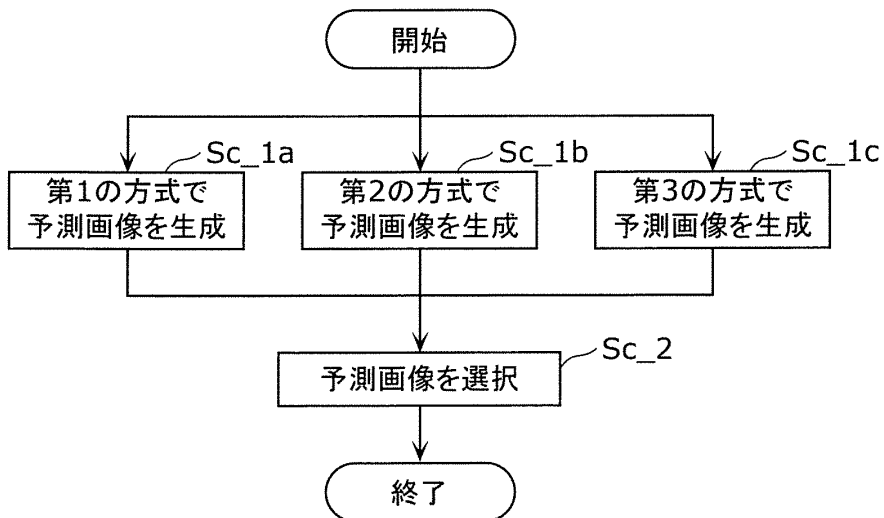
[図10]

Bs 値を決定するブロック境界を挟むブロックPとQ の条件	Bs 値
・ 少なくとも一方のブロックが画面内予測ブロックである	2
・ 少なくとも片方のブロックに非零の直交変換係数が存在し、TU 境界である	1
・ 境界を挟む 2 つのブロックの動きベクトルの差の絶対値が 1 画素以上である	1
・ 境界を挟む 2 つのブロックの動き補償の参照画像が異なるか、動きベクトルの数が異なる	1
・ 上記以外	0

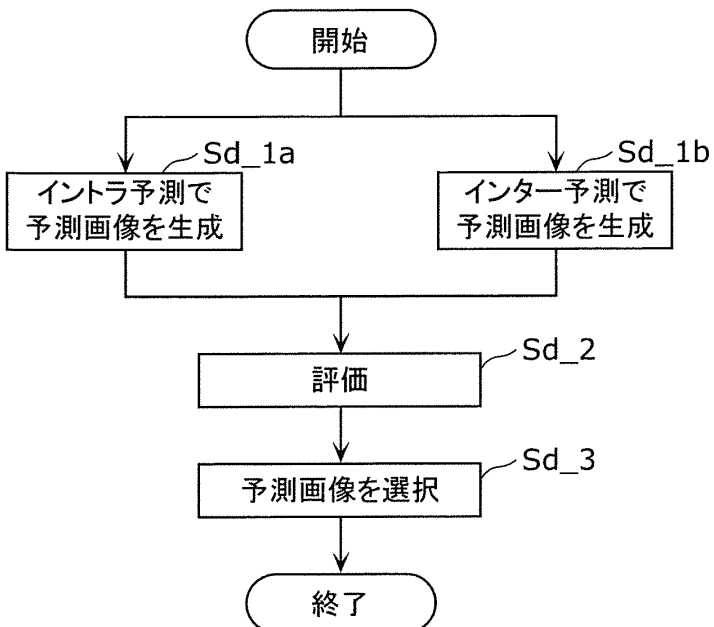
[図11]



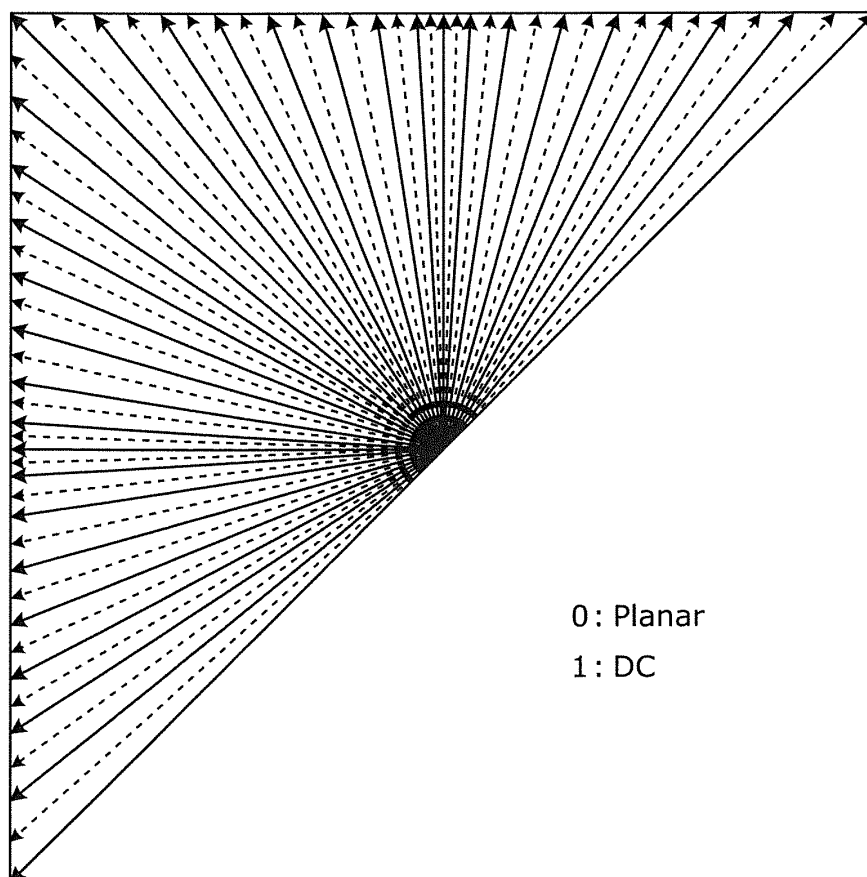
[図12]



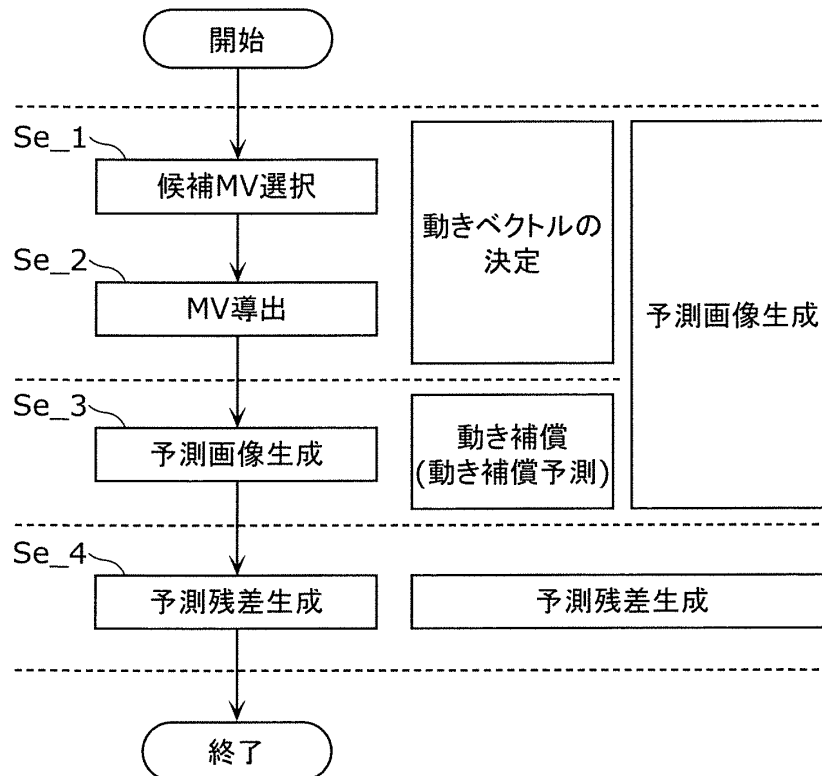
[図13]



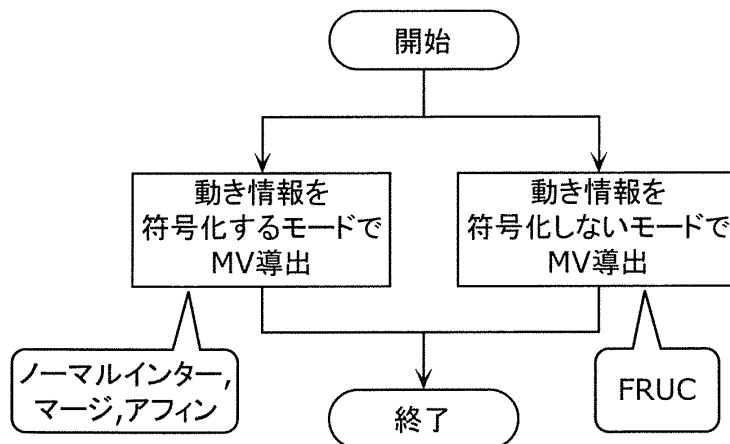
[図14]



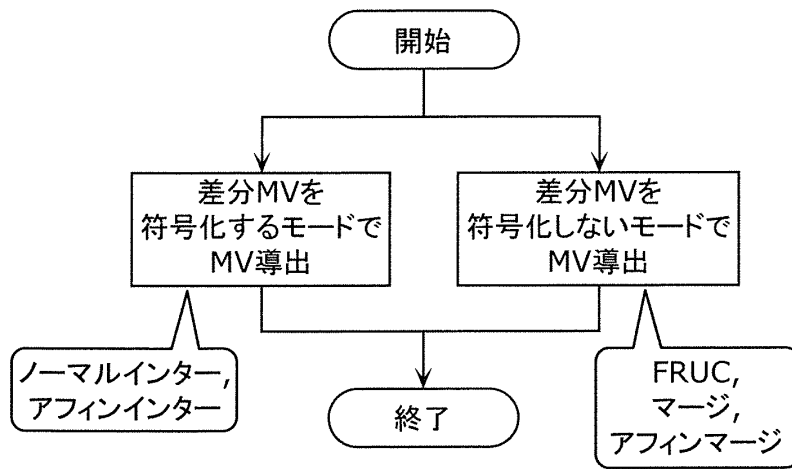
[図15]



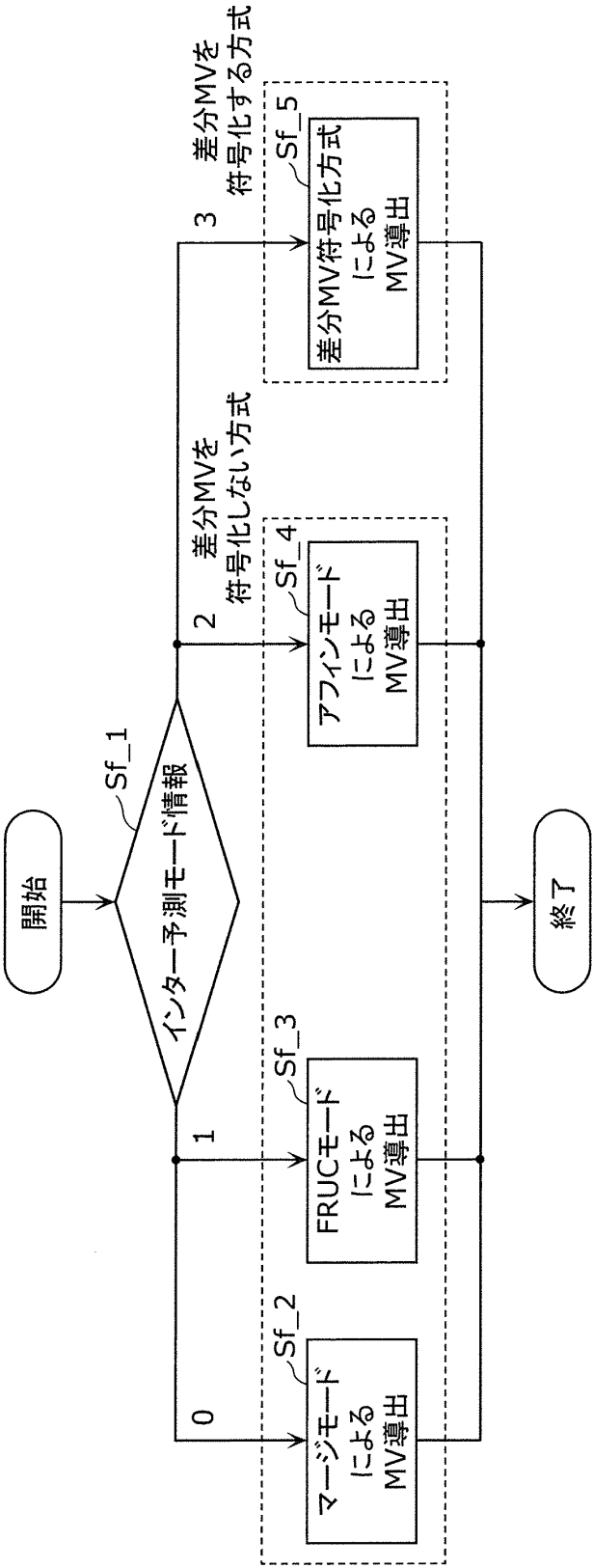
[図16]



[図17]



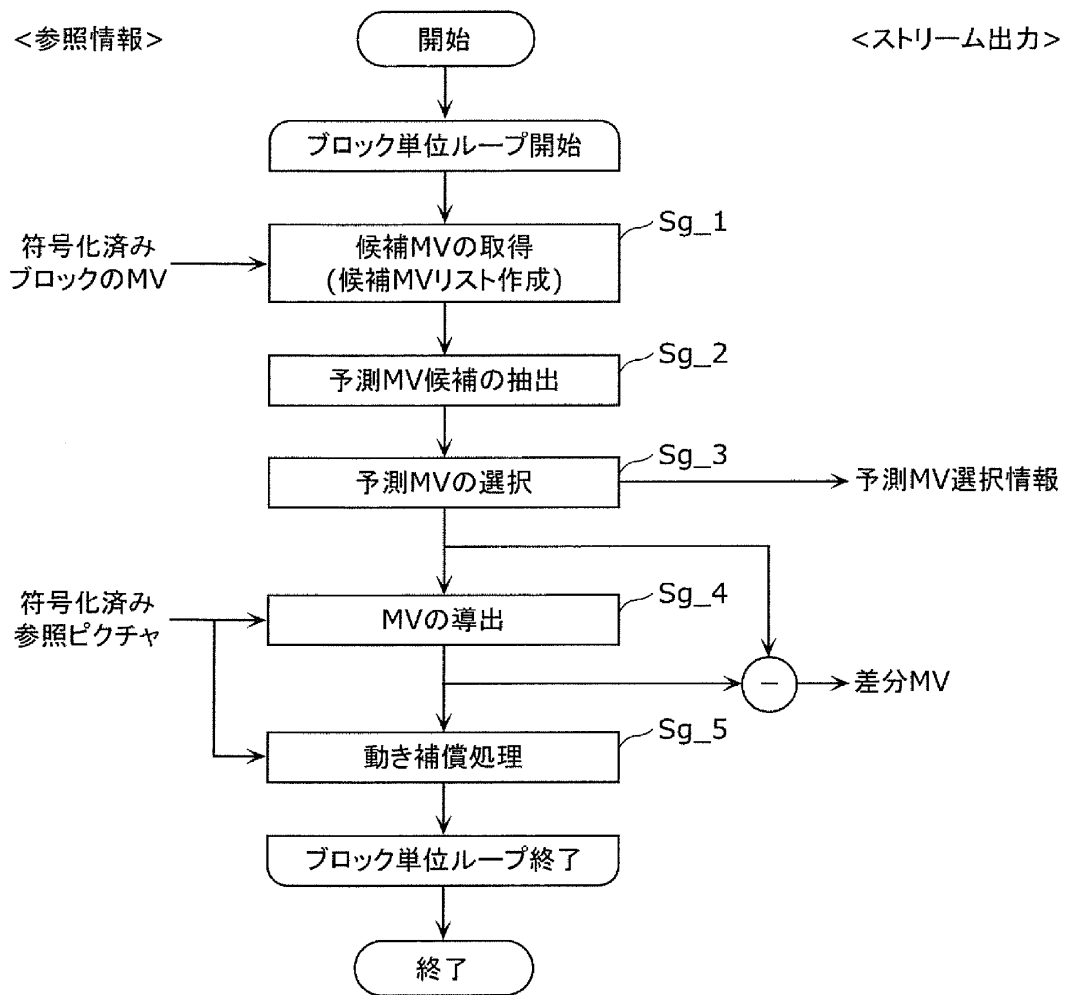
[図18]



[図19]

<参照情報>

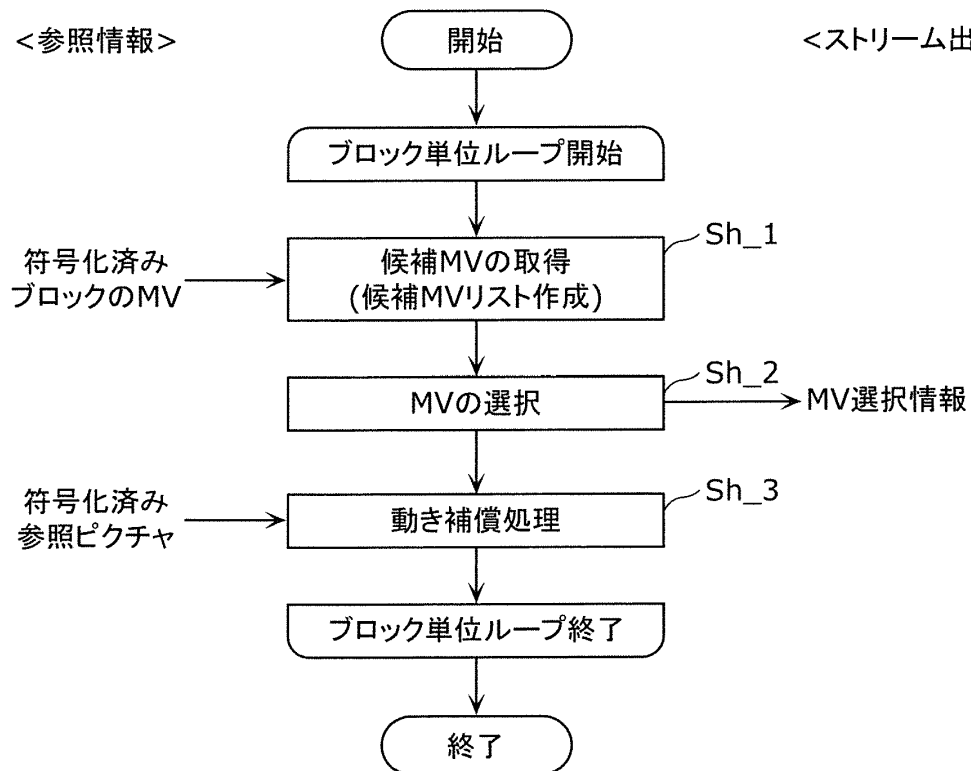
<ストリーム出力>



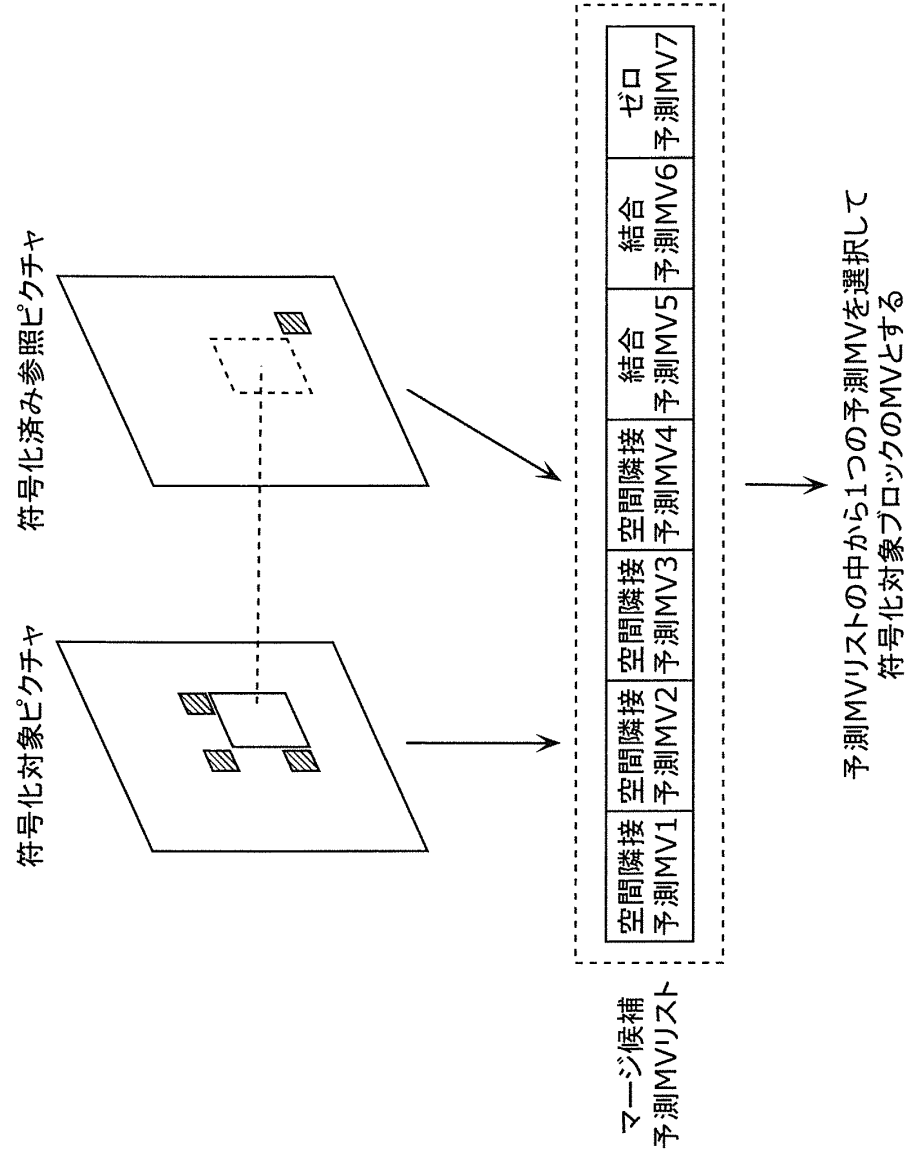
[図20]

<参照情報>

<ストリーム出力>



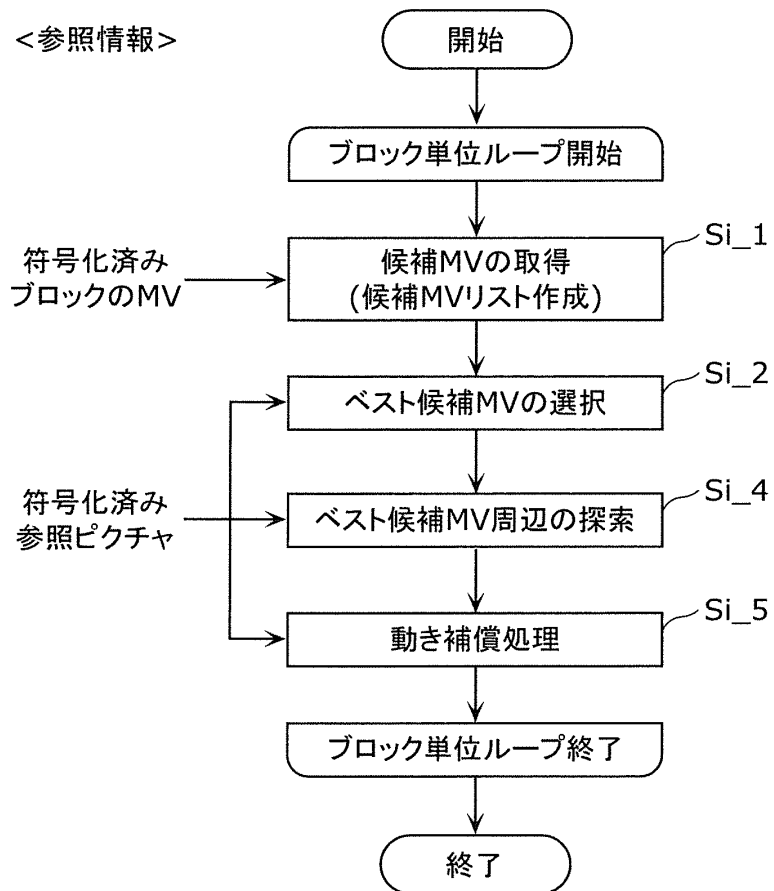
[図21]



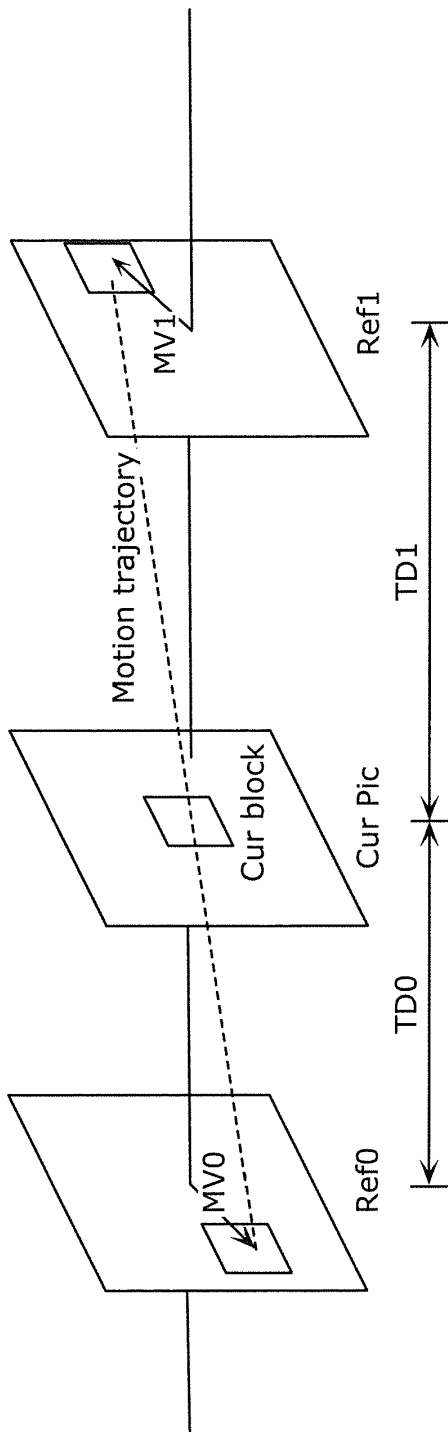
[図22]

<参照情報>

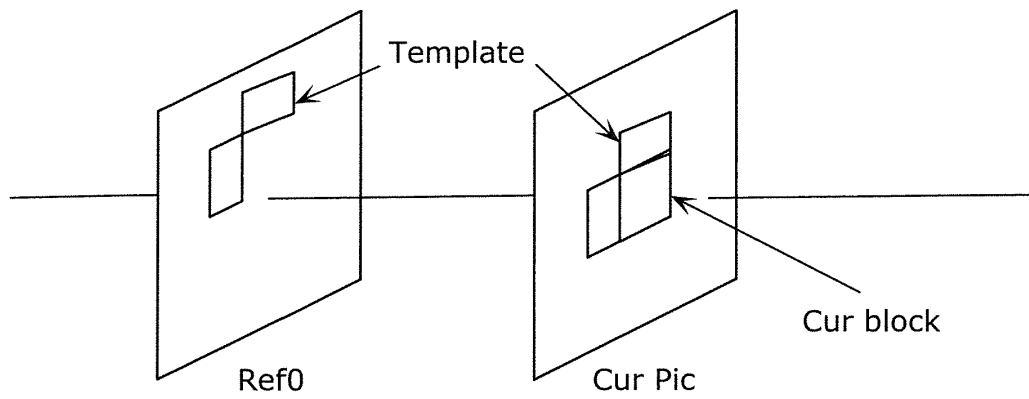
<ストリーム出力>



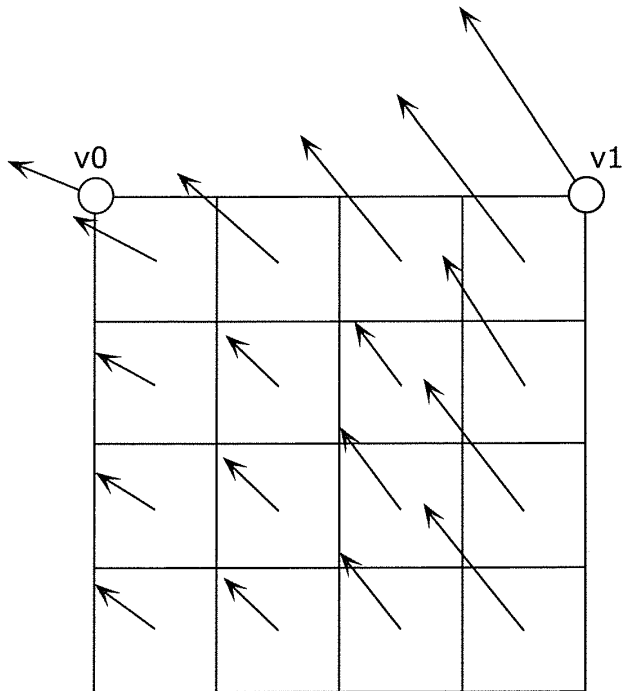
[図23]



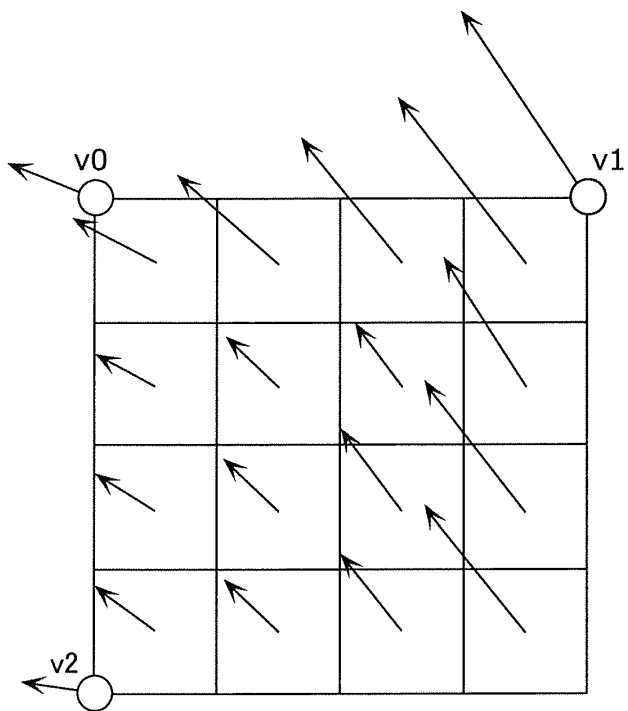
[図24]



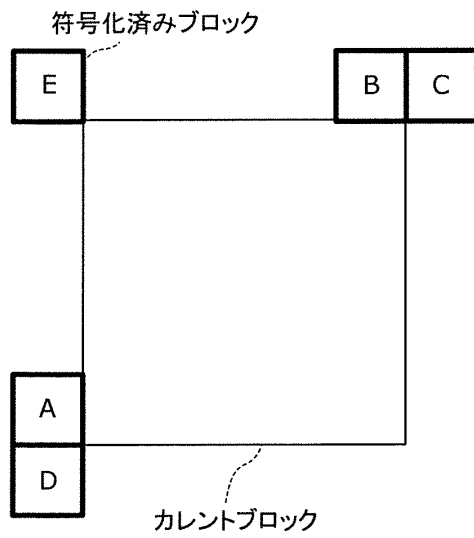
[図25A]



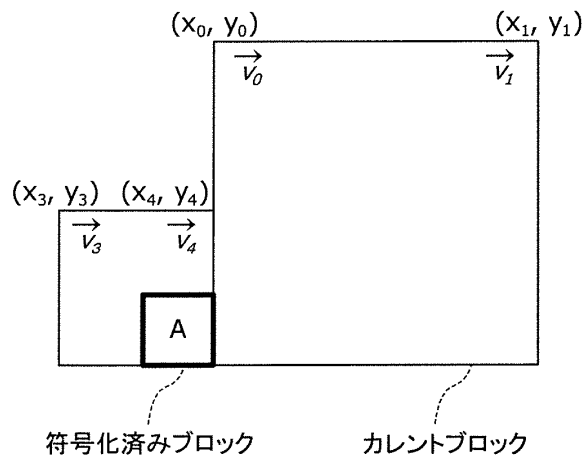
[図25B]



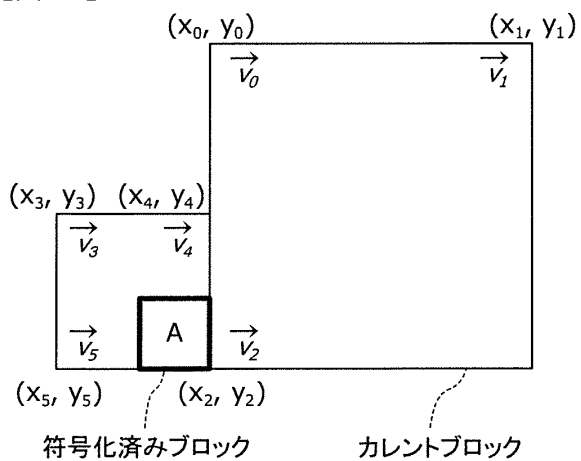
[図26A]



[図26B]



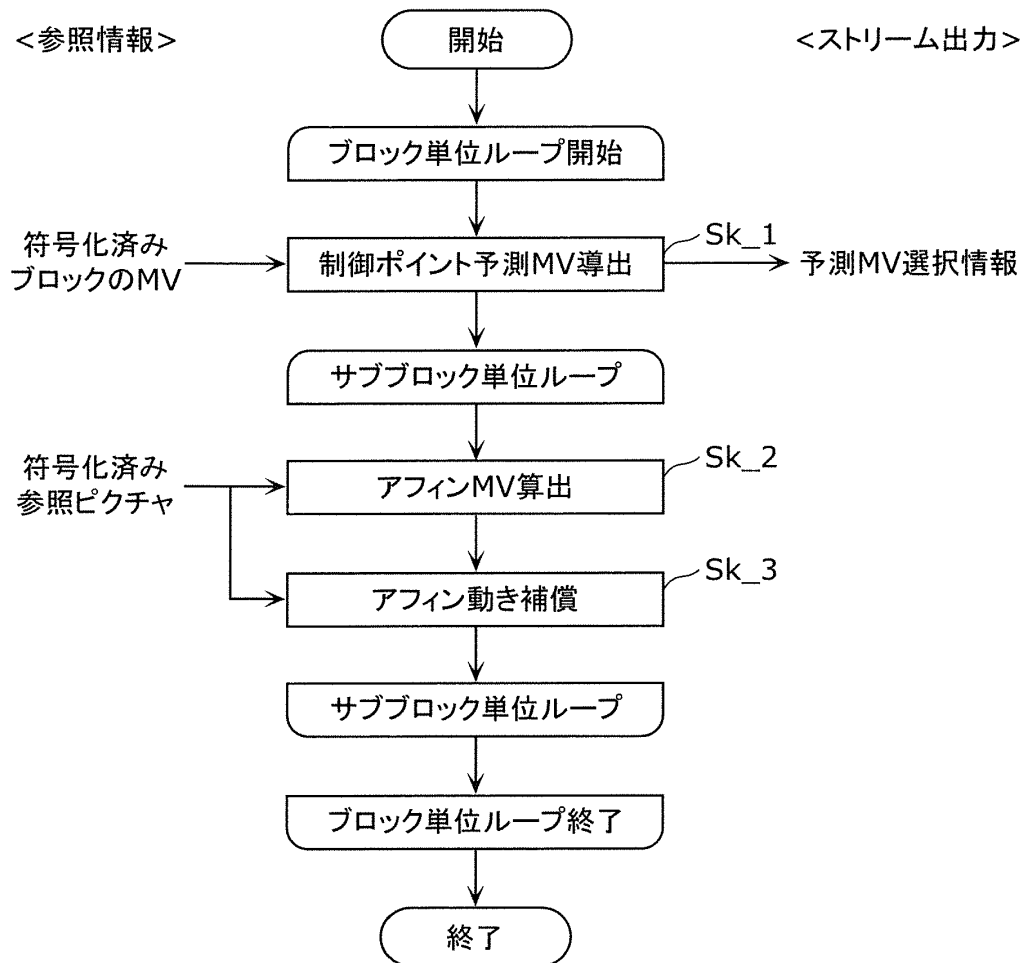
[図26C]



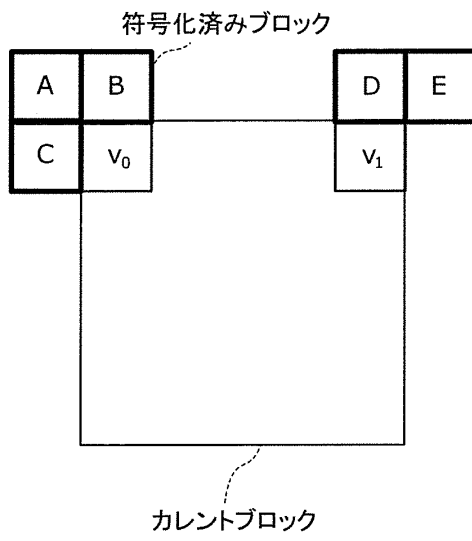
[図27]

<参照情報>

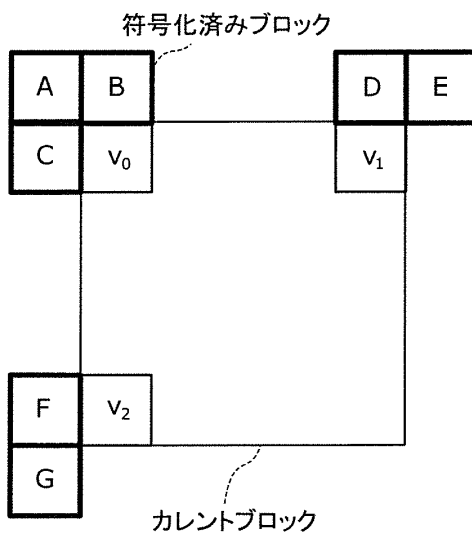
<ストリーム出力>



[図28A]



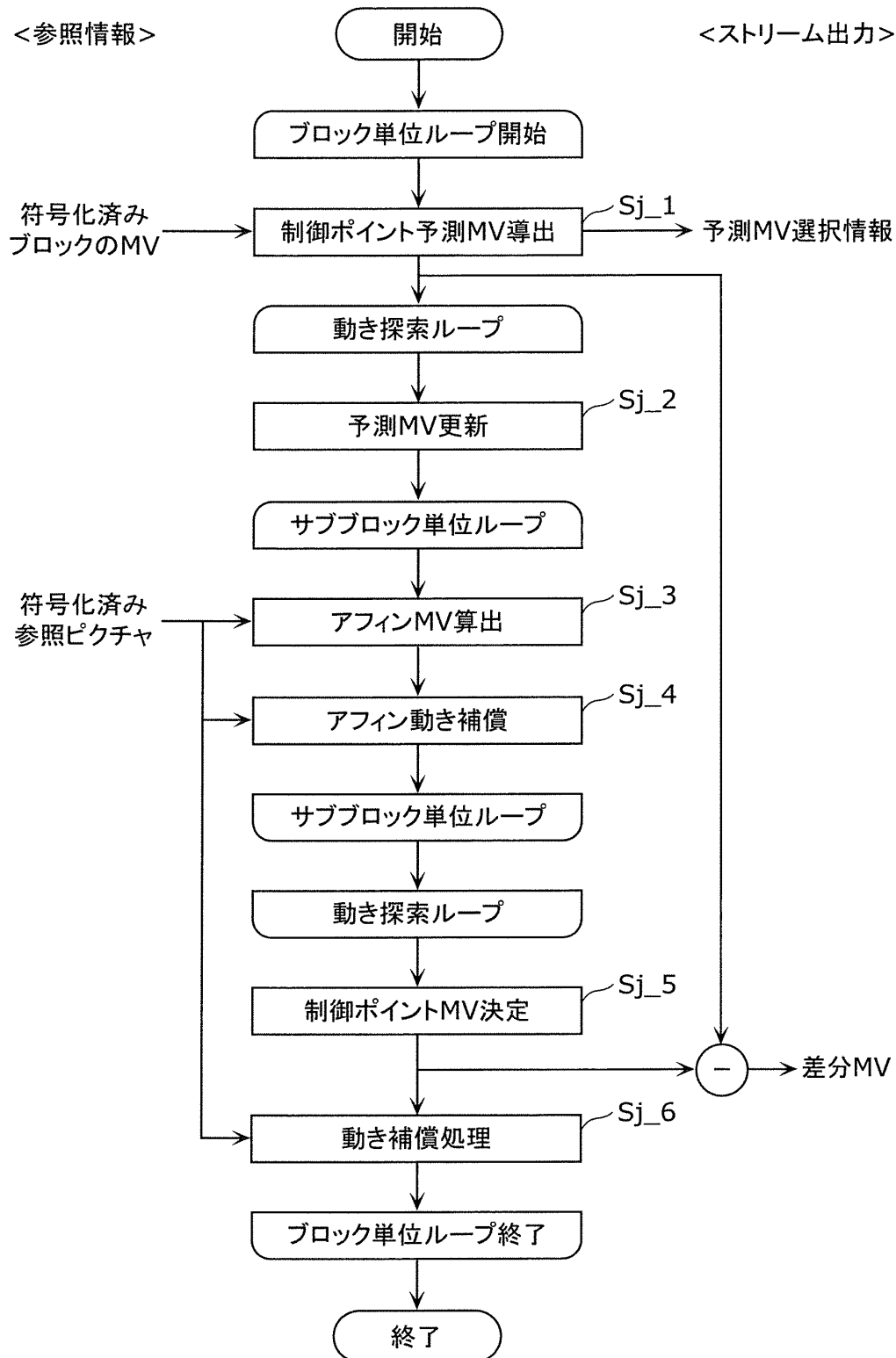
[図28B]



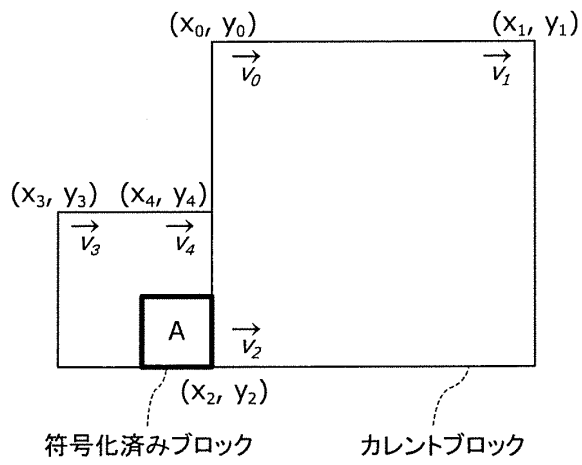
[図29]

<参照情報>

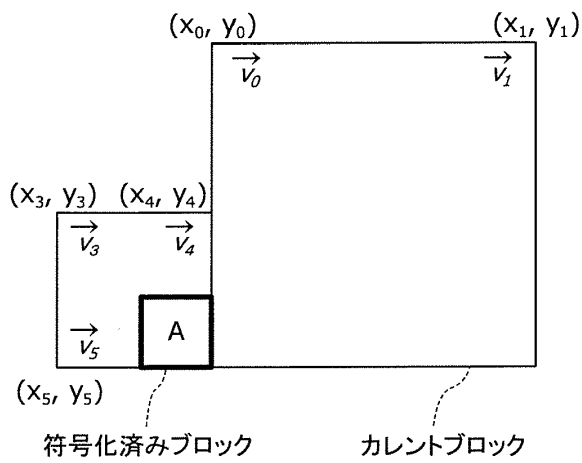
<ストリーム出力>



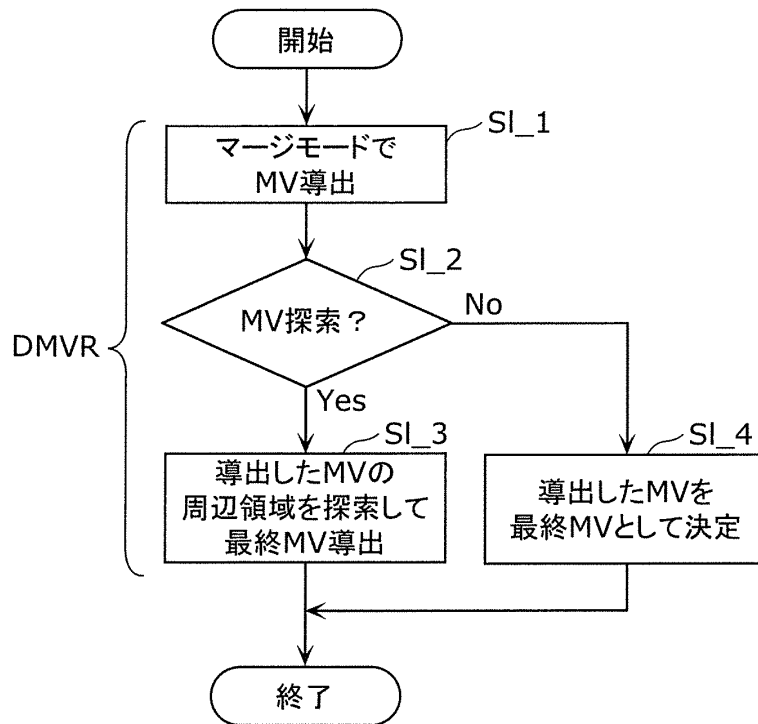
[図30A]



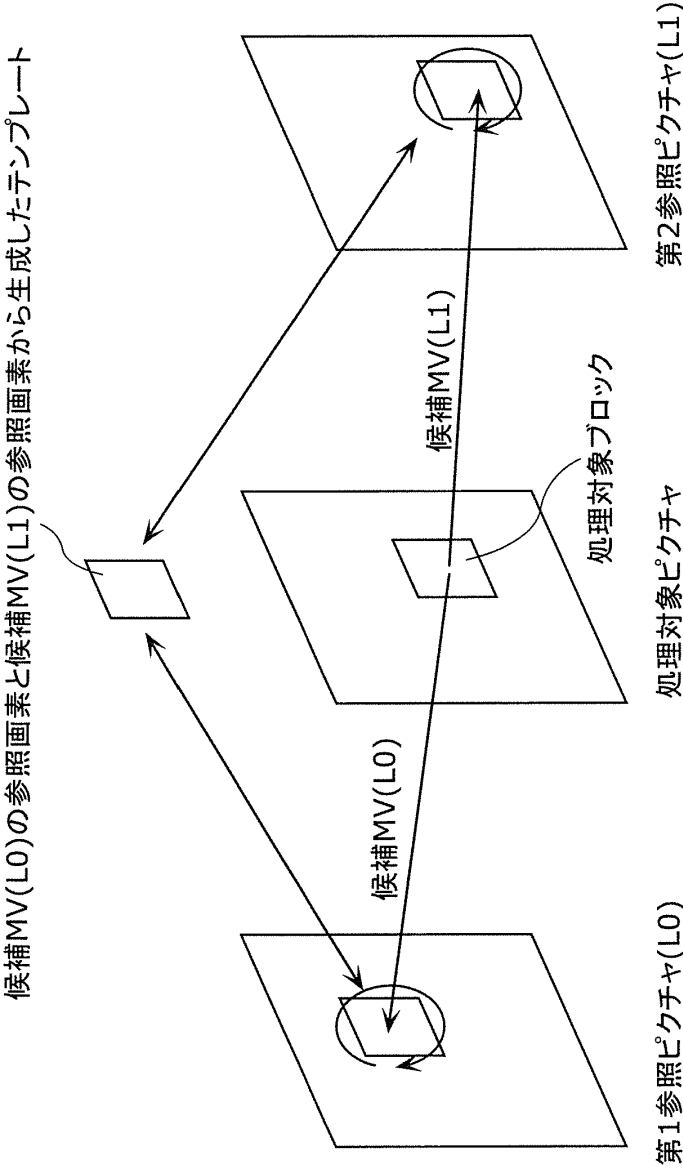
[図30B]



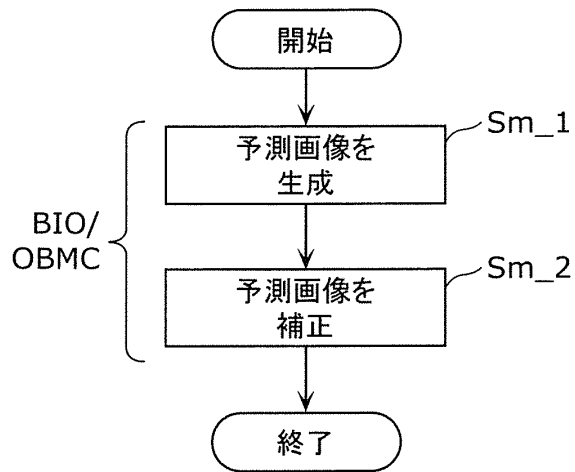
[図31A]



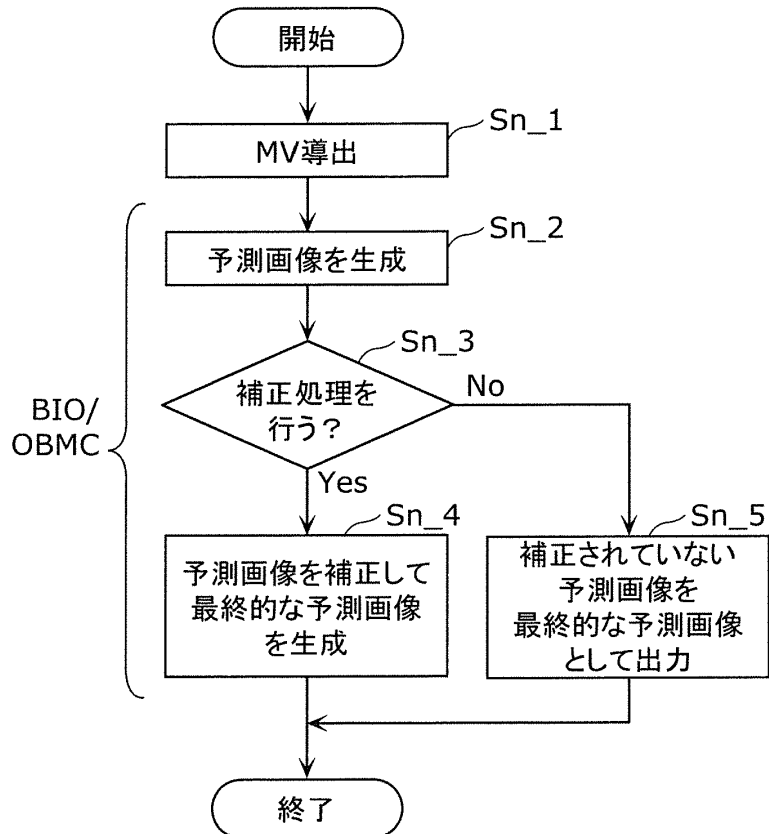
[図31B]



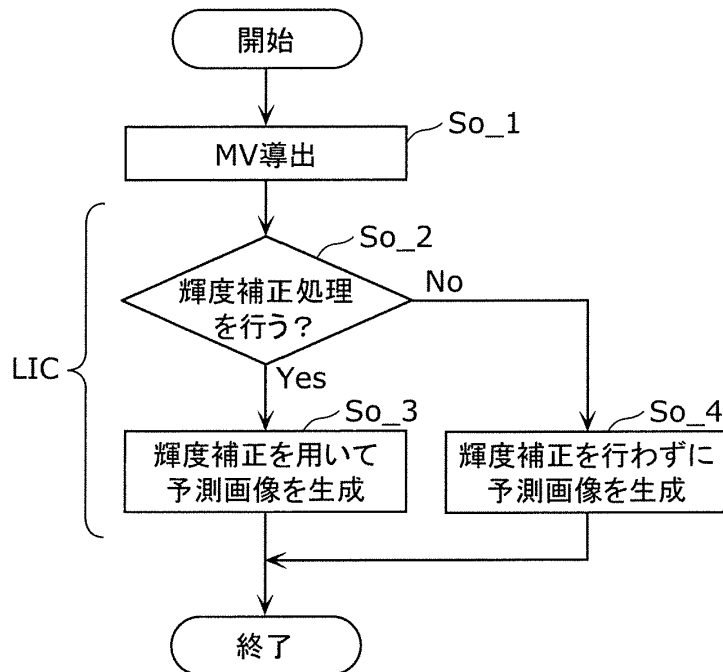
[図32]



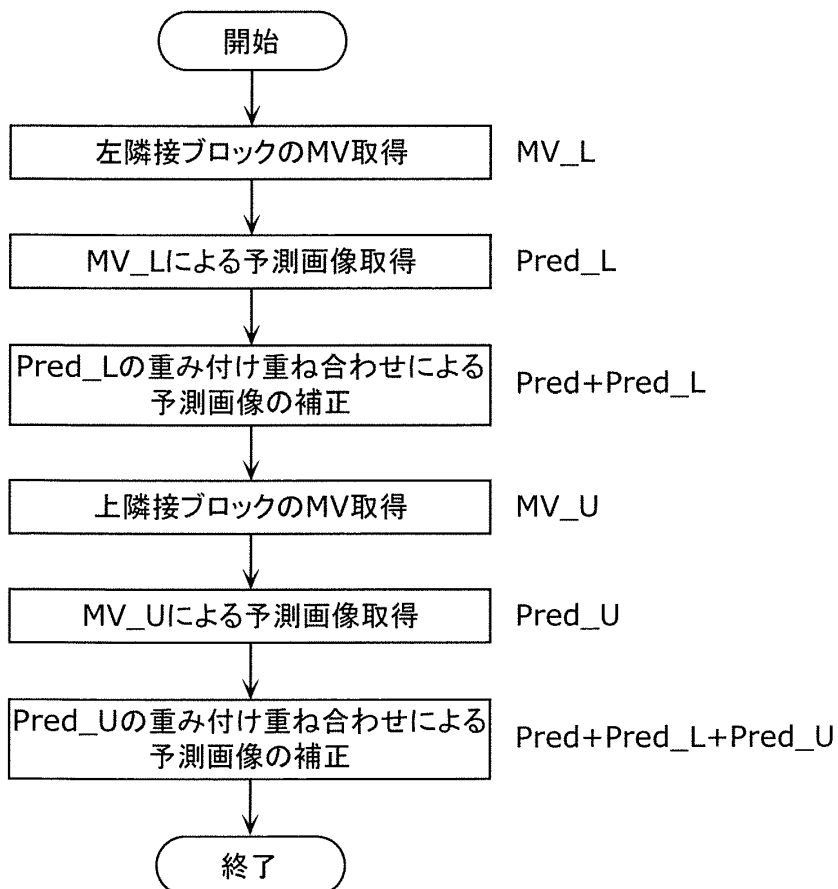
[図33]



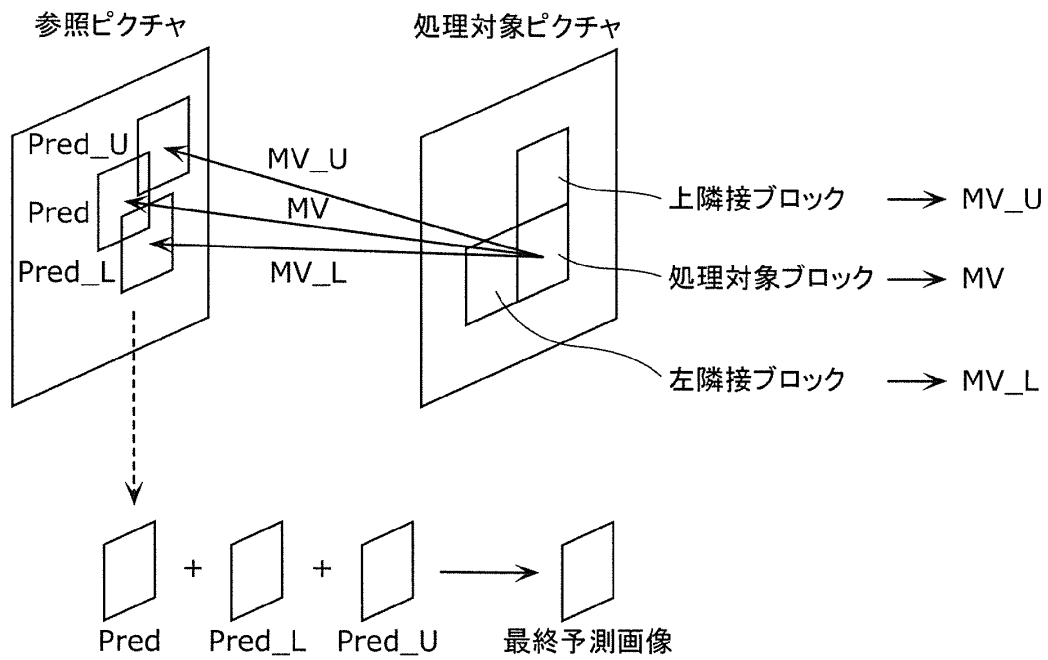
[図34]



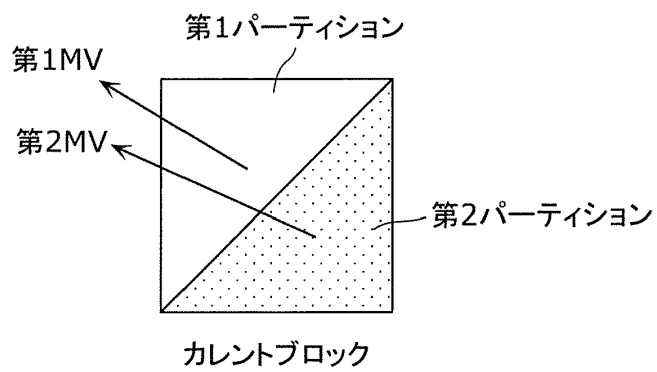
[図35]



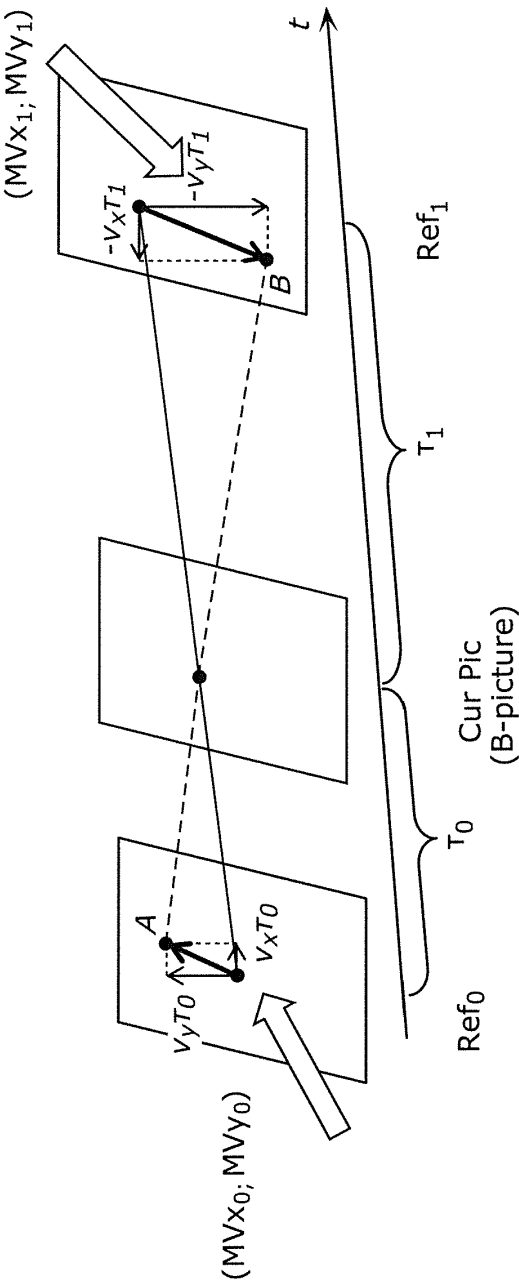
[図36]



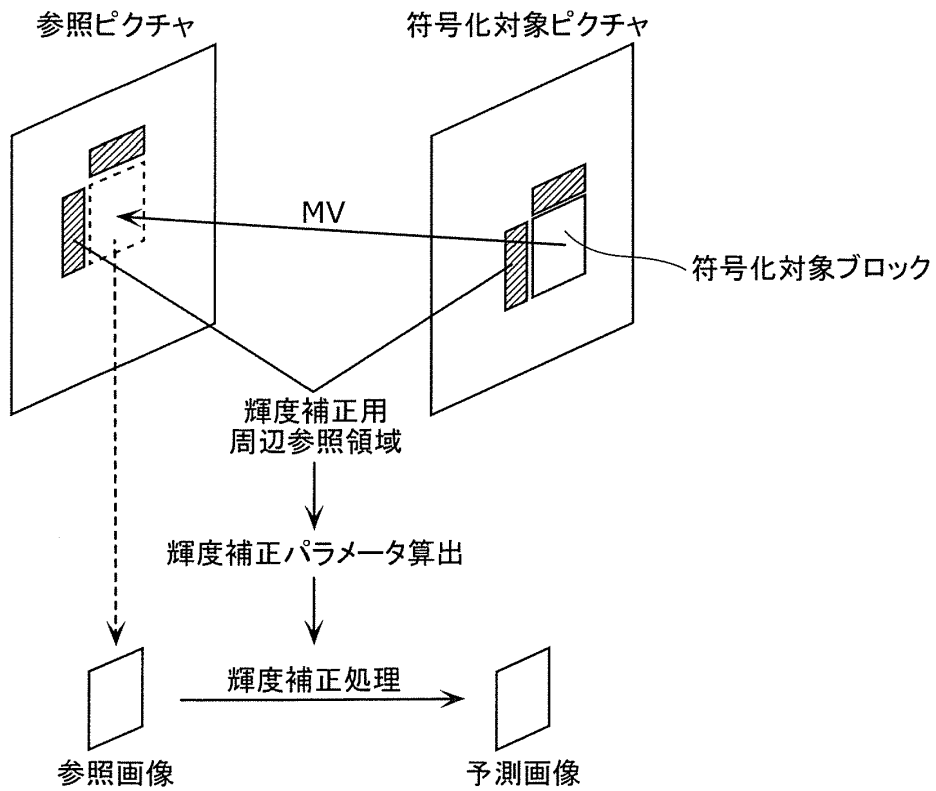
[図37]



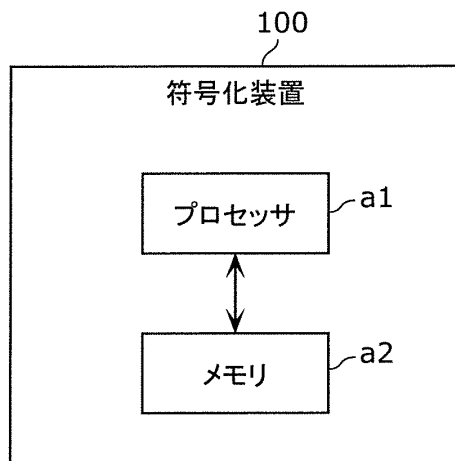
[図38]



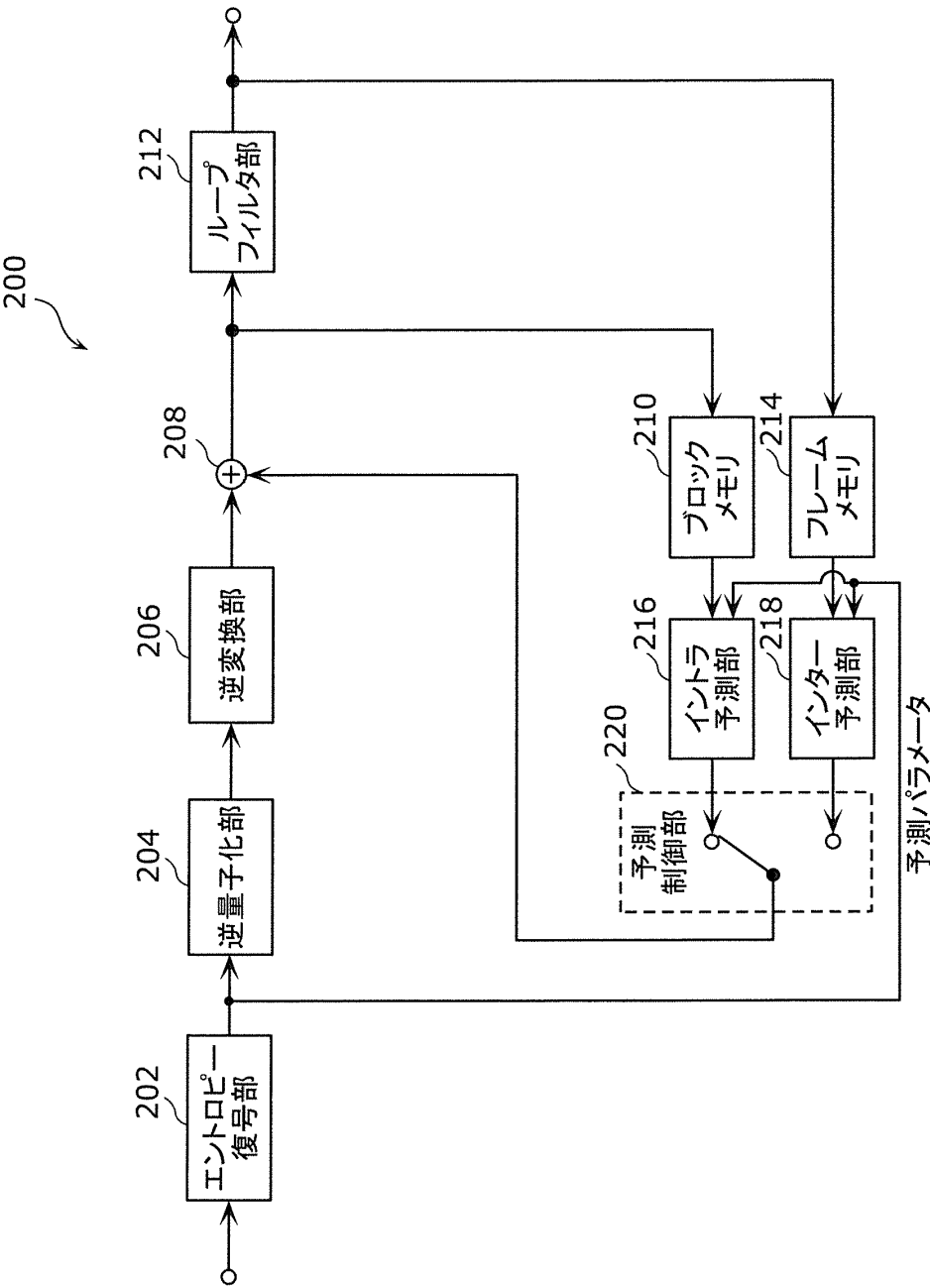
[図39]



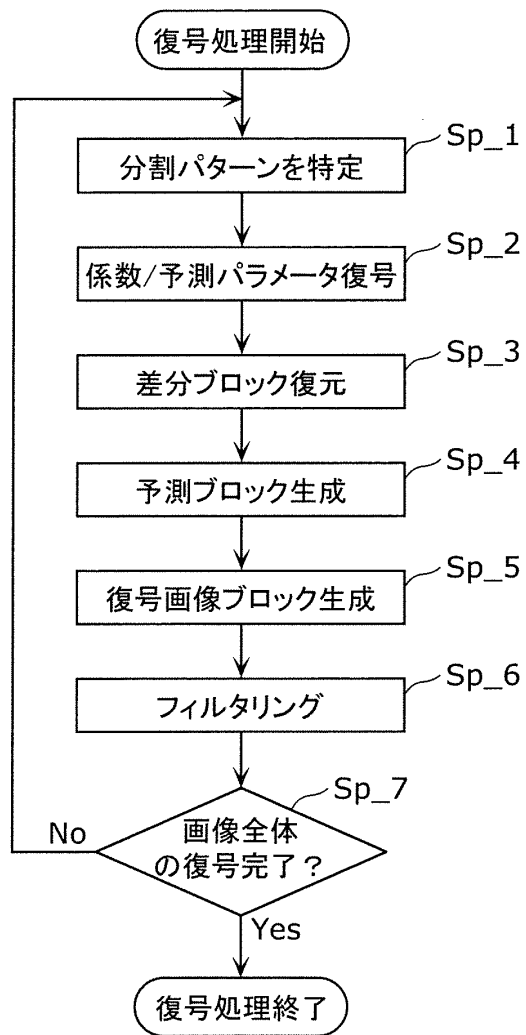
[図40]



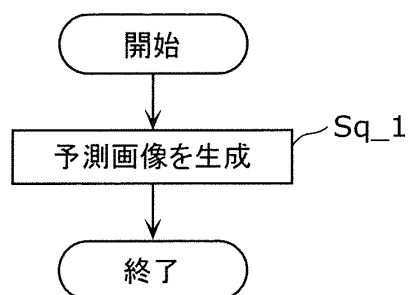
[図41]



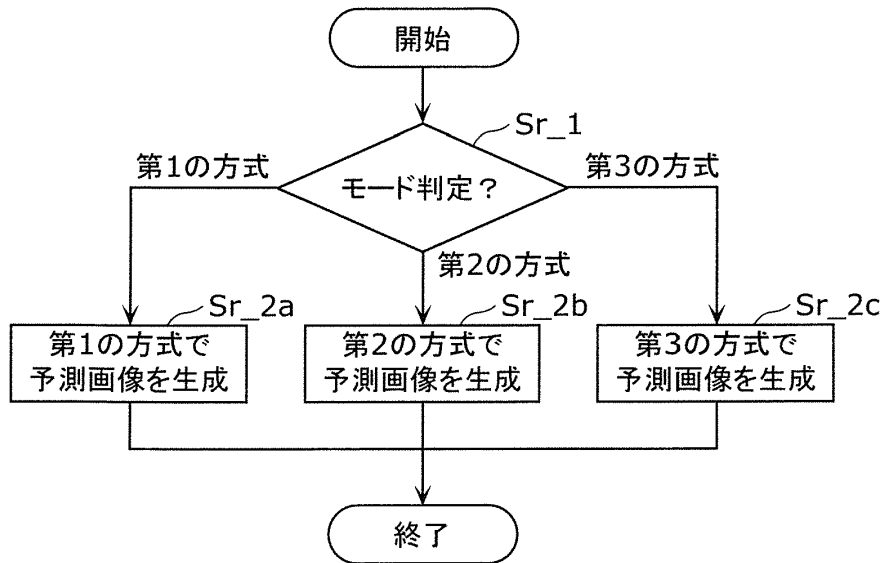
[図42]



[図43]



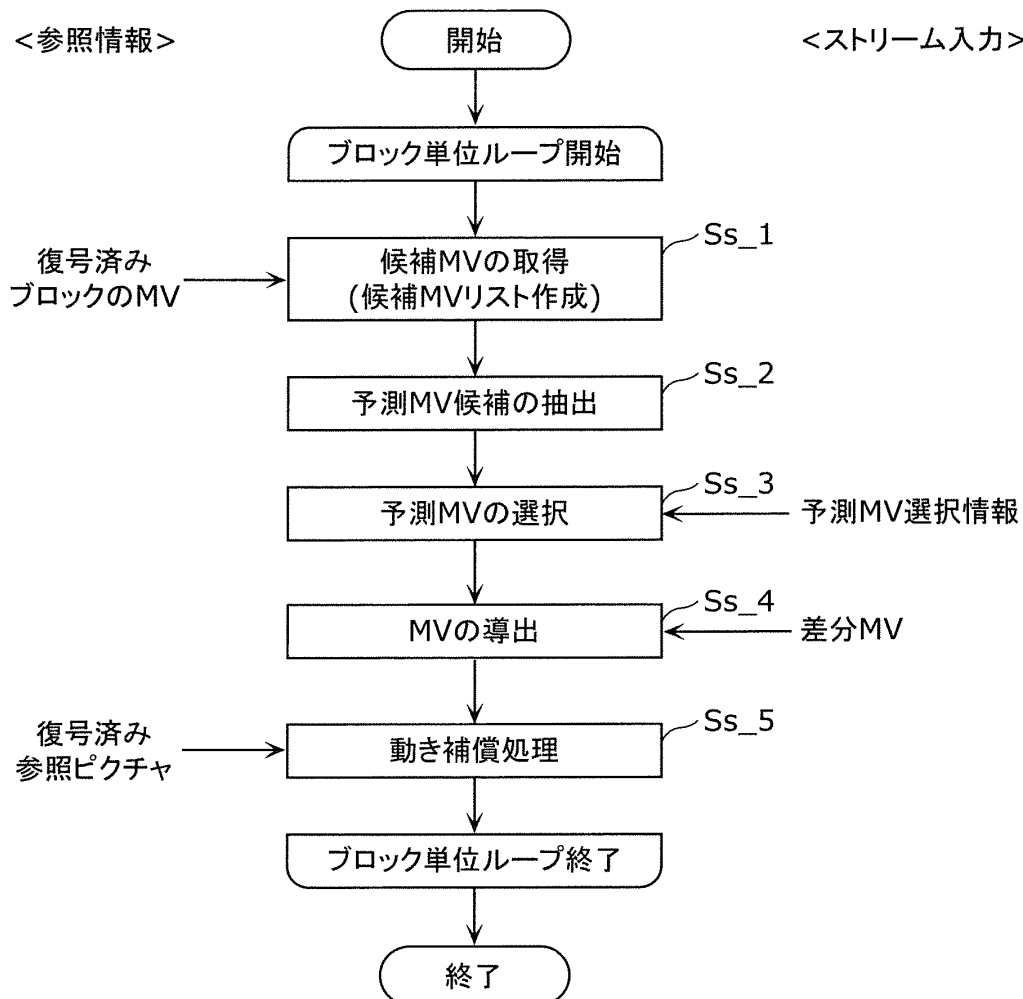
[図44]



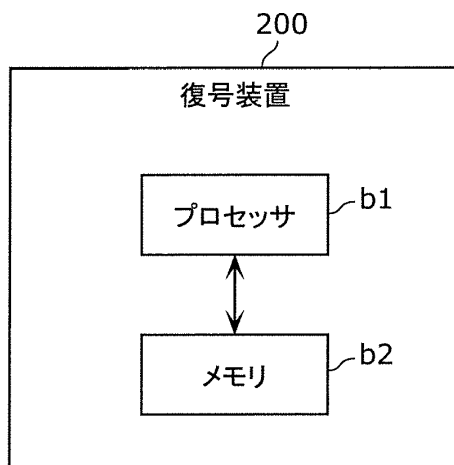
[図45]

<参照情報>

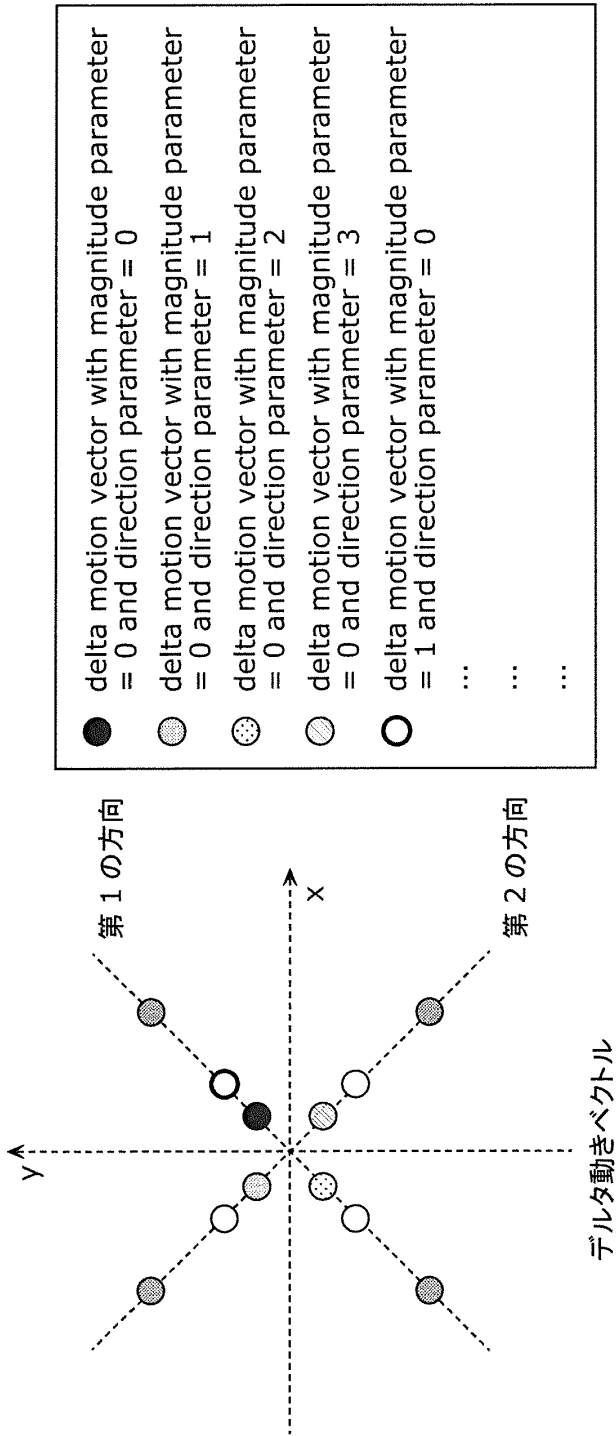
<ストリーム入力>



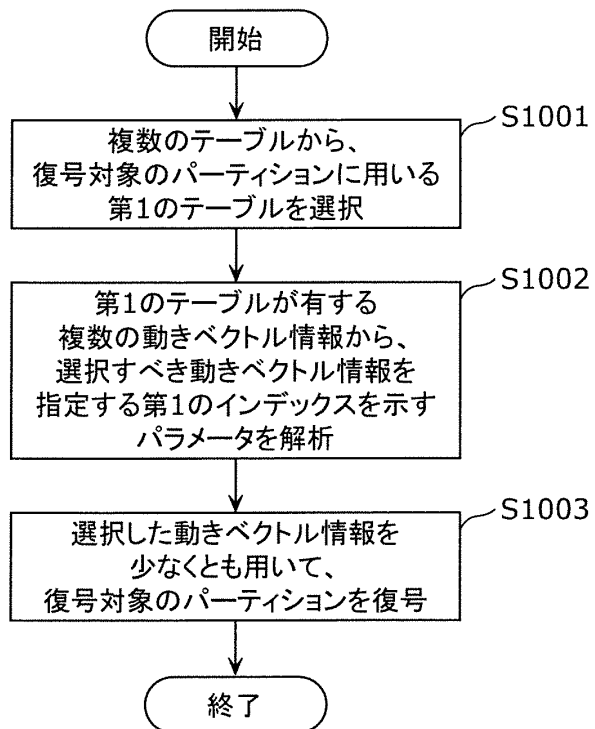
[図46]



[図47]



[図48]



[図49]

表 1

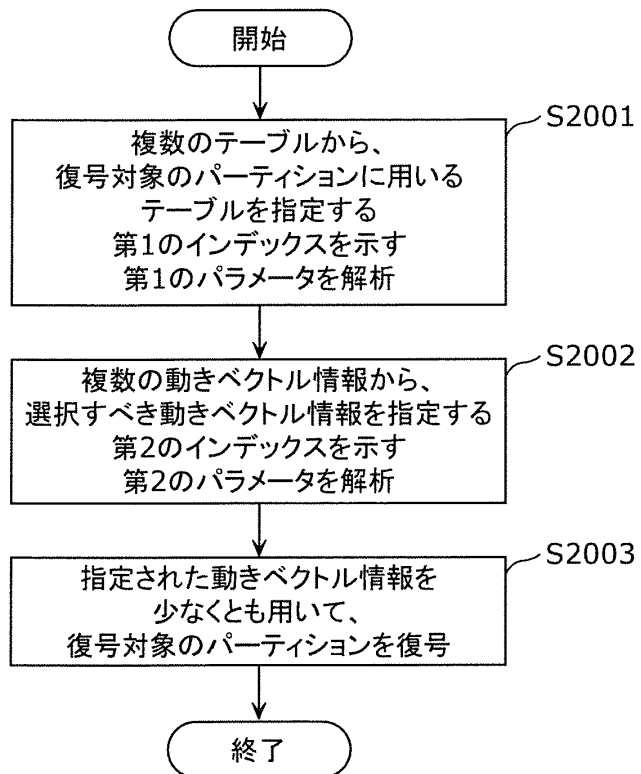
table index	magnitude value (in 1/4 pel)							
	1	2	4	8	16	32	64	128
0	1	2	4	8	16	32	64	128
1	2	4	8	16	32	64	128	256
2	4	8	16	32	64	128	256	512
3	8	16	32	64	128	256	512	1024

[図50]

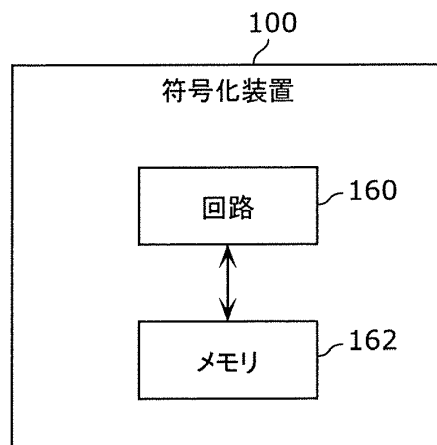
表 2

table index	magnitude value (in 1/4 pel)									
	1	2	4	8	16	32	64	128	256	1024
0	1	2	4	8	16	32	64	128	256	1024
1	2	4	8	16	32	64	128	256	512	1024
2	4	8	16	32	64	128	256	512	1024	2048
3	8	16	32	64	128	256	512	1024	2048	4096
4	16	32	64	128	256	512	1024	2048	4096	8192
5	32	64	128	256	512	1024	2048	4096	8192	16384
6	64	128	256	512	1024	2048	4096	8192	16384	32768
7	128	256	512	1024	2048	4096	8192	16384	32768	65536

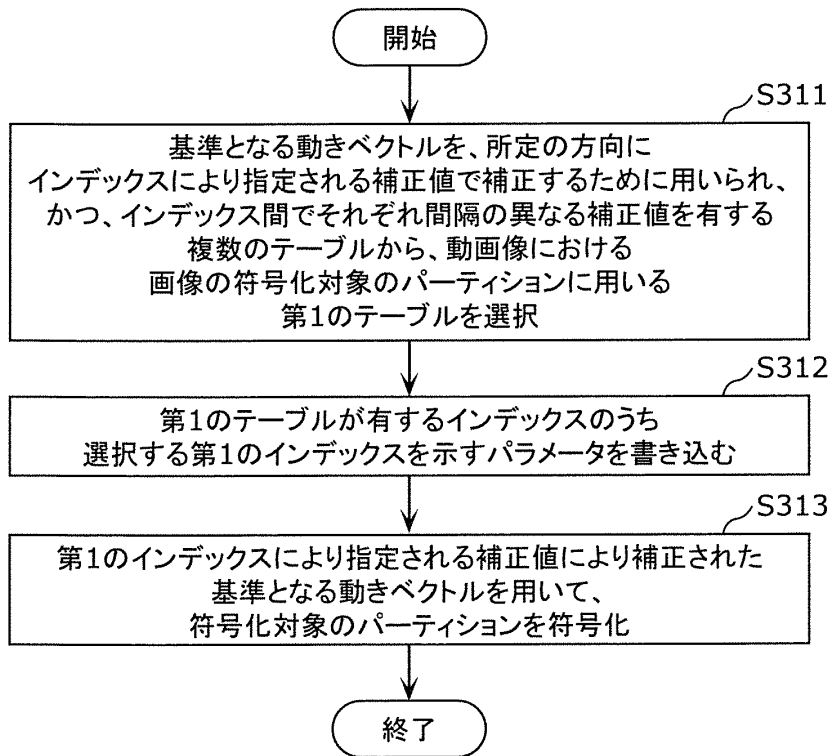
[図51]



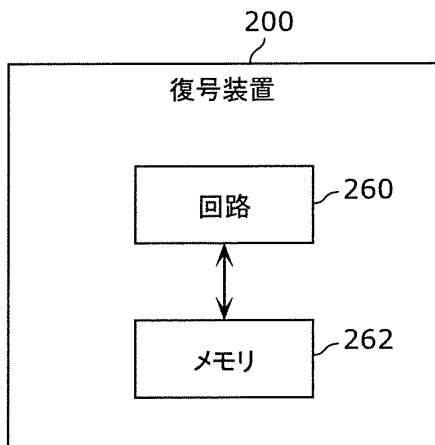
[図52]



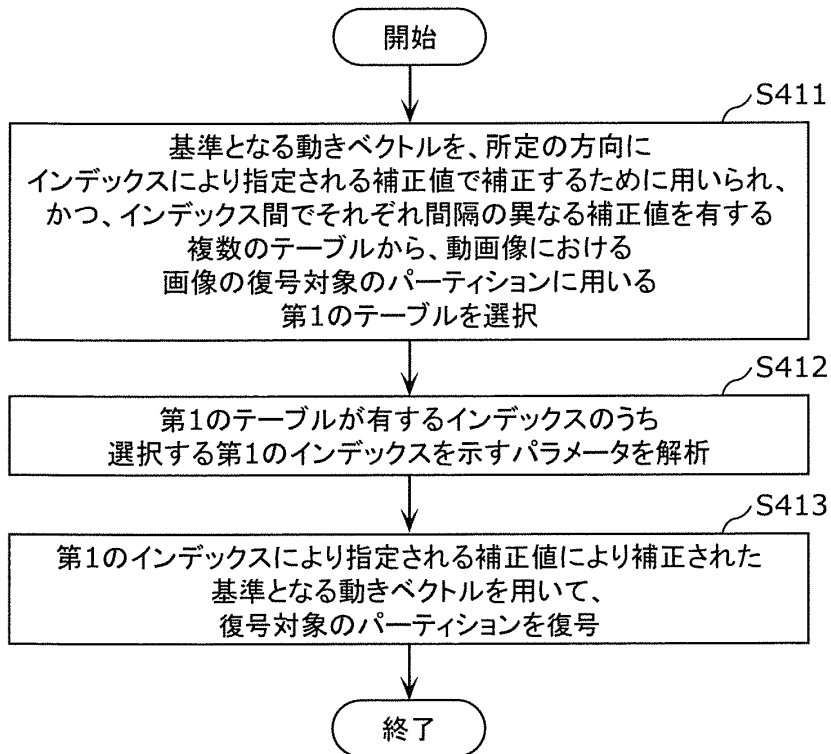
[図53]



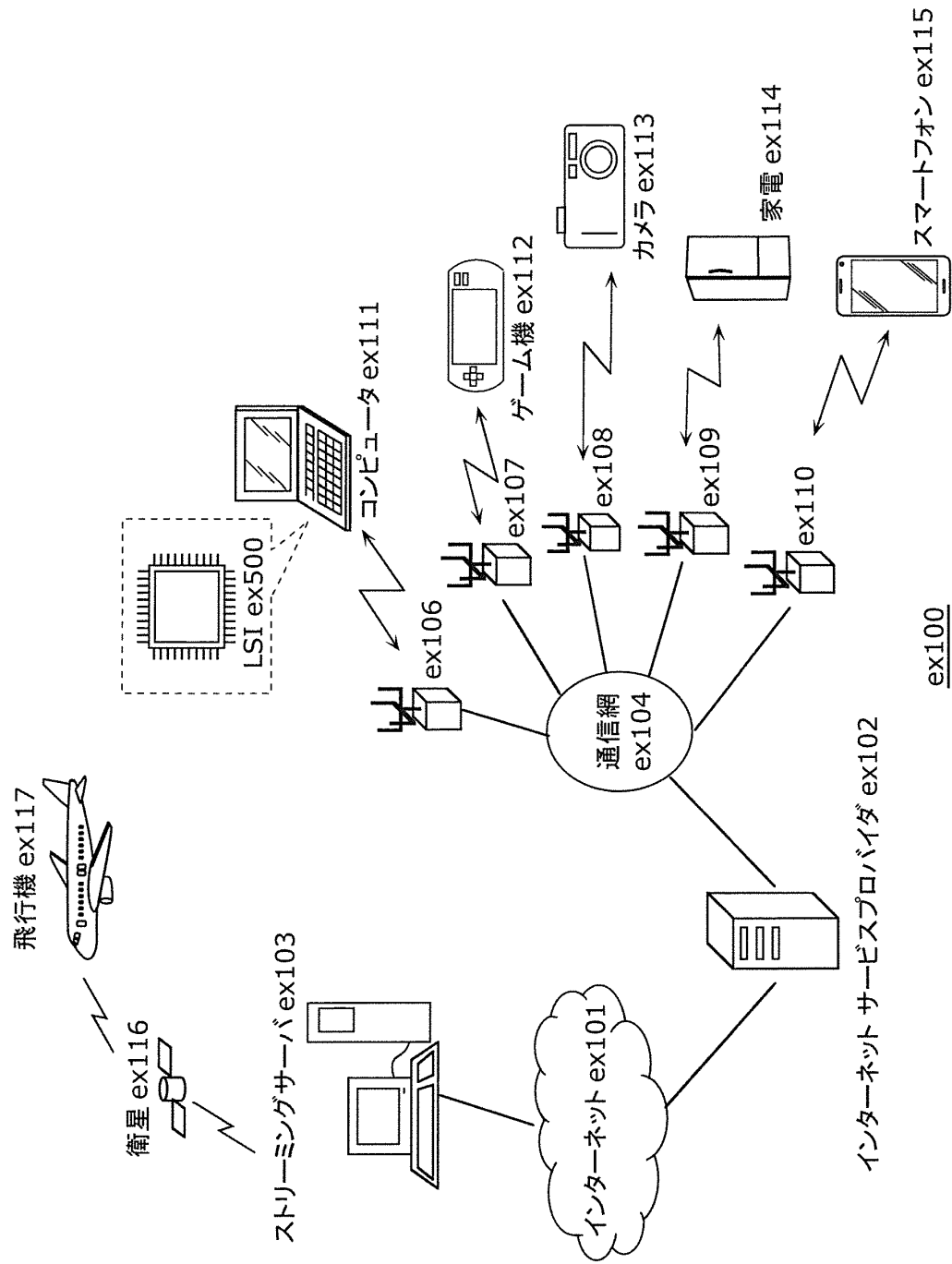
[図54]



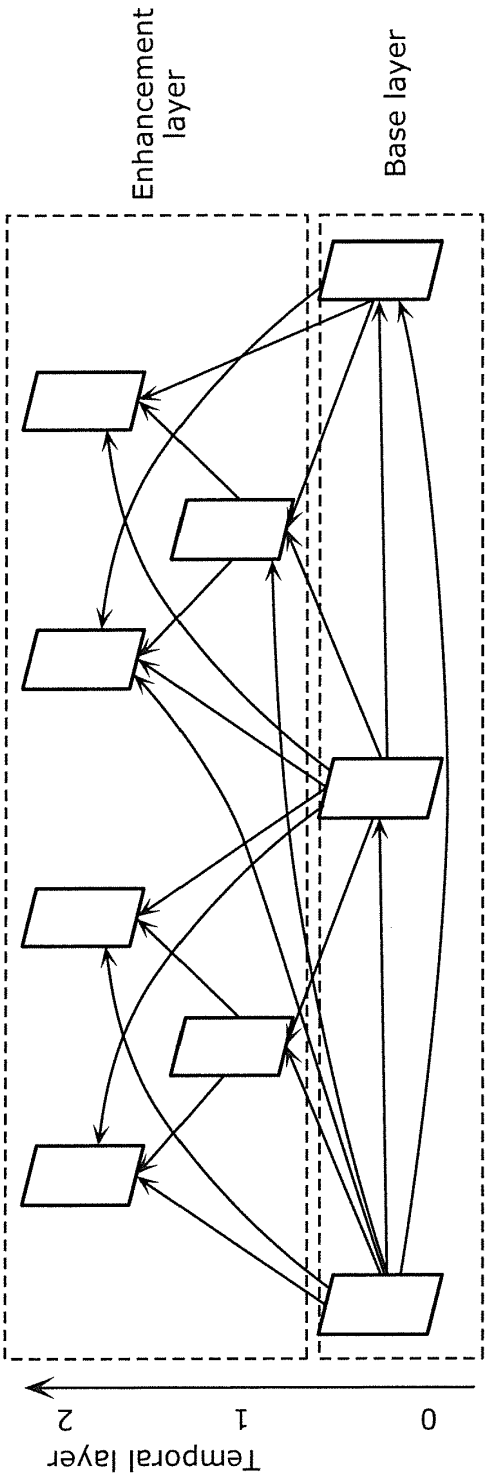
[図55]



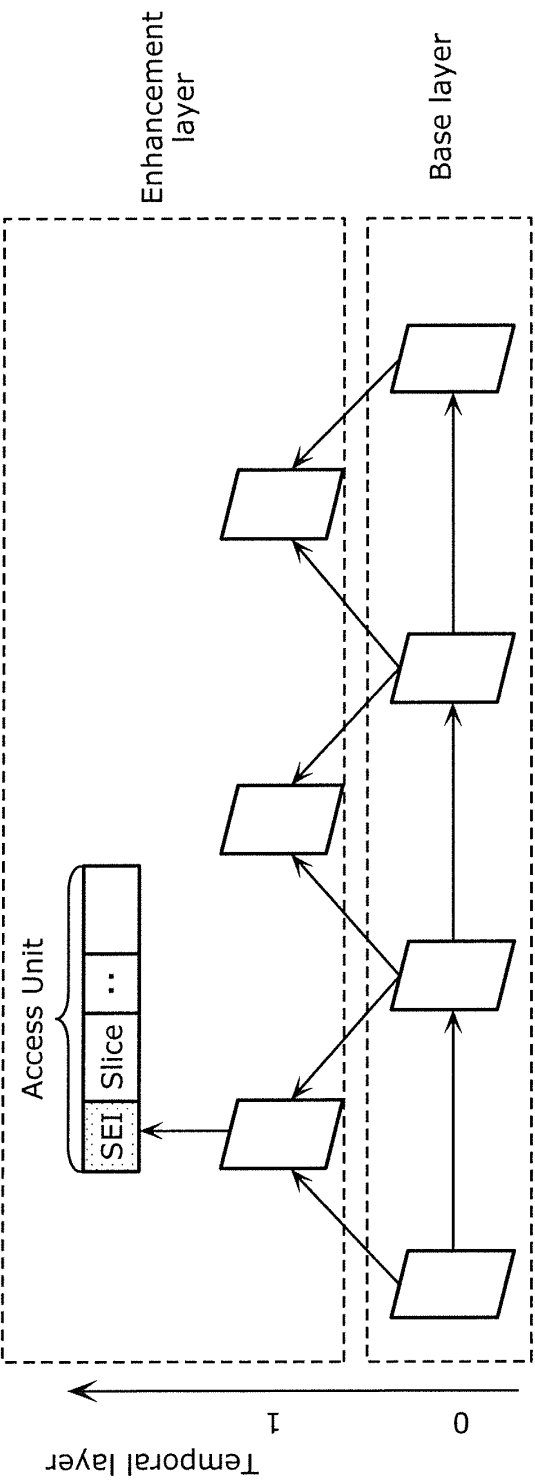
[図56]



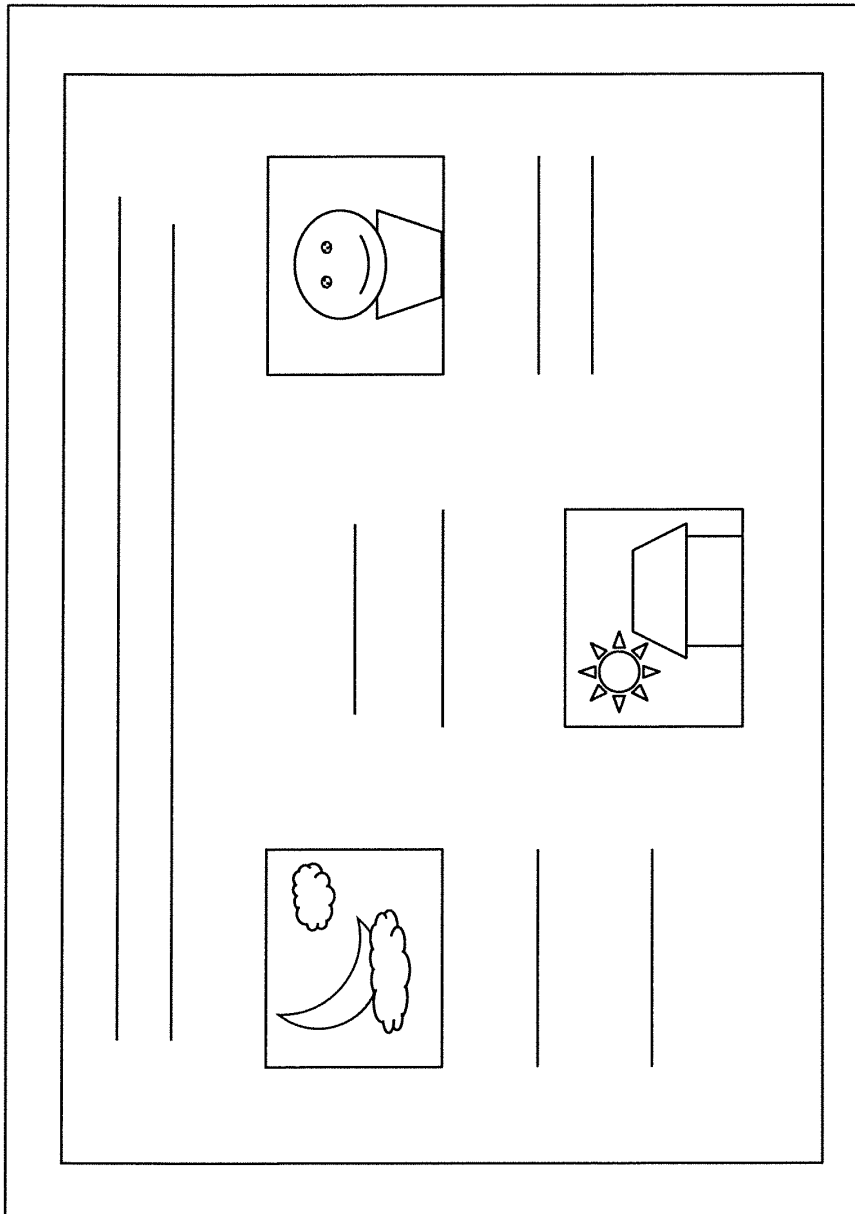
[図57]



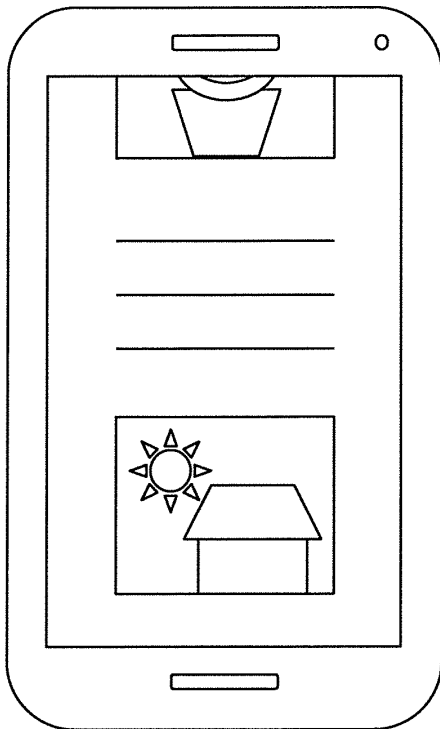
[図58]



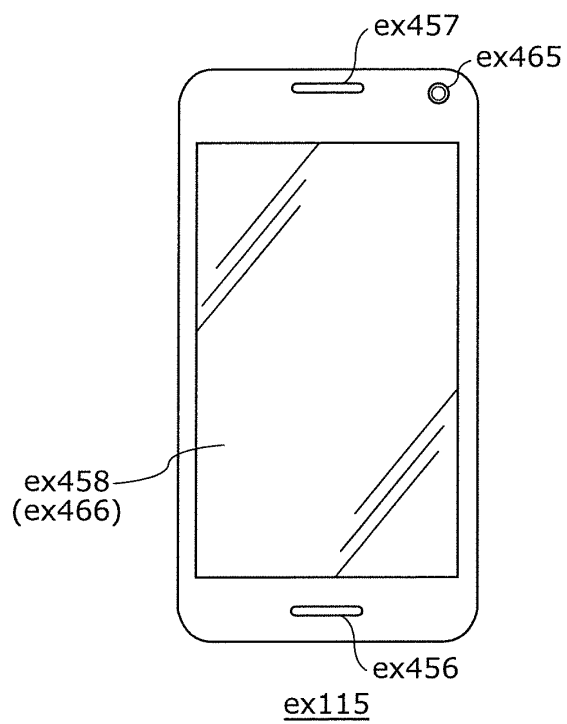
[図59]



[図60]



[図61]



[図62]

