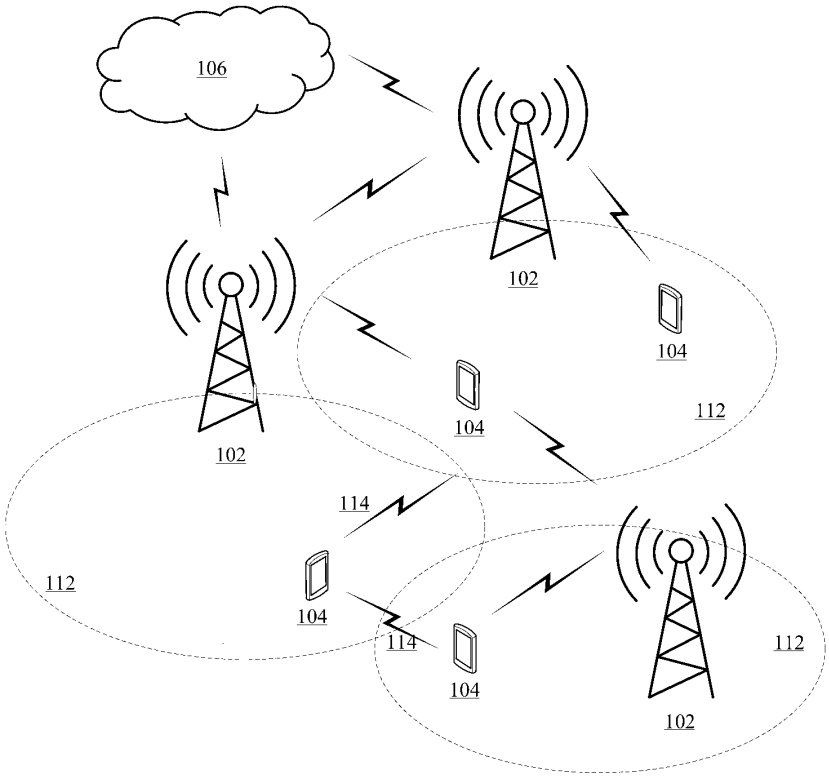


Figure 1

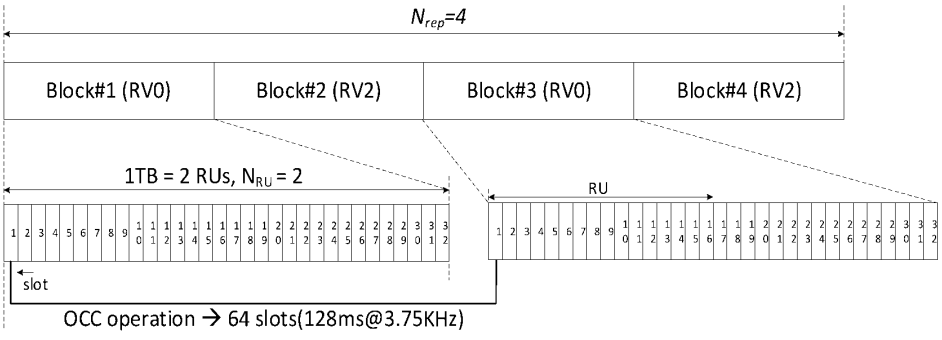


FIG. 2

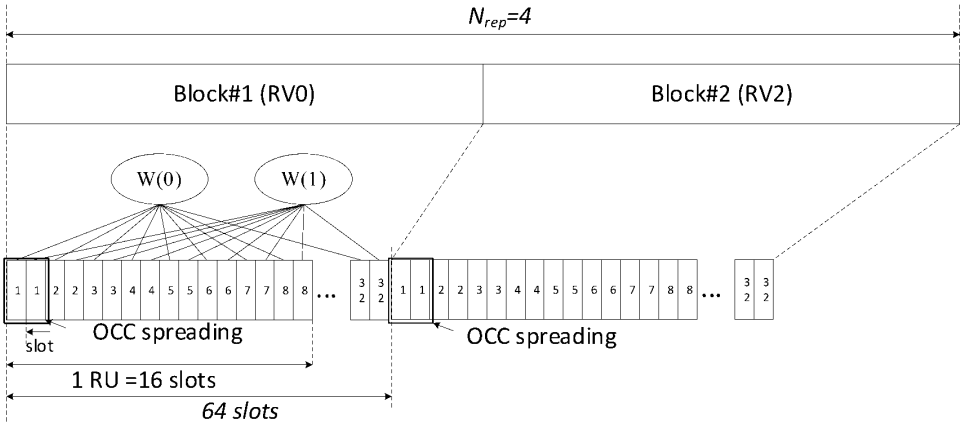

N_{rep}=4
Block#1 (RV0)
Block#2 (RV2)
W(0)
W(1)
slot
OCC spreading
OCC spreading
1 RU =16 slots
64 slots

FIG. 3

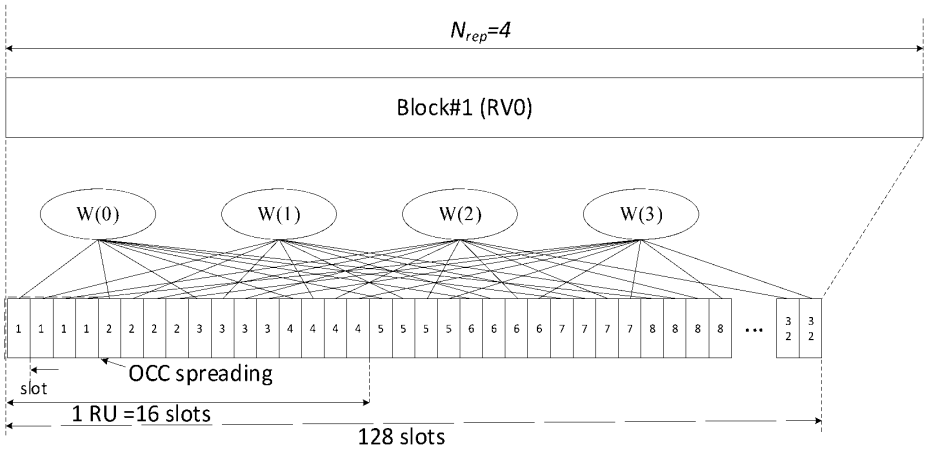

N_{rep}=4
Block#1 (RV0)
W(0)
W(1)
W(2)
W(3)
slot
OCC spreading
1 RU =16 slots
128 slots

FIG. 4

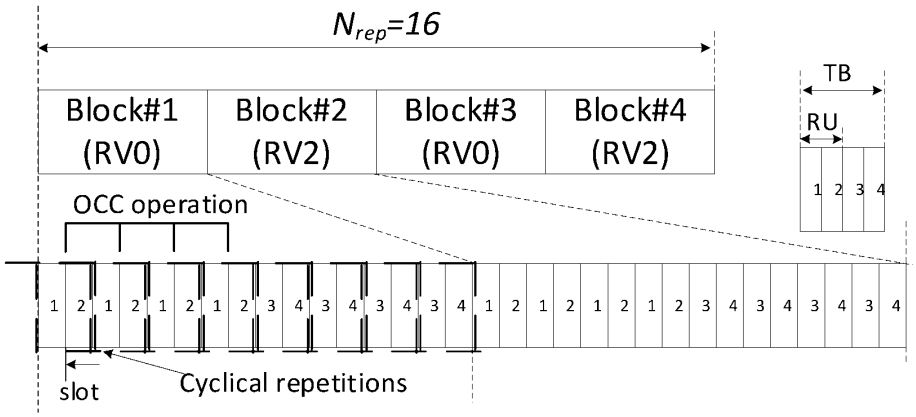


Figure 5

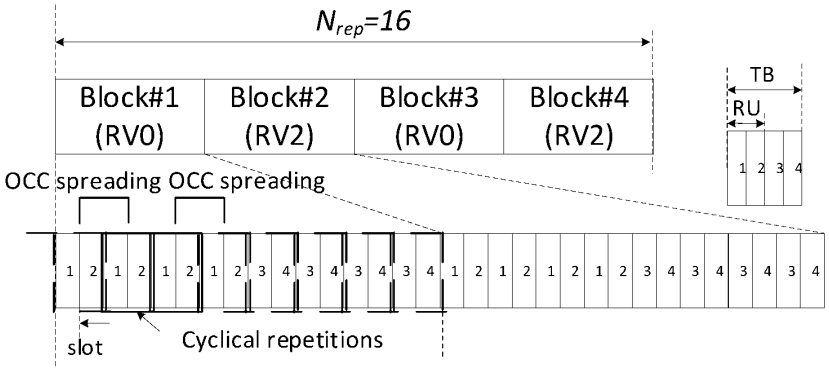


Figure 6

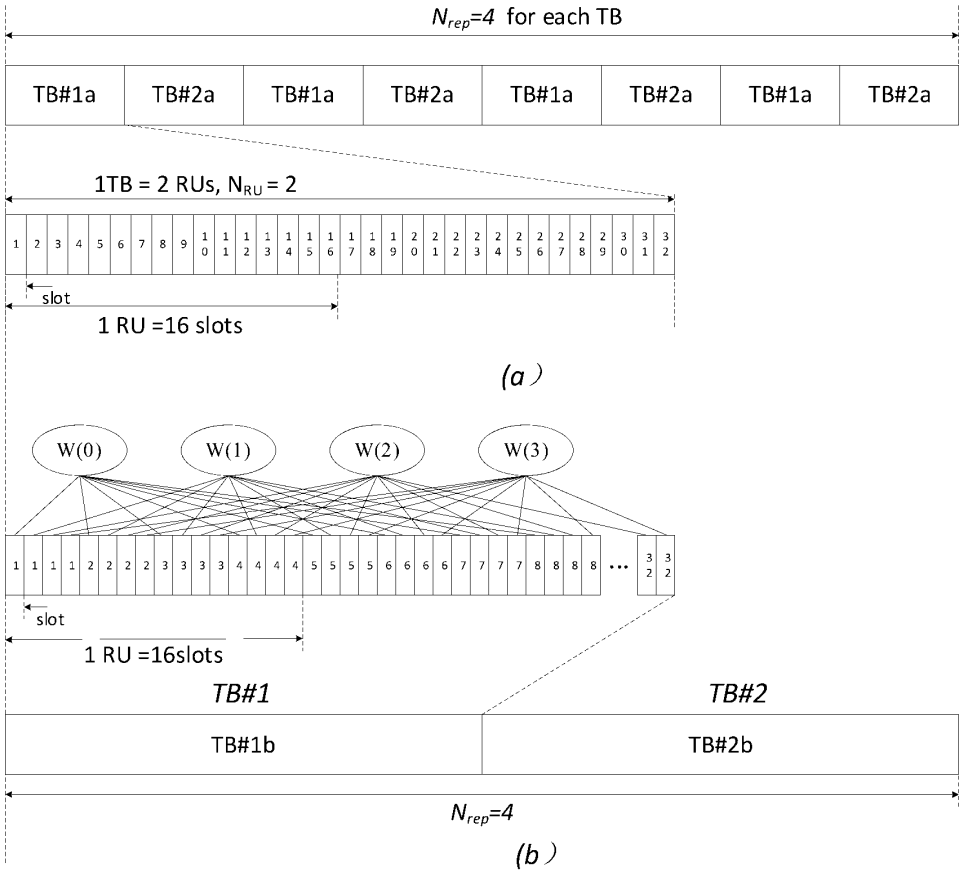

N_{rep}=4 for each TB
TB#1a
TB#2a
TB#1a
TB#2a
TB#1a
TB#2a
TB#1a
TB#2a
1TB = 2 RUs, N_{RU} = 2
slot
1 RU =16 slots
(a)
W(0)
W(1)
W(2)
W(3)
slot
1 RU =16slots
TB#1
TB#2
TB#1b
TB#2b
N_{rep}=4
(b)

Figure 7

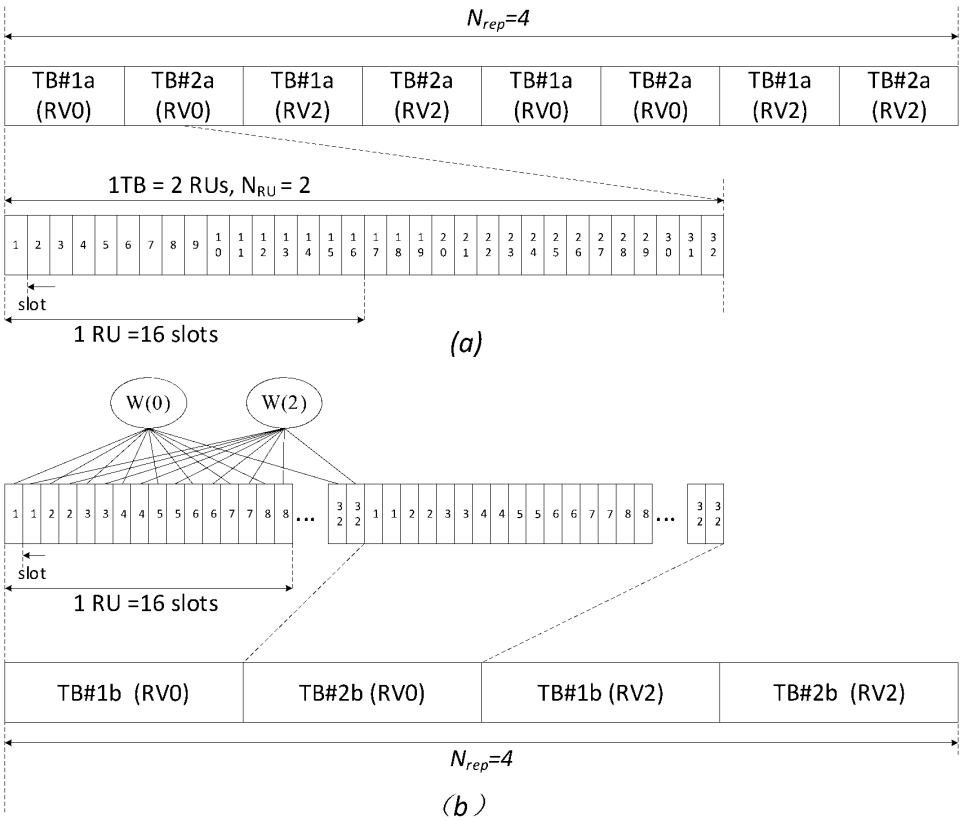

N_{rep}=4
TB#1a (RV0)
TB#2a (RV0)
TB#1a (RV2)
TB#2a (RV2)
TB#1a (RV0)
TB#2a (RV0)
TB#1a (RV2)
TB#2a (RV2)
1TB = 2 RUs, N_{RU} = 2
slot
1 RU =16 slots
(a)
W(0)
W(2)
slot
1 RU =16 slots
TB#1b (RV0)
TB#2b (RV0)
TB#1b (RV2)
TB#2b (RV2)
N_{rep}=4
(b)

Figure 8

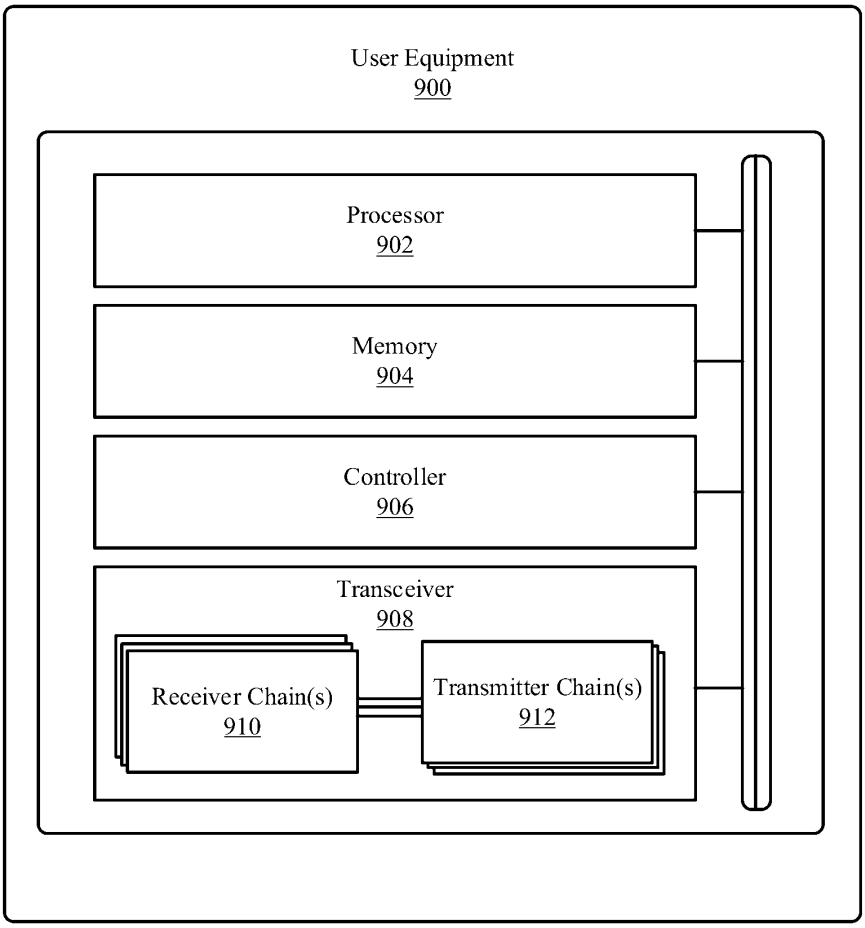

User Equipment
900
Processor
902
Memory
904
Controller
906
Transceiver
908
Receiver Chain(s)
910
Transmitter Chain(s)
912

Figure 9

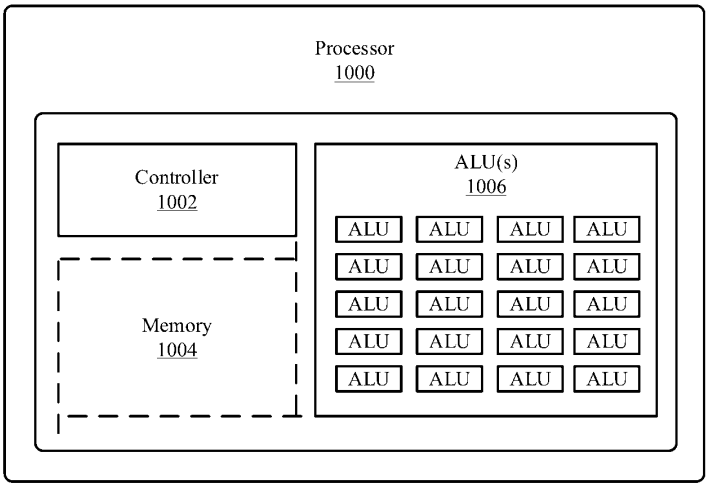


Figure 10

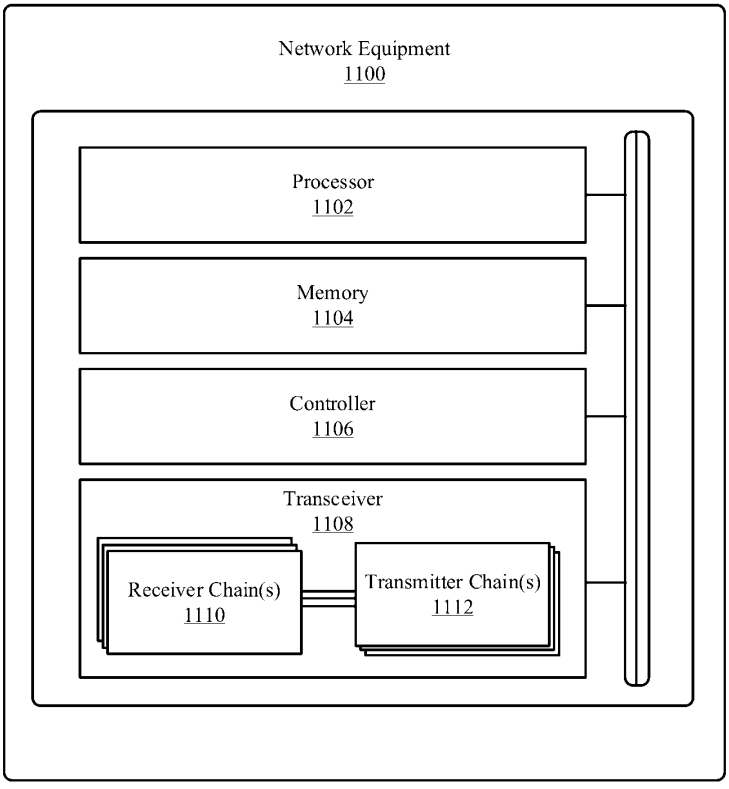


Figure 11

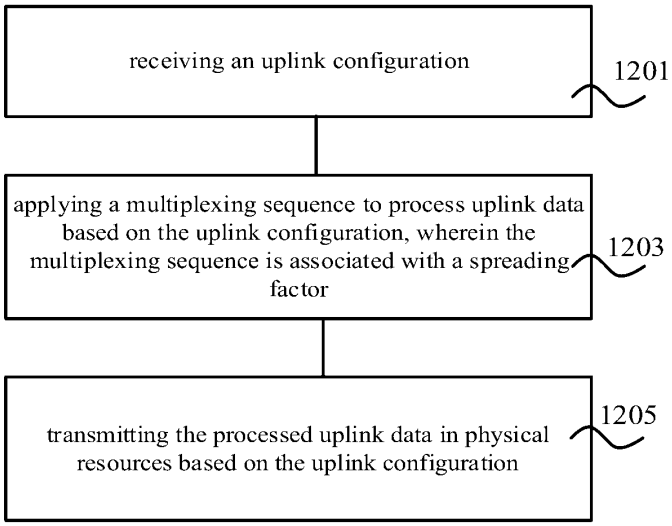


Figure 12

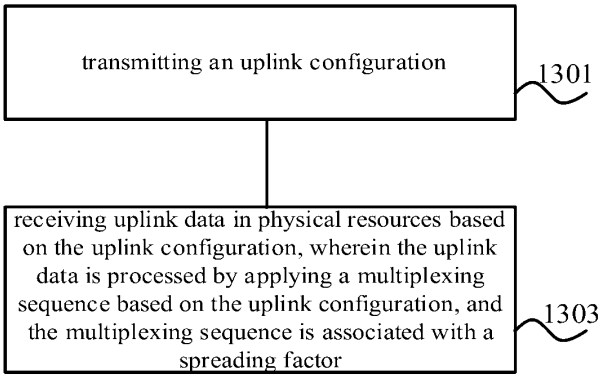


Figure 13