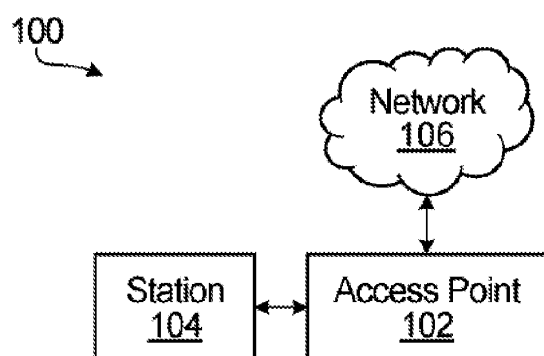
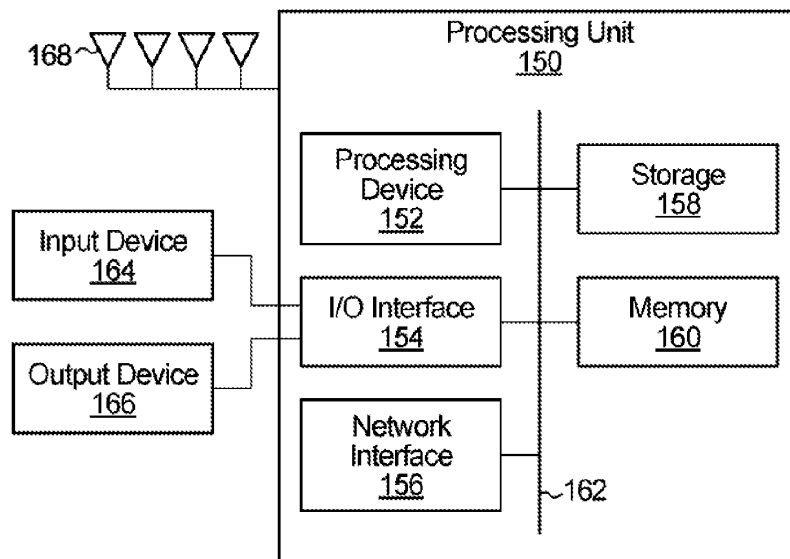


**FIG. 1A**

**FIG. 1B**

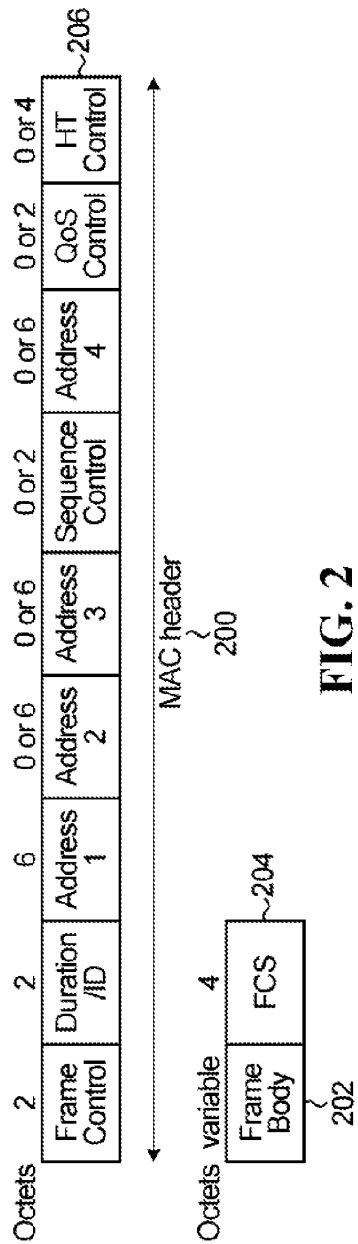


FIG. 2

Variant	B0	B1	B2-B29	B30	B31
HT	0	HT Control Middle	AC Constraint	RDG/More PPDU	
VHT	1	0	VHT Control Middle	AC Constraint	RDG/More PPDU
HE	1	1	A-Control		

FIG. 3A

Bits:	B0	B3
Control ID	4	Variable
Control Information		

FIG. 3B

400 }		402 }		404 }		406 }	
Control ID value	Meaning	Length of the Control Information subfield (bits)	Content of the Control Information subfield				
0	Triggered response scheduling (TRS)	26	TRS Control				
1	Operating mode (OM)	12	OM Control				
2	HE link adaptation (HLA)	26	HLA Control				
3	Buffer status report (BSR)	26	BSR Control				
4	UL power headroom (UPH)	8	UPH Control				
5	Bandwidth query report (BQR)	10	BQR Control				
6	Command and status (CAS)	8	CAS Control				
7	<u>EHT operating mode (EHT OM)</u>	<u>6</u>	<u>EHT OM Control</u>				
8	<u>Single response scheduling (SRS)</u>	<u>10</u>	<u>SRS Control</u>				
10	AP assistance request (AAR)	20	AAR Control				
<u>9, 11-14</u> <u>7-14</u>	Reserved						
15	Ones need expansion surely (ONES)	26	Set to all 1s				

FIG. 4

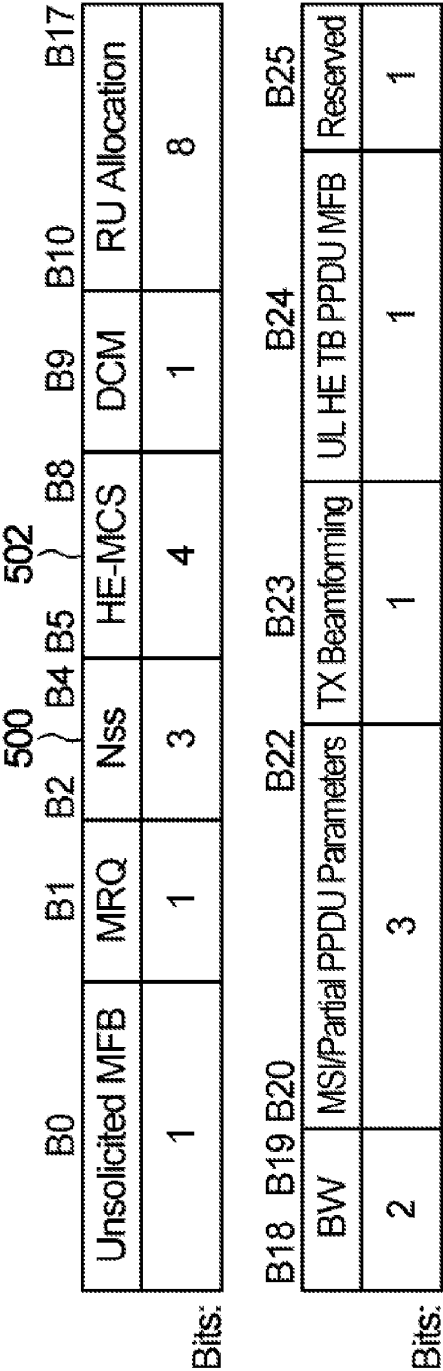


FIG. 5

B0		B1	B2	B5	B6	B9	B10	B18
Unsolicited MFB		MRQ	Nss for SU MIMO		EHT-MCS for SU MIMO		RU Allocation	
1		1	4		4		9	

Bits:

B19		B21	B22	B23		B24	B25
BW		MSI/Partial PPDU Parameters		TX Beamforming		UL EHT TB PPDU MFB	
3		2		1		1	

Bits:

FIG. 6

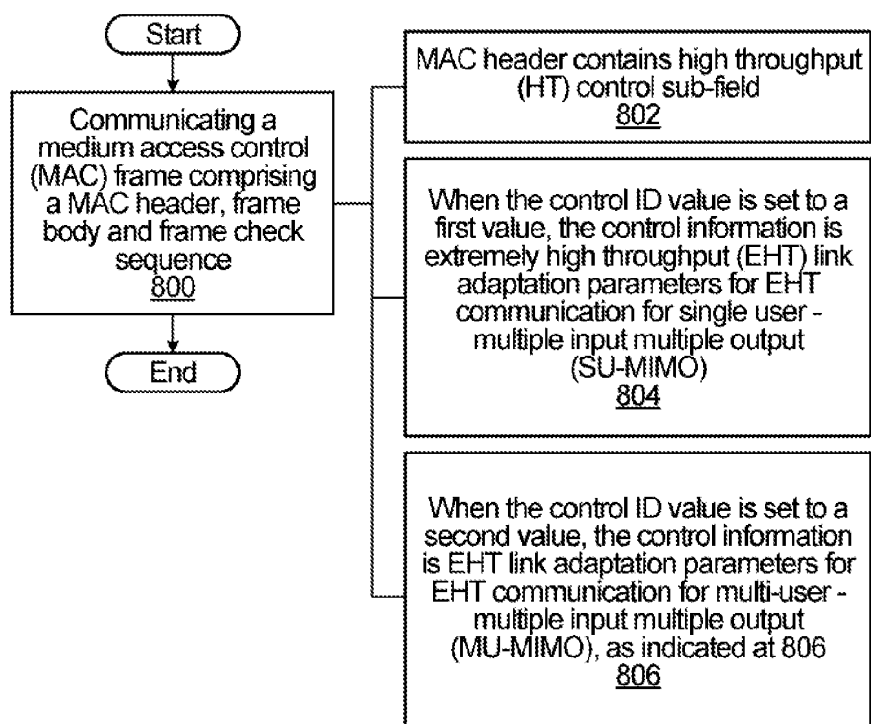
B0		B1	B2	B3	B4	B5	B6	B9	B10	B18
Unsolicited MFB		MRQ	Nss for MU MIMO		Reserved		EHT-MCS for MU MIMO		RU Allocation	
1		1	2		2		4		9	

Bits:

B19		B21	B22	B23		B24	B25
BW		MSI/Partial PPDU Parameters		TX Beamforming		UL EHT TB PPDU MFB	
3		2		1		1	

Bits:

FIG. 7

**FIG. 8**

B0		B1	B2	B5	B6	B9	B10	B18
Unsolicited MFB		MRQ	NSS	EHT-MCS		RU Allocation/(Partial RU Allocation, SUMU-MIMO)		
1		1	4	4		9		

Bits:

B19	B21	B22	B23		B24	B25	
BW	MSI/(PPDU Format, Coding Type)/(PPDU Format, SUMU-MIMO)					TX Beamforming	ULE HT TB PPDU MFB
3			2	1		1	

Bits:

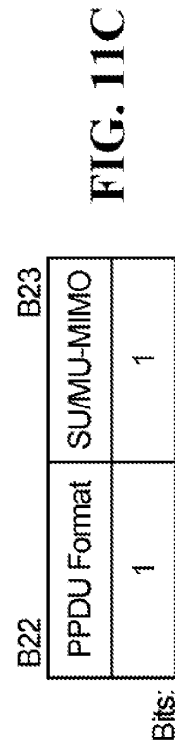
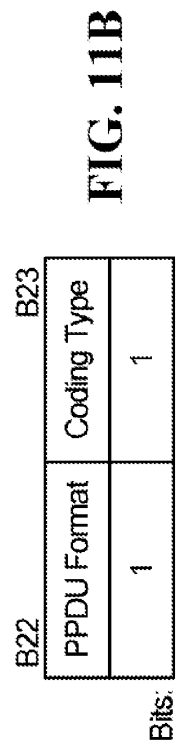
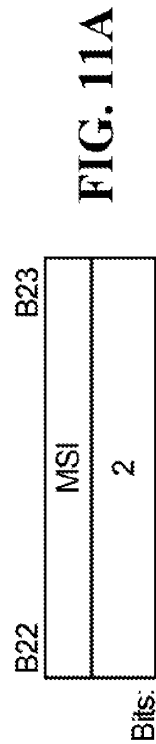
FIG. 9

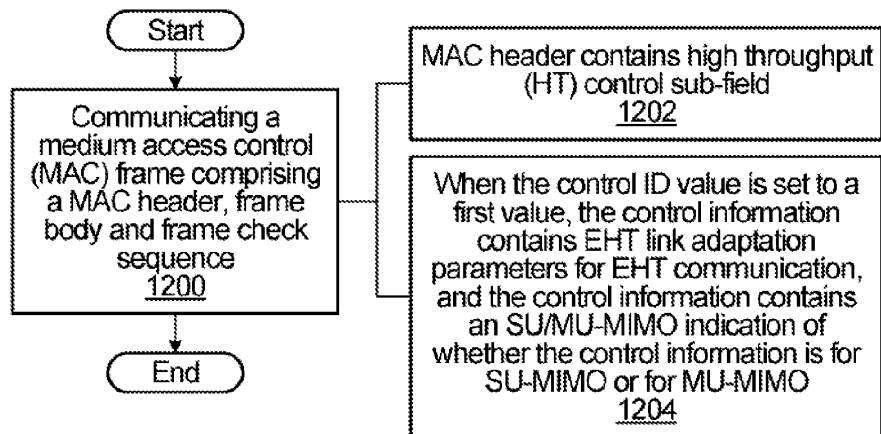
B10	B17	B18
Partial RU Allocation		SU/MU-MIMO
8		1
Bits:		

FIG. 10A

B10	B18
RU Allocation	
9	
Bits:	

FIG. 10B



**FIG. 12**