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SYSTEM AND METHOD FOR MANAGING MEMORY ERRORS IN AN INFORMATION
HANDLING SYSTEM

ABSTRACT OF THE DISCLOSURE

5 A method for handling memory defects during the POST phase and memory calibration in single processor and multiprocessor information handling systems is disclosed whereby information regarding the location of a known memory defect is utilized to optimize the performance of an information handling system. Memory defects within system memory are identified and replaced during operation with error free memory space.

FIG. 5