

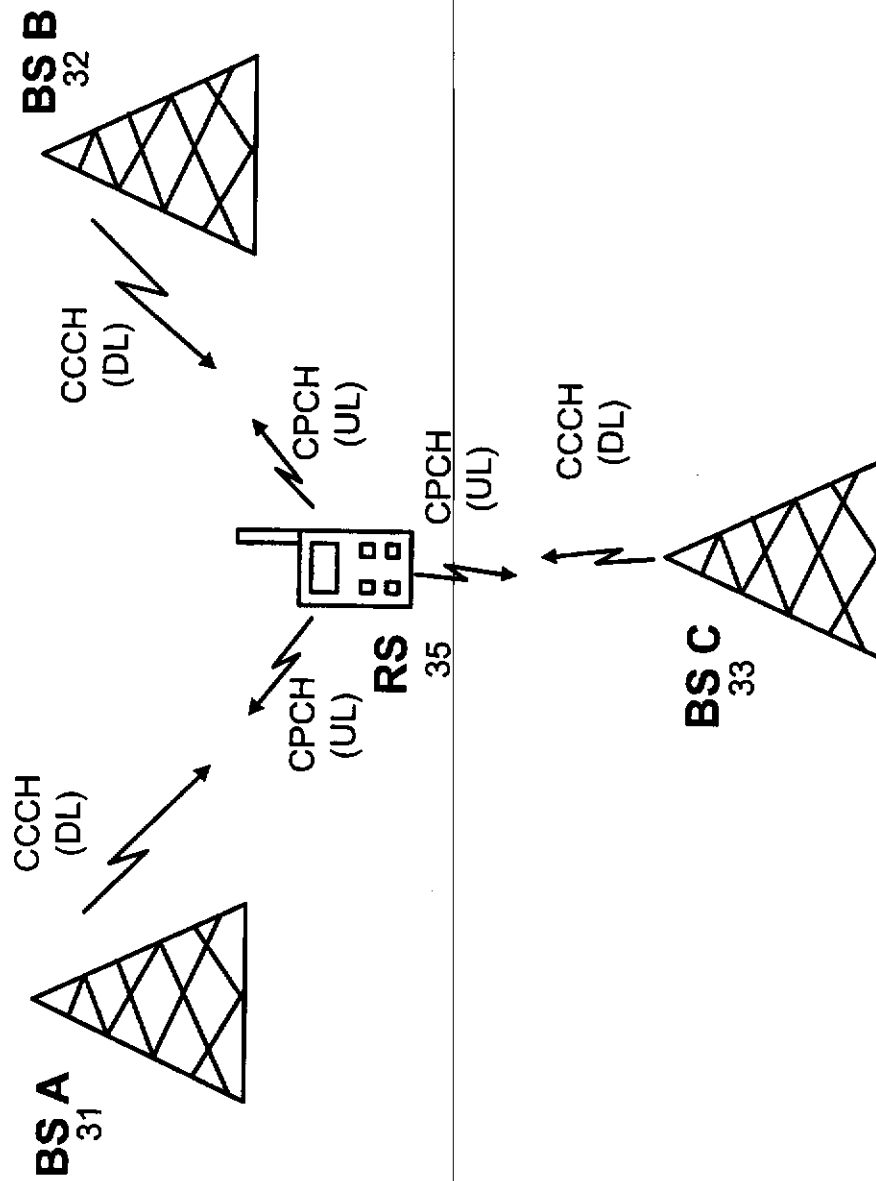


**FIG 1. Common Packet Channel System Block Diagram
with a Common Control Downlink Channel**

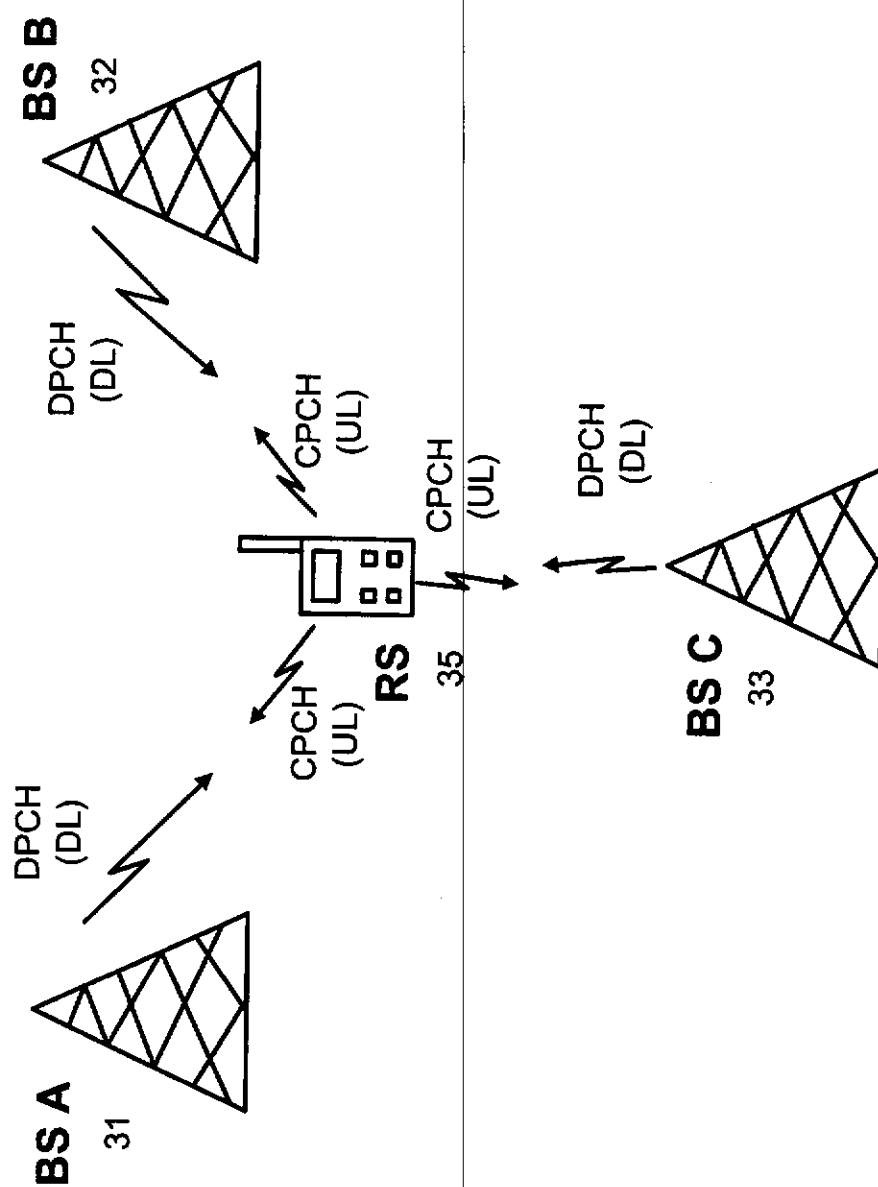
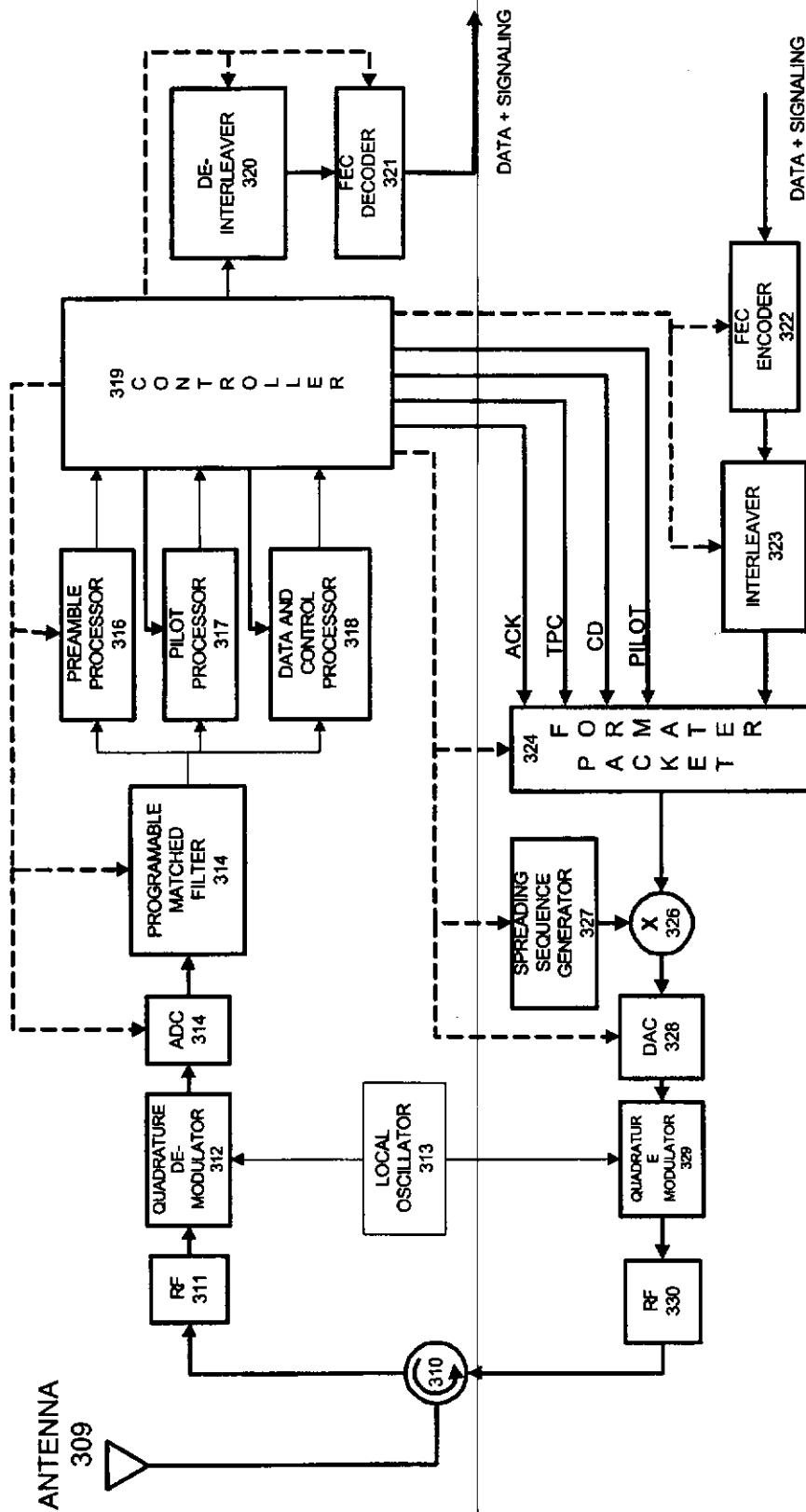


**FIG 2 Common Packet Channel System Block Diagram
with a Dedicated Downlink Channel**



DATA

CONTROL

FIG 3 BASE STATION RECEIVER FOR COMMON PACKET CHANNEL

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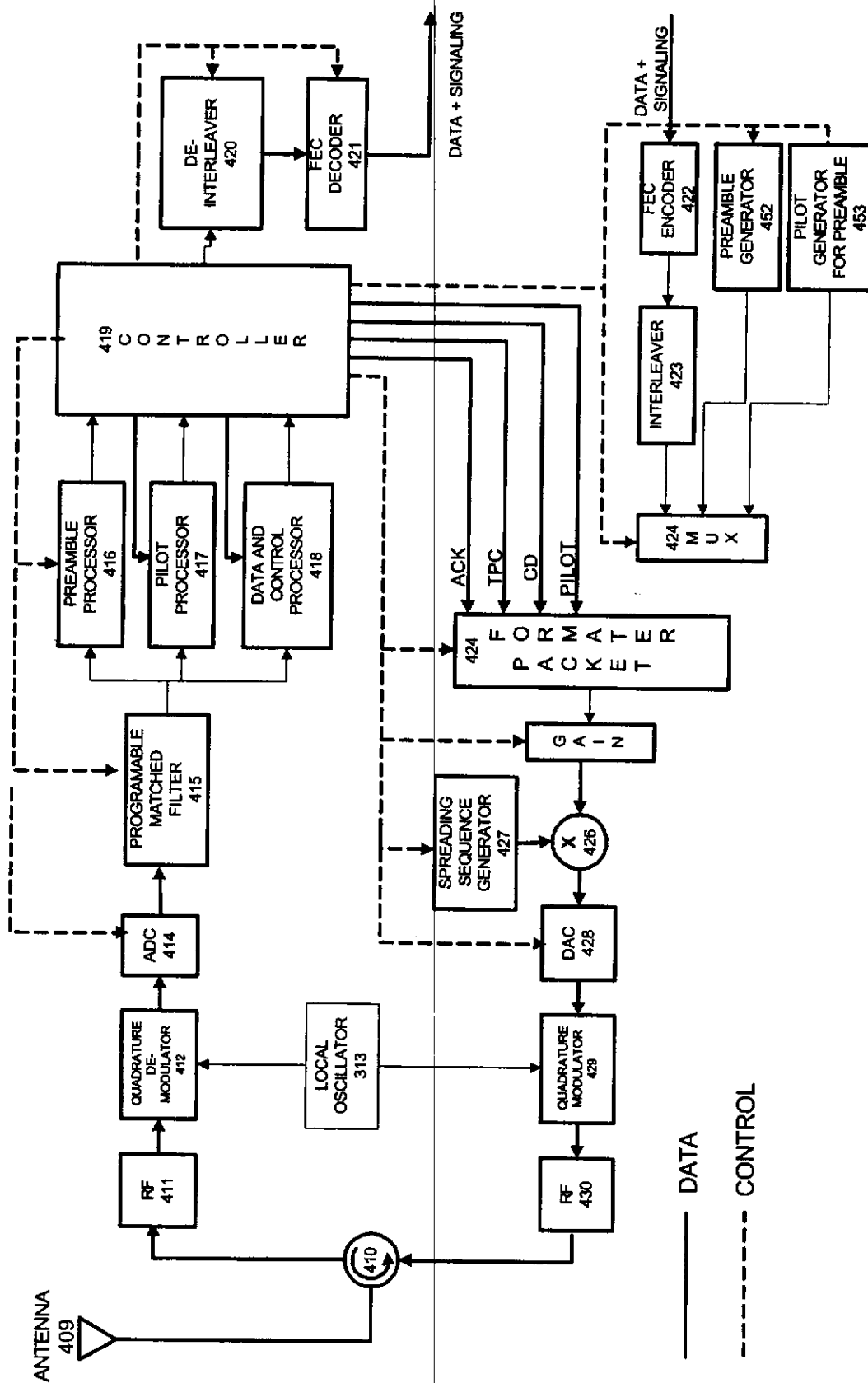


FIG 4 REMOTE STATION RECEIVER FOR COMMON PACKET CHANNEL

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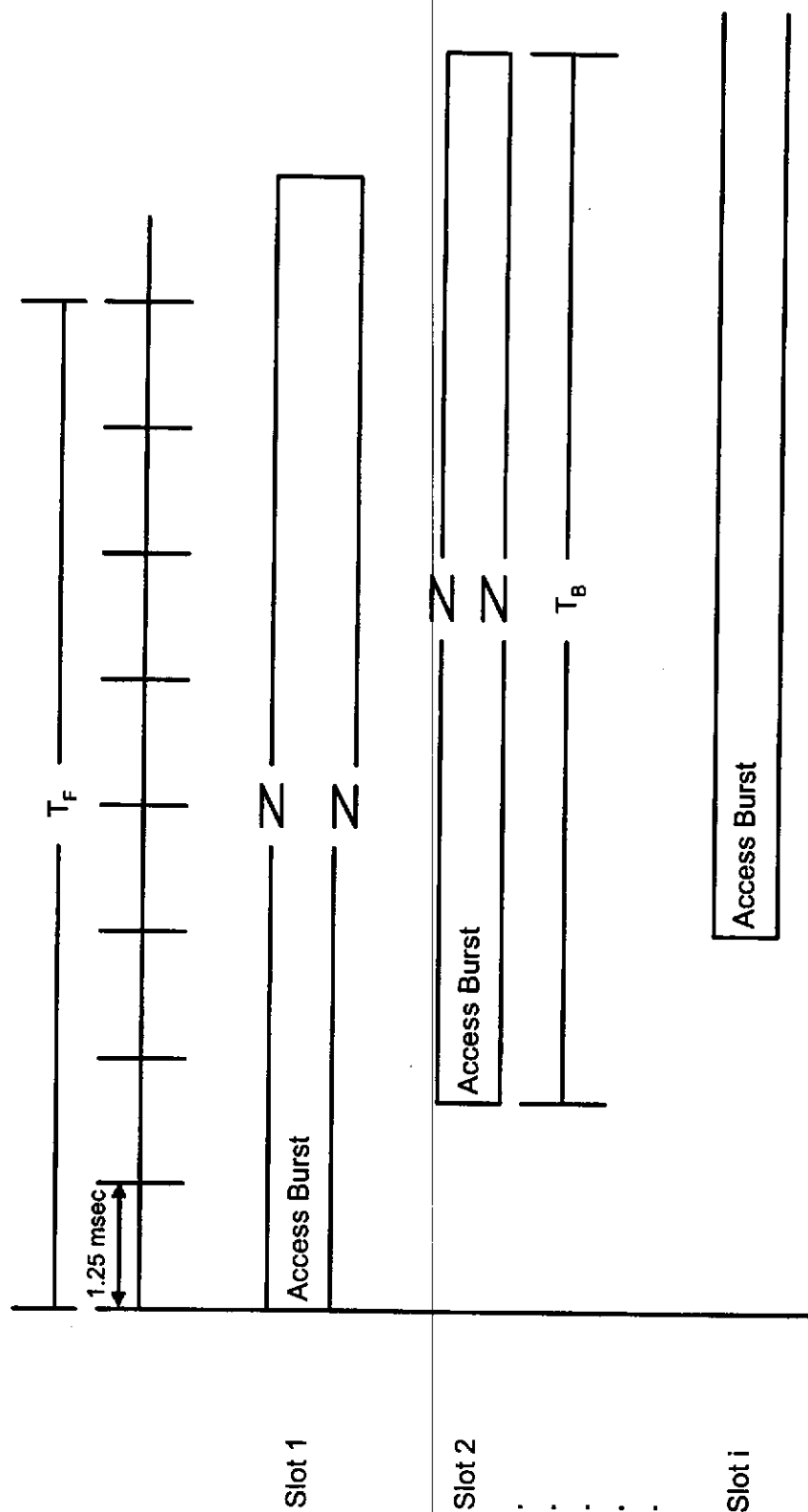


FIG 5 Timing Diagram for access burst transmission

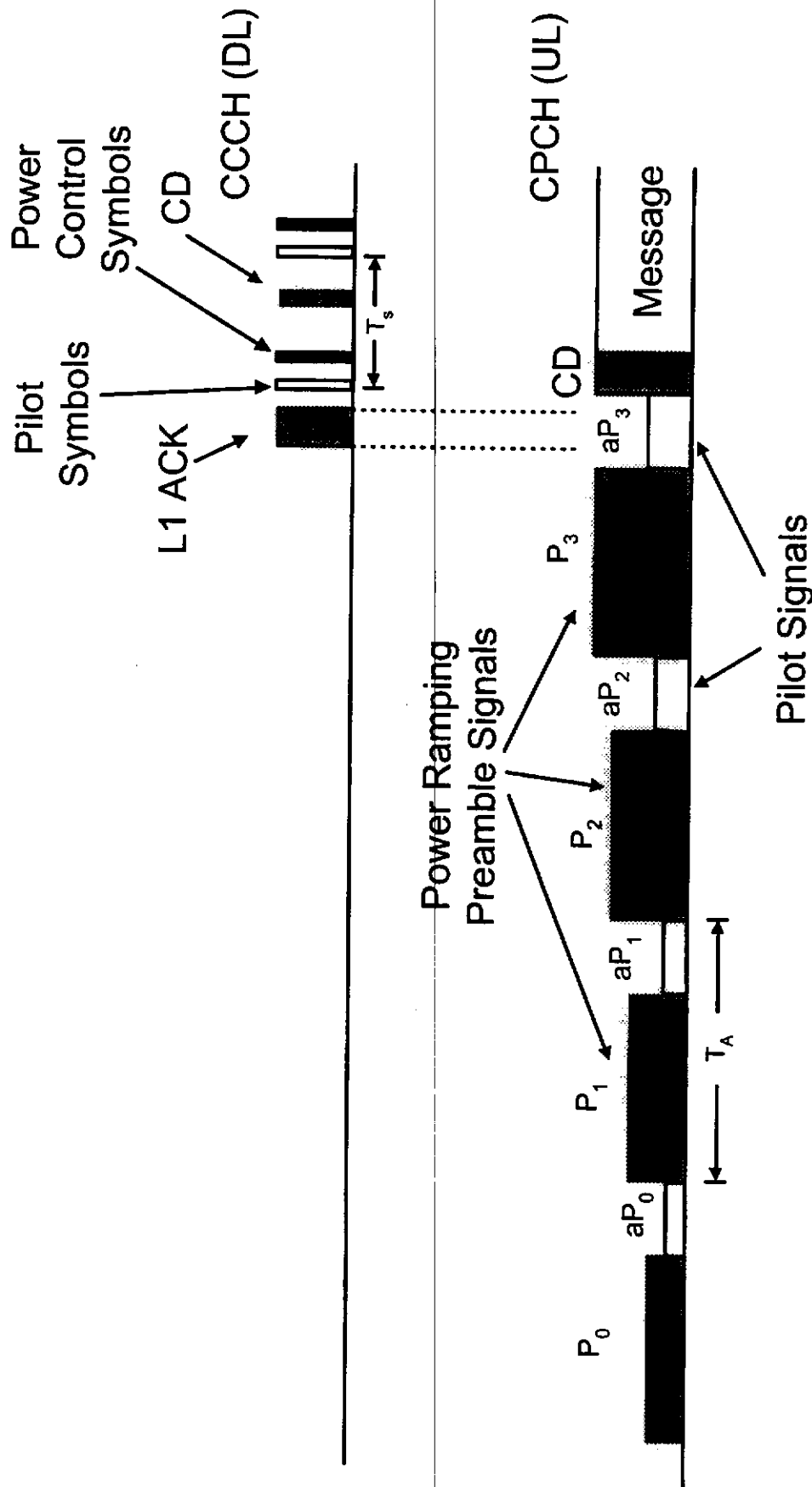


**FIG 6 Common Packet Channel Access Burst Format
with A Common Control Channel Downlink**

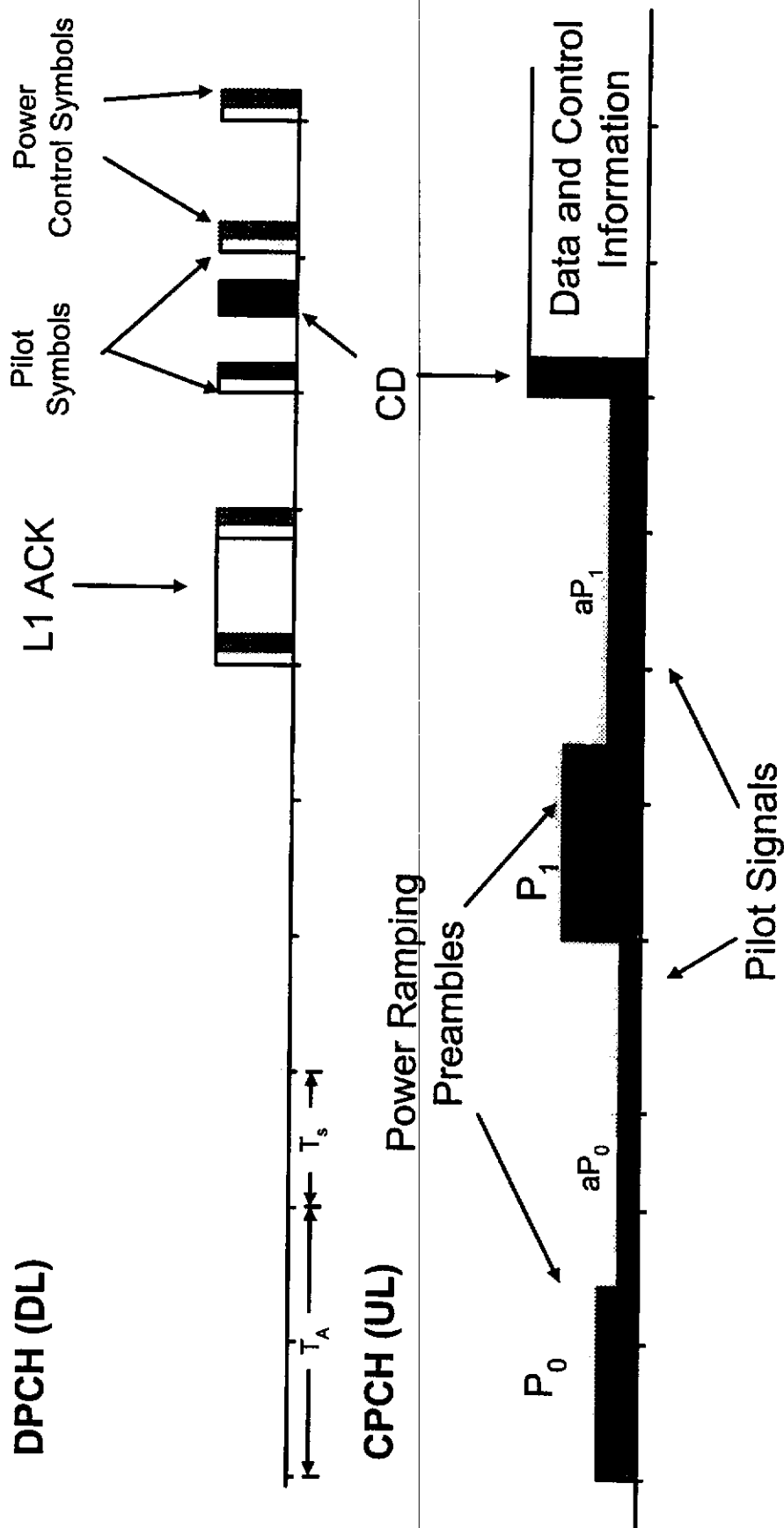


FIG 7. Common Packet Channel (CPCH) Access Burst Format (UL). Associated Dedicated Physical Channel (DPCH) (DL).

$g_{k,0}A$	$g_{k,1}A$	$g_{k,2}A$	-----	$g_{k,N-1}A$
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(A)

$g_{k,0}A_{k,i0}$	$g_{k,1}A_{k,i1}$	$g_{k,2}A_{k,i2}$	-----	$g_{k,N-1}A_{k,i(N-1)}$
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(B)

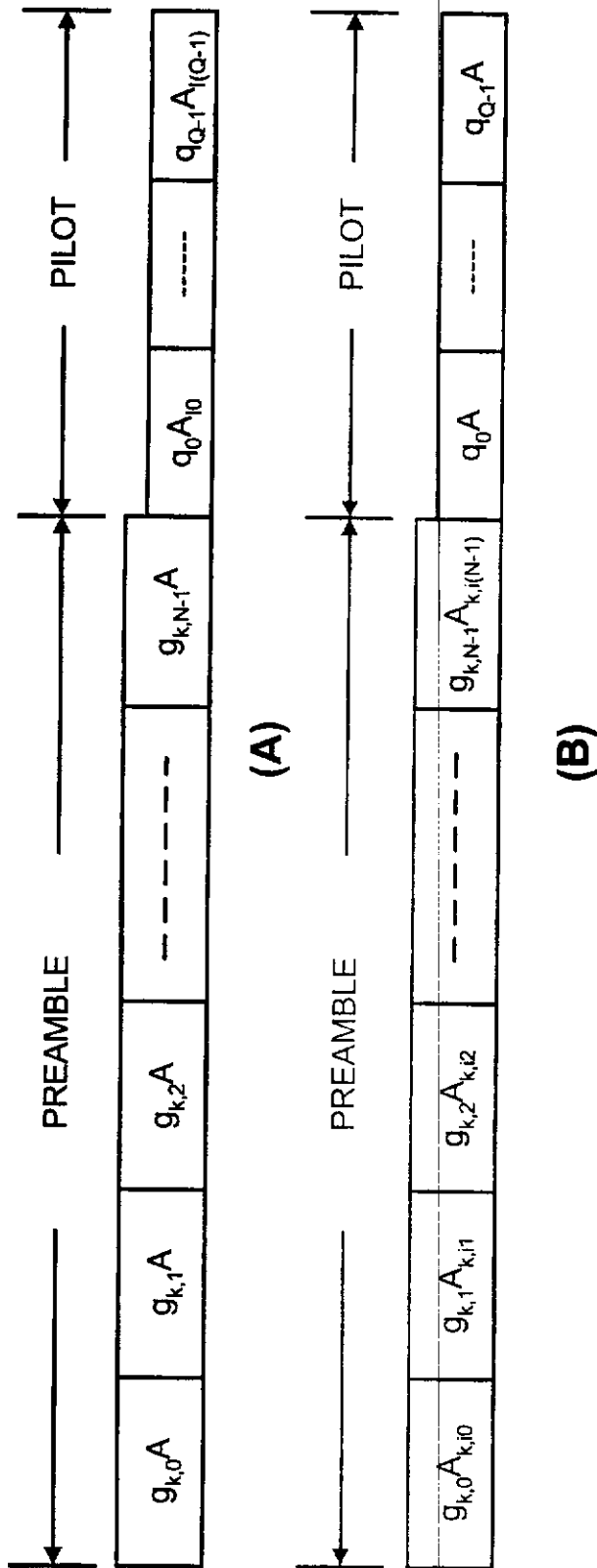
$$A_{k,ij} \in [A_0, A_1, A_2, \dots, A_{N-1}]$$

$$A_{k1,ij} \neq A_{k2,ij}$$

FIG 8 Preamble Format

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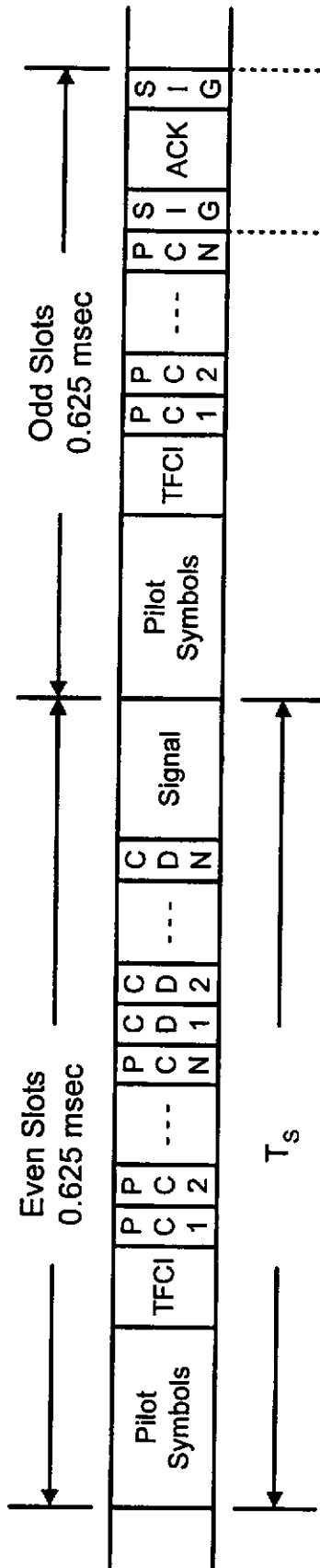


$$A_{k,ij} \in [A_0, A_1, A_2, \dots, A_{N-1}]$$

$$A_{k1,ij} \neq A_{k2,ij}$$

FIG 9 Preamble and Pilot Format

CCCH (DH)



CPCH (UL)

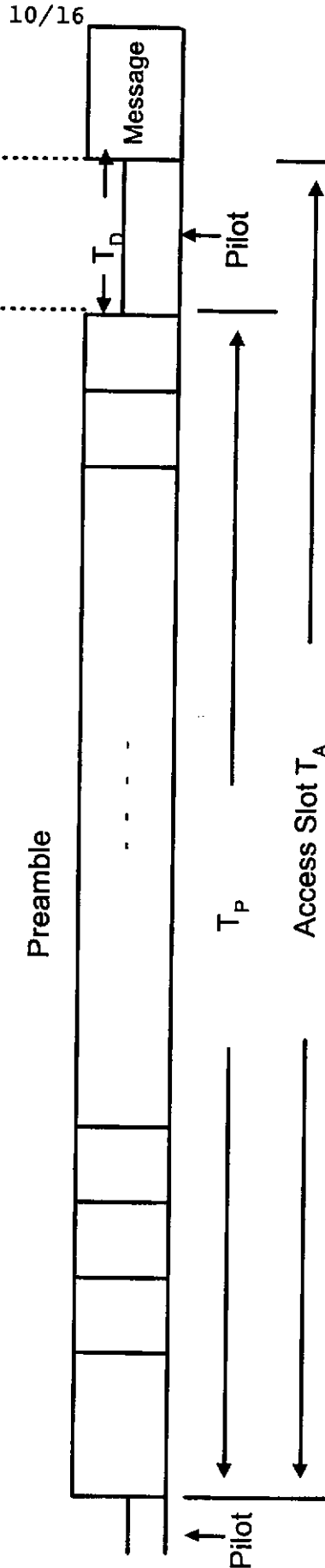


FIG 10 Common Packet Chnnel Timing Diagram and Frame Format of the Downlink Common Control Link

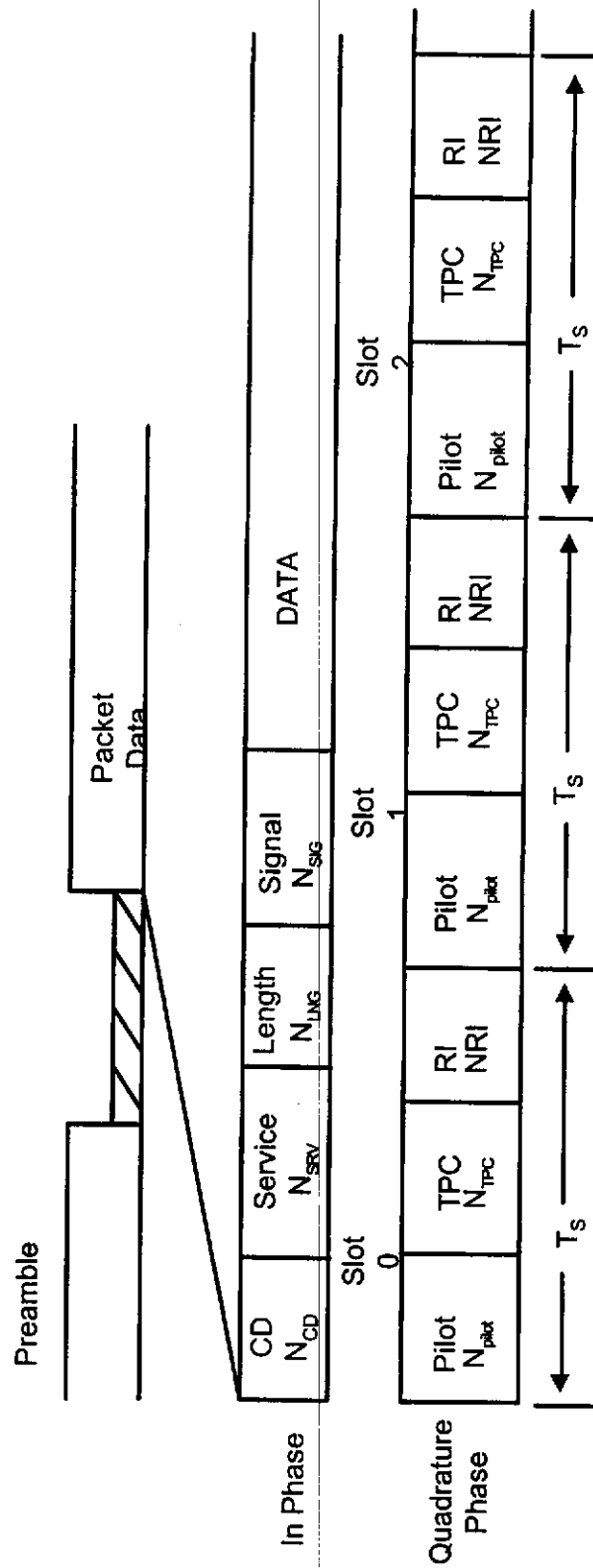


FIG 11 Frame Format of Common Packet Channel and Packet Data

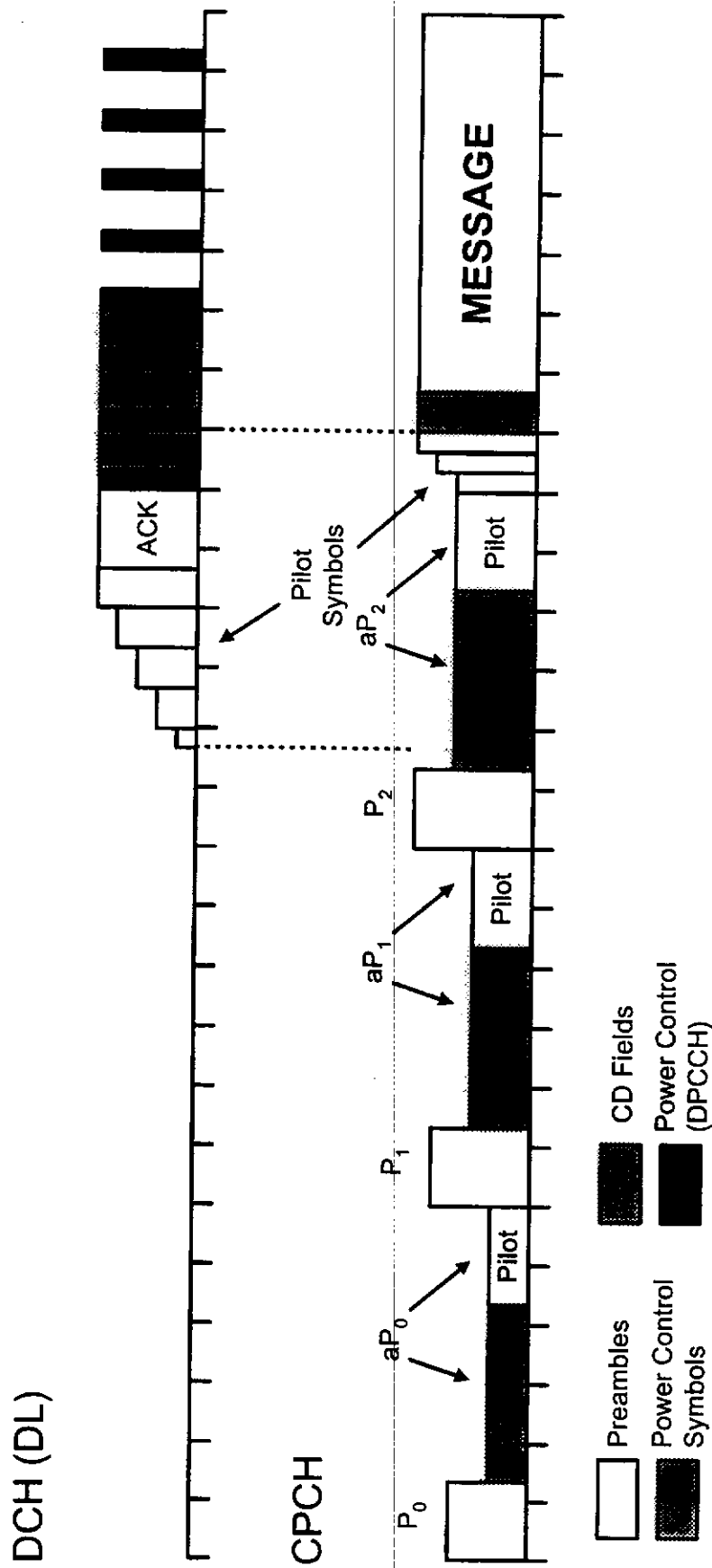


Fig 12 Common Packet Channel Timing Diagram for Mutual pre-data transmission power control

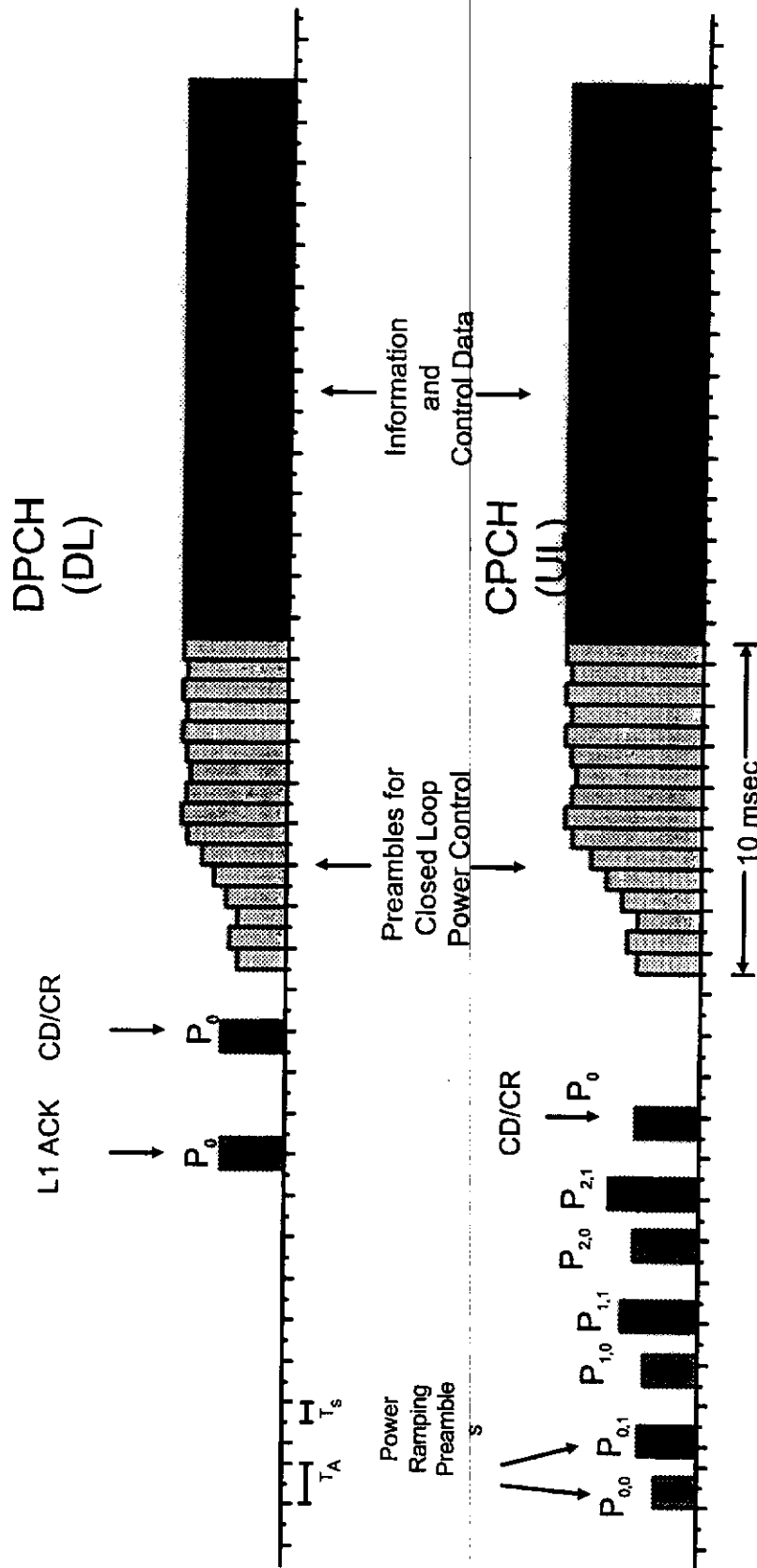


FIG 13. Common Packet Channel (CPCH) Timing Diagram with its Associated Downlink Dedicated Physical Channel.

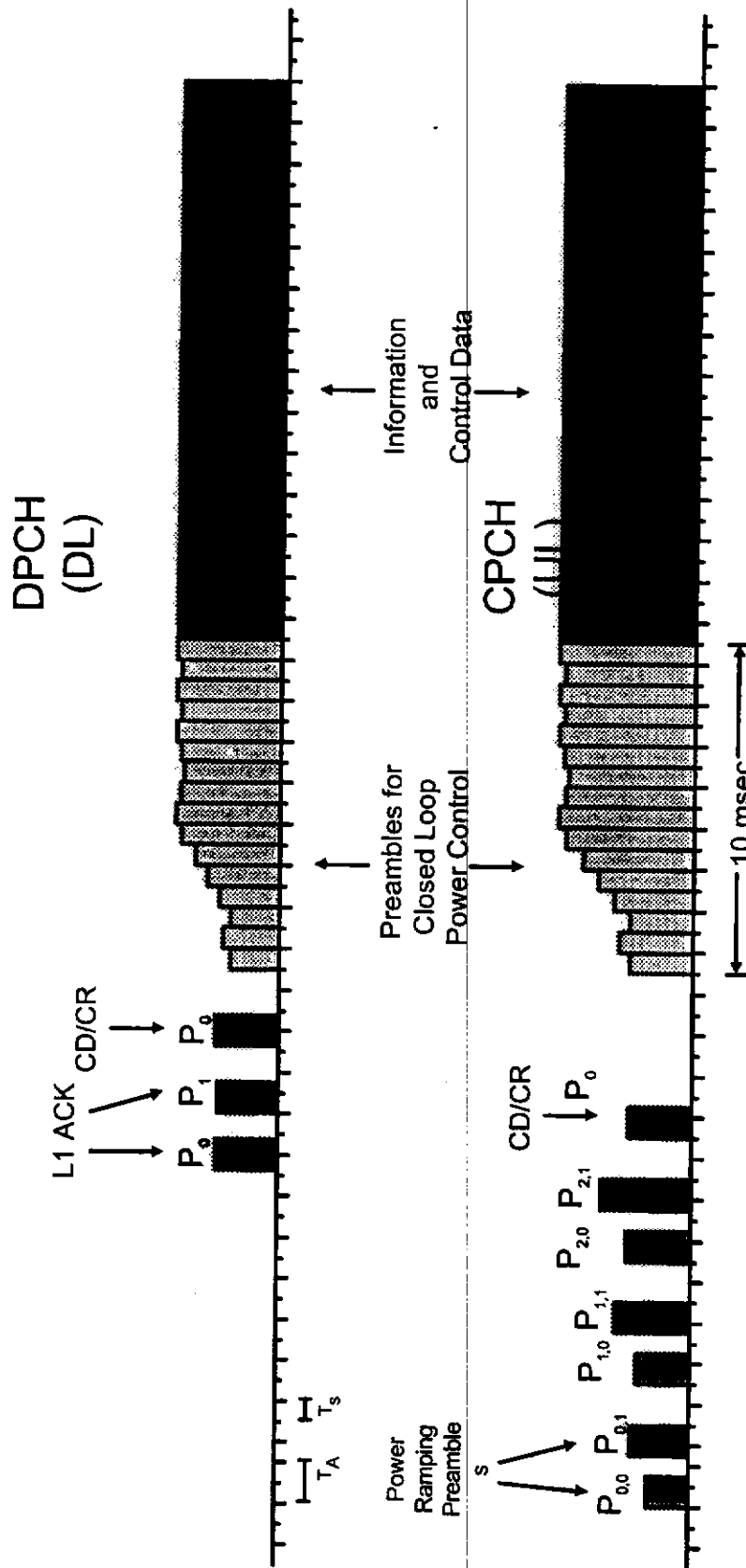


FIG 14 Common Packet Channel (CPCH) Timing Diagram with its Associated Downlink Dedicated Physical Channel.

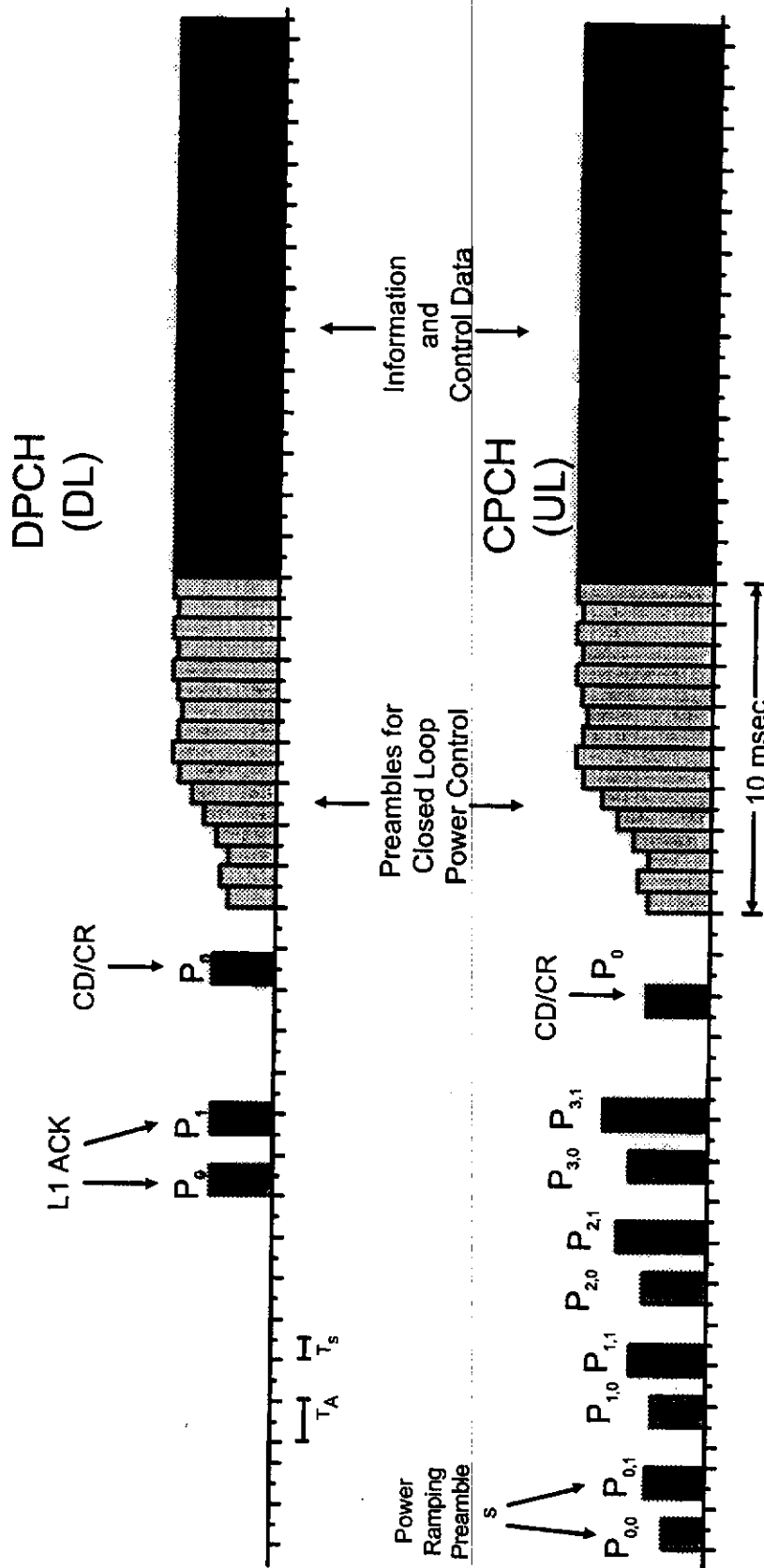


FIG 15 Common Packet Channel (CPCH) Timing Diagram with its Associated Downlink Dedicated Physical Channel.

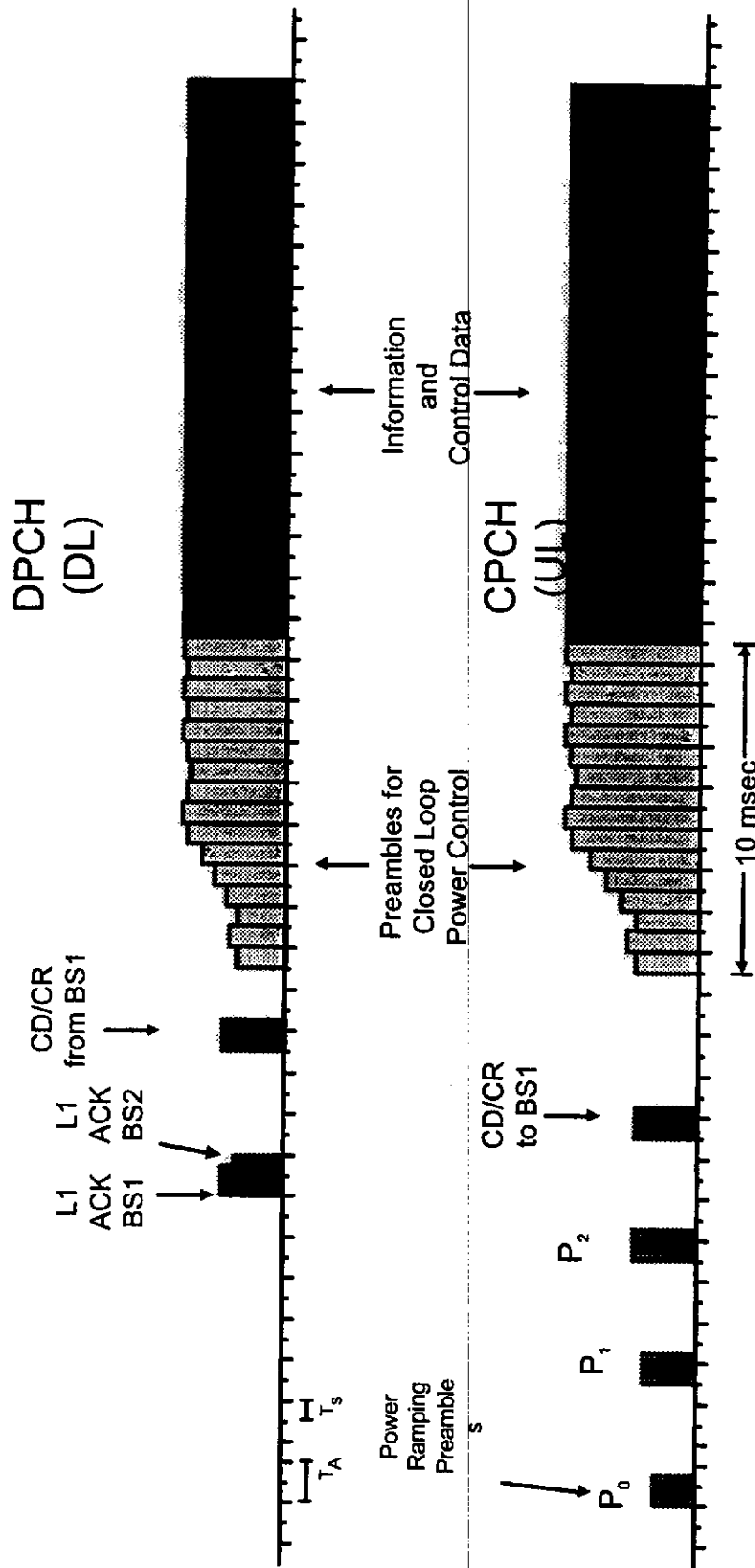


FIG 16. Common Packet Channel (CPCH) Timing Diagram with its Associated Downlink Dedicated Physical Channel Common Preamble for two or more Base Stations