20000/10728

PCT WORLD INTELLECTUAL PROPERTY ORGANIZATION
International Bureau

INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 6 : H03K 7/08, 9/08 — A1

(11) International Publication Number: WO 99/13576

(43) International Publication Date: 18 March 1999 (18.03.99)

(21) International Application Number: PCT/US98/14462

(22) International Filing Date: 9 July 1998 (09.07.98)

(30) Priority Data:
08/925,205 8 September 1997 (08.09.97) US

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(81) Designated States: AL, AM, AT, AT (Utility model), AU (Petty patent), AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, CZ (Utility model), DE, DE (Utility model), DK, DK (Utility model), EE, EE (Utility model), ES, FI, FI (Utility model), GB, GE, GH, GM, HR, HU, ID, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SK (Utility model), SL, TJ, TM, TR, TT, UA, UG, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published
With international search report.

(54) Title: METHOD AND APPARATUS FOR ENCODING AND DECODING A BIT SEQUENCE FOR TRANSMISSION OVER POTS WIRING

(57) Abstract

A method and apparatus for encoding a bit sequence as a symbol, suitable for transmission over pots wiring (14), are described. The symbol has a duration indicative of the bit sequence, this time duration be determined by first and second delimiters (92A) and (92B) respectively. The method commences upon the receipt of a bit sequence at an encoder (46A) thereafter, a symbol, having a duration indicative of the bit sequence, is identified. The symbol is characterized in that it includes a buffer portion (50A) and an encoding portion (46A). The buffer portion (50A) is of sufficient duration to allow reflections, generated by propagation of the first delimiter (92A) of the symbol on the pots wiring (14), to decay to a predetermined level before propagation of the second delimiter (92B).

