

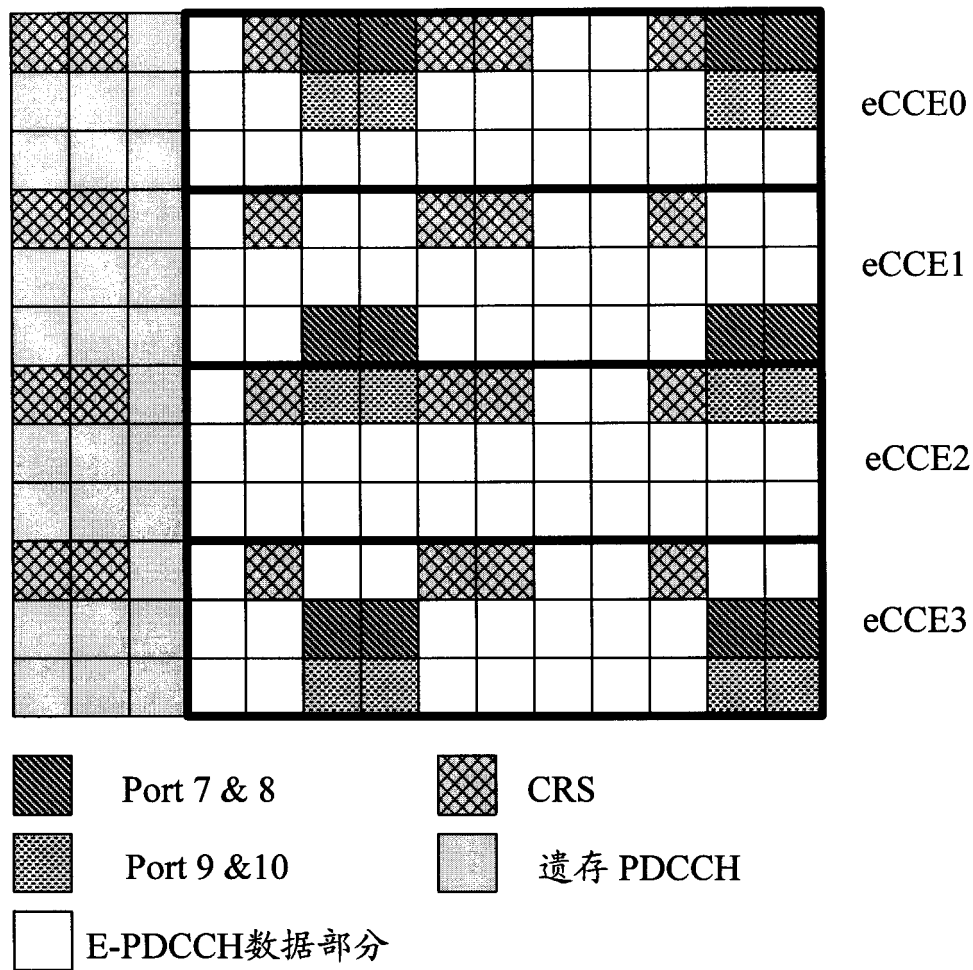


图 1

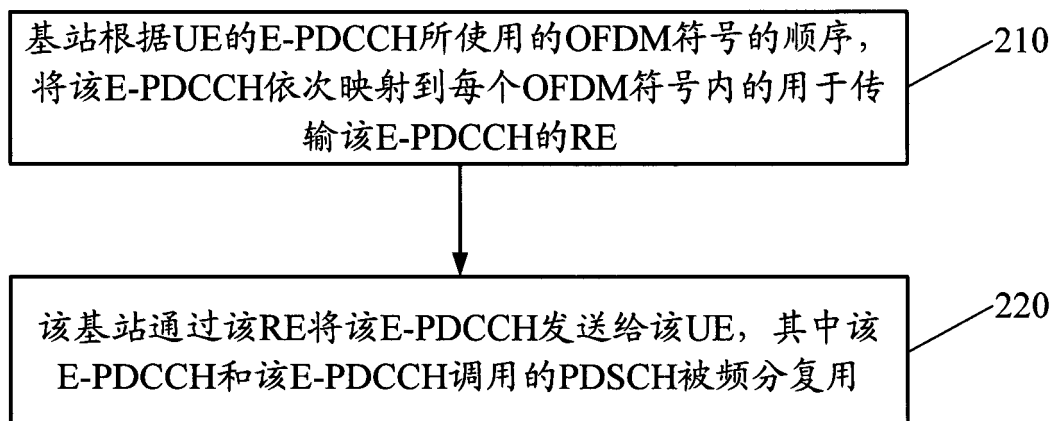


图2

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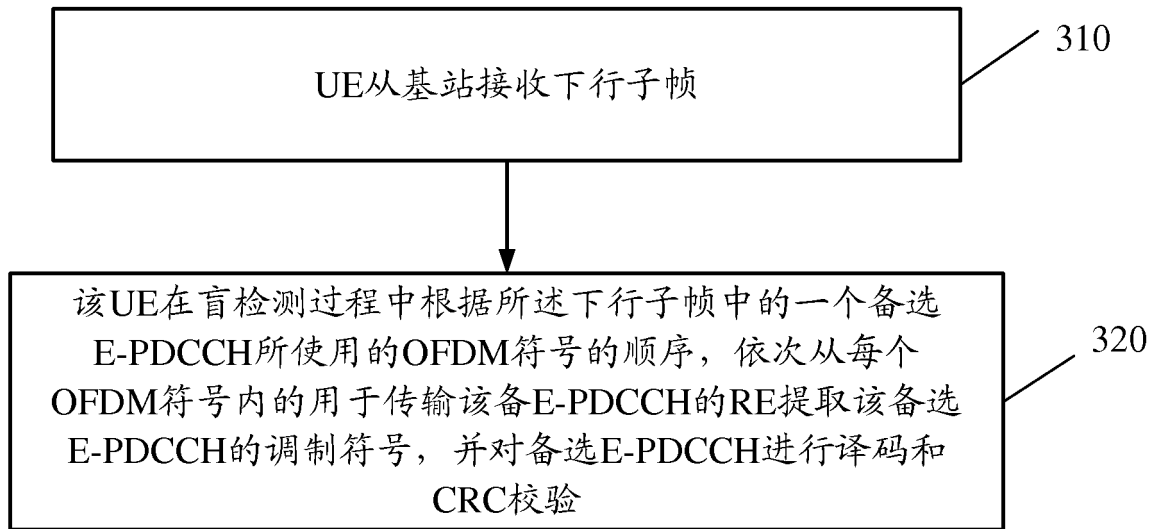


图3

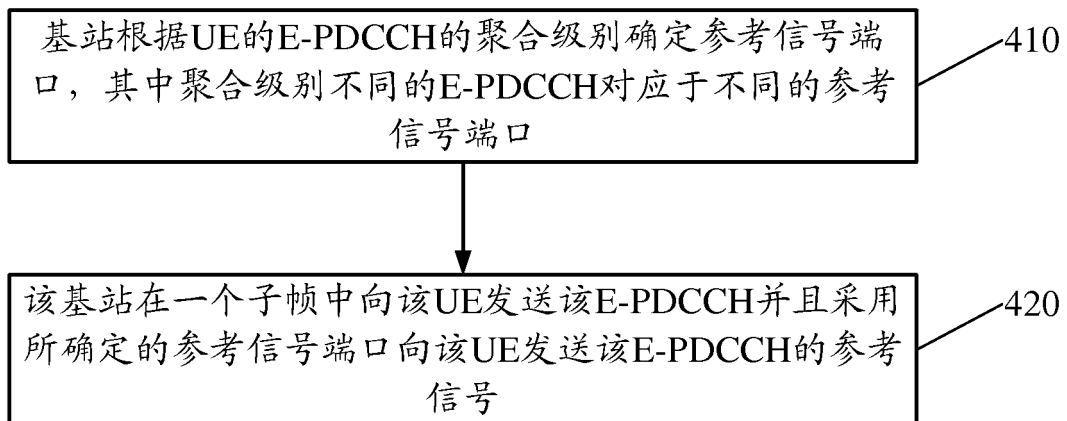


图4

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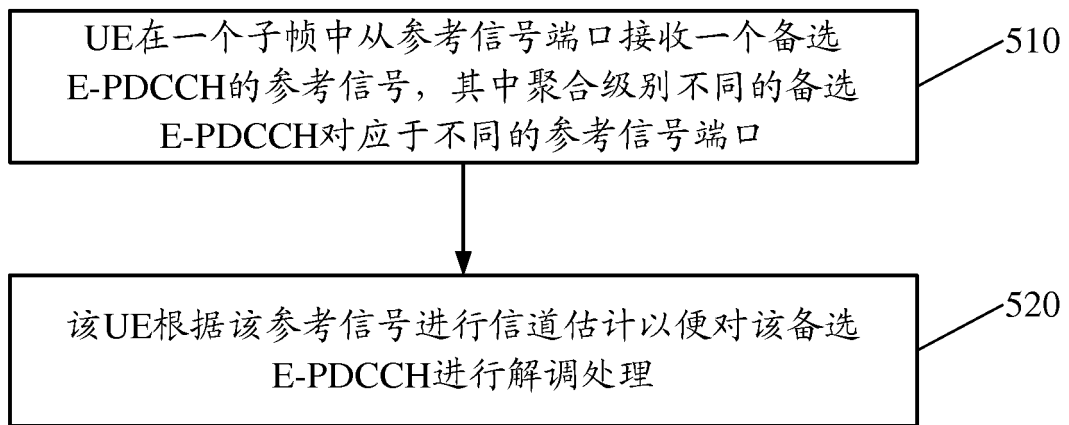


图5

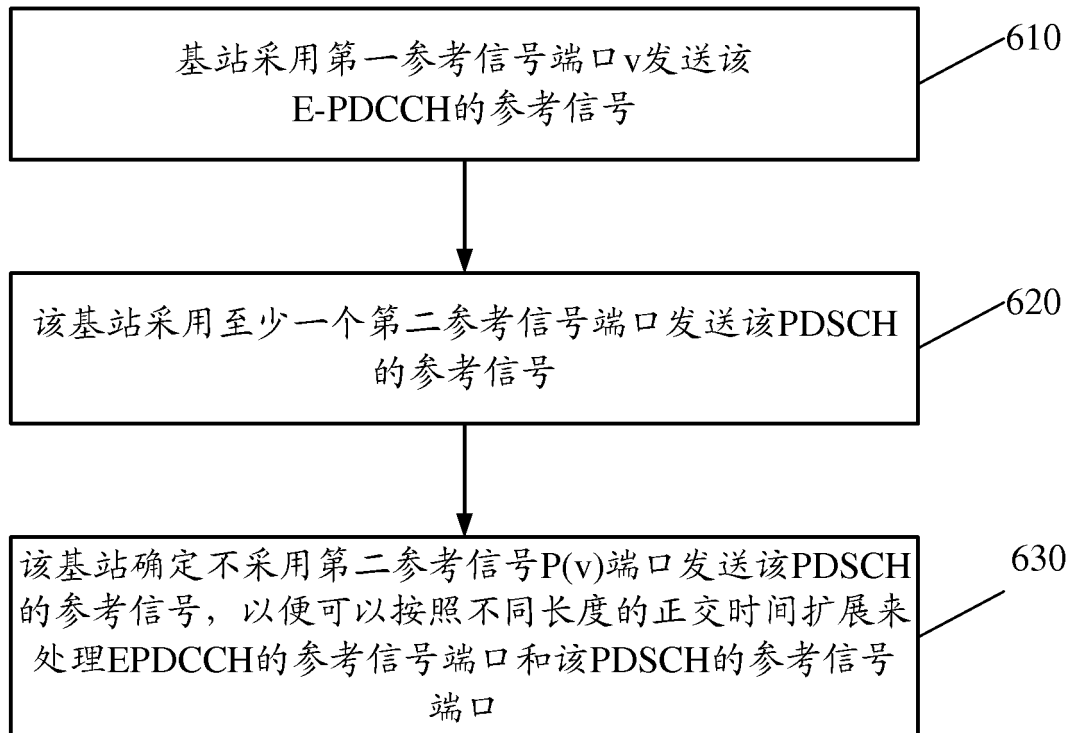


图6

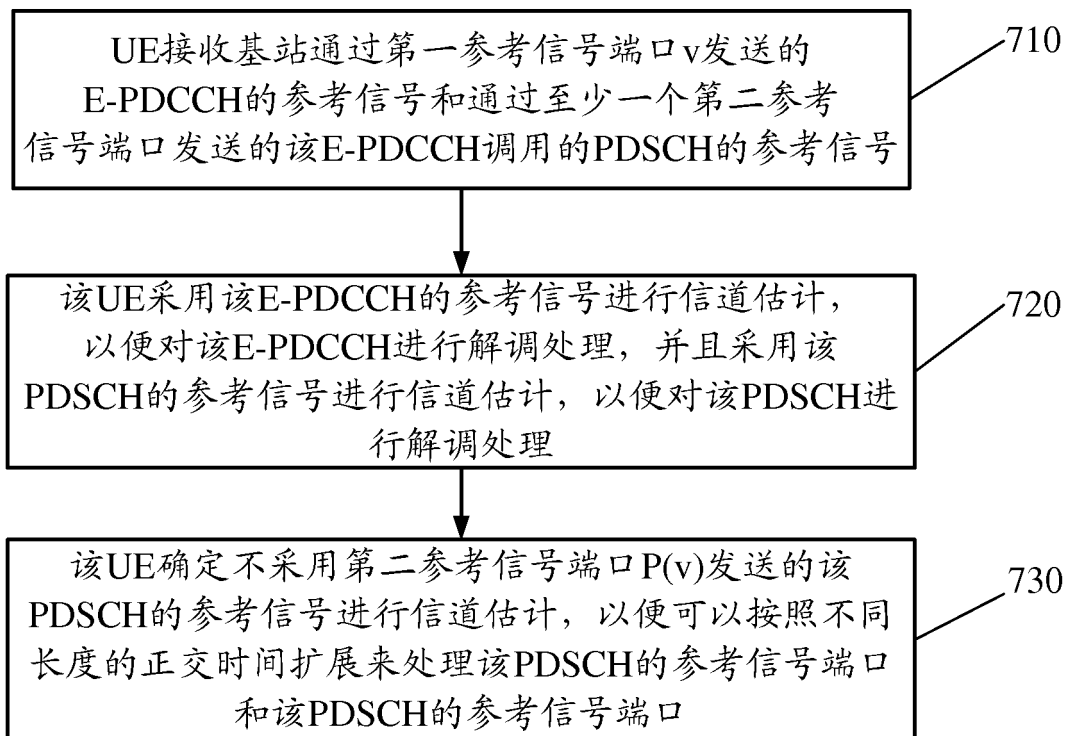


图7

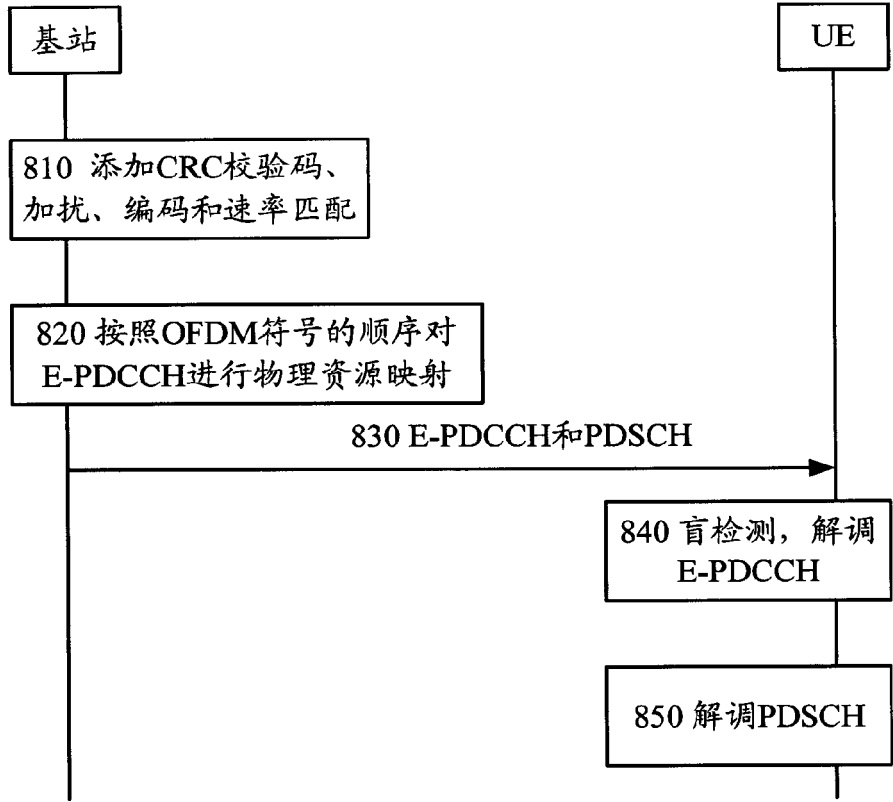


图 8A

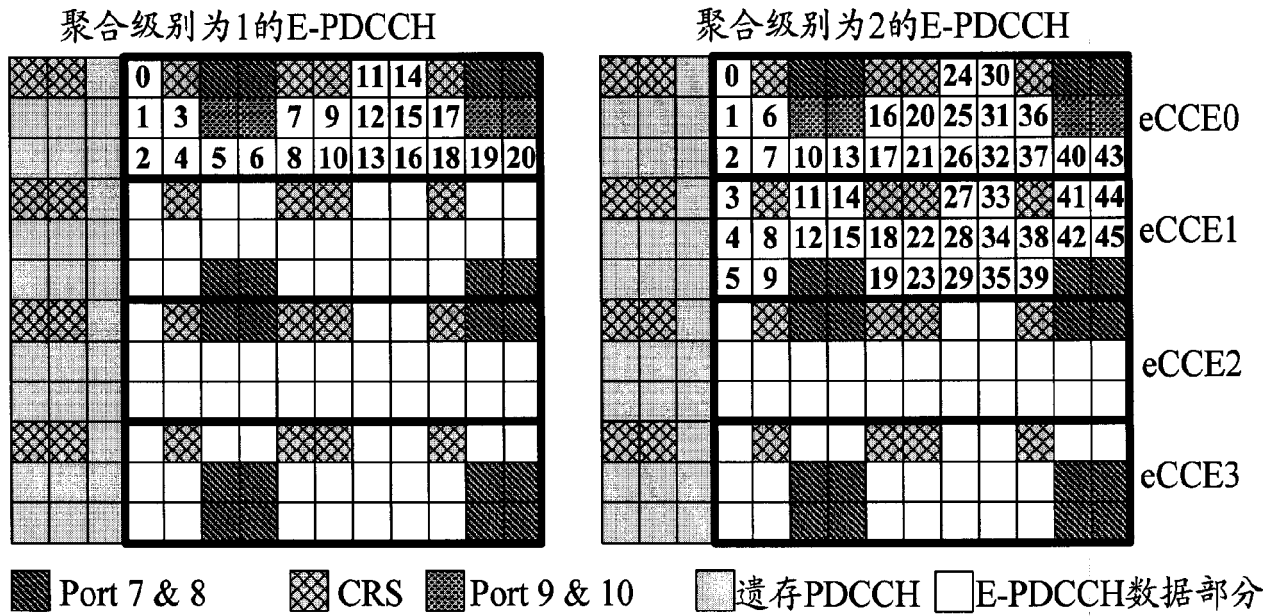


图 8B

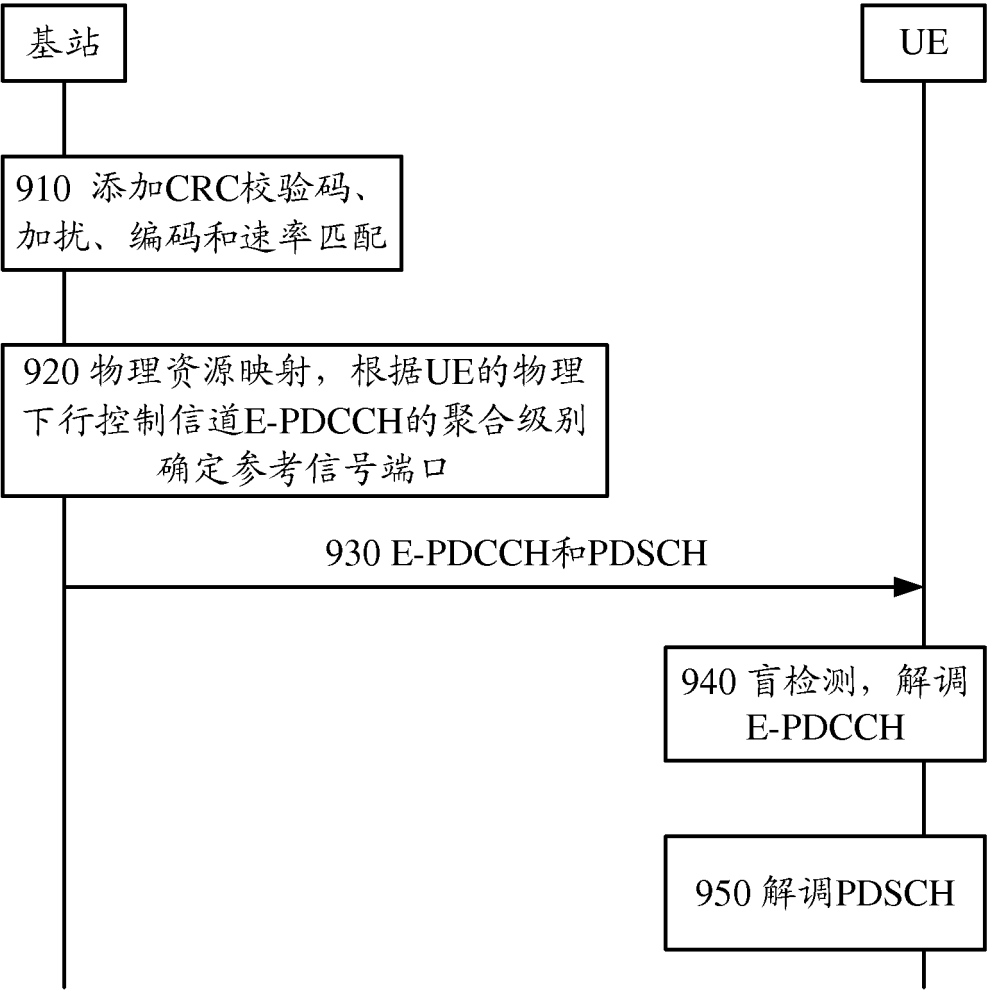


图 9A

7	8	9	10	7	8	9	10
8	8	10	10	8	8	10	10
9	9	9	9	9	9	9	9
10	10	10	10	10	10	10	10
RB0				RB1			
RBG0							

图9B

7	8	9	10	7	8	9	10	7
8	8	10	10	8	8	10	10	
9	9	9	9	9	9	9	9	
10	10	10	10	10	10	10	10	
RB0			RB1			RB2		

RBG0

图9C

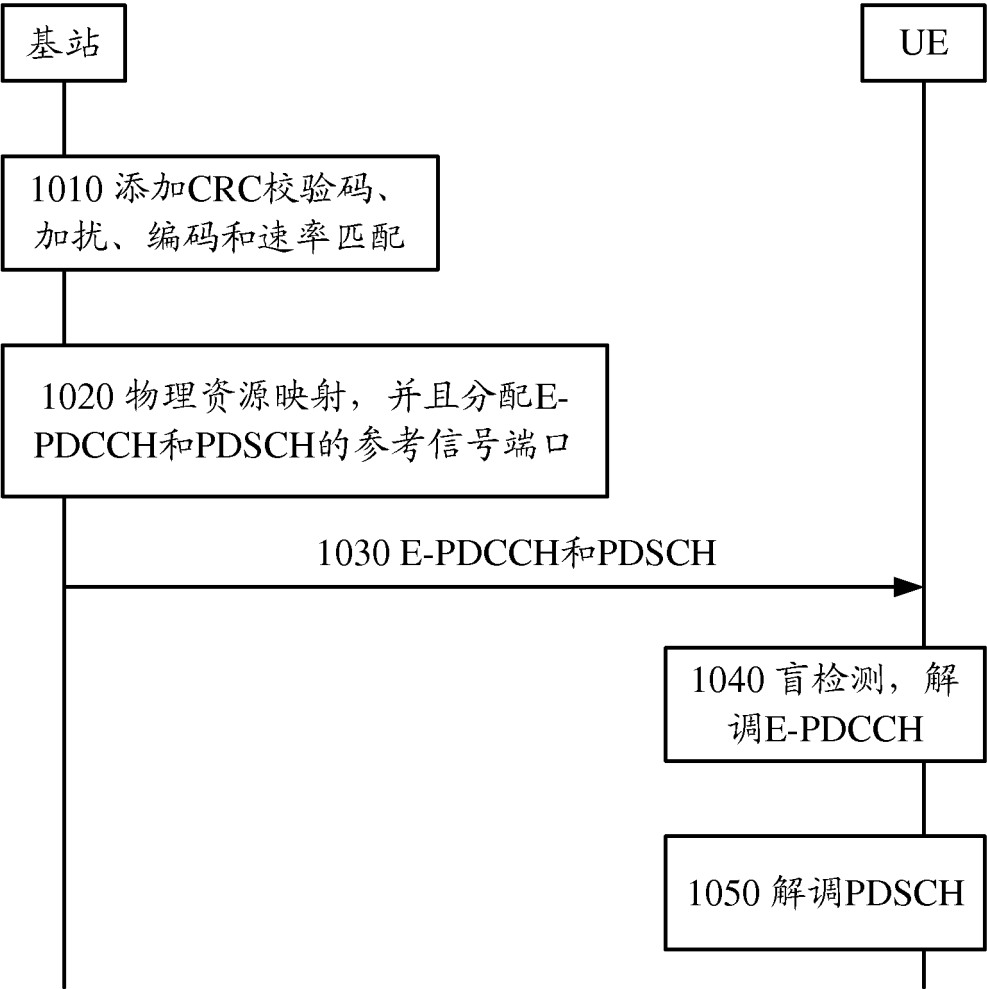


图 10

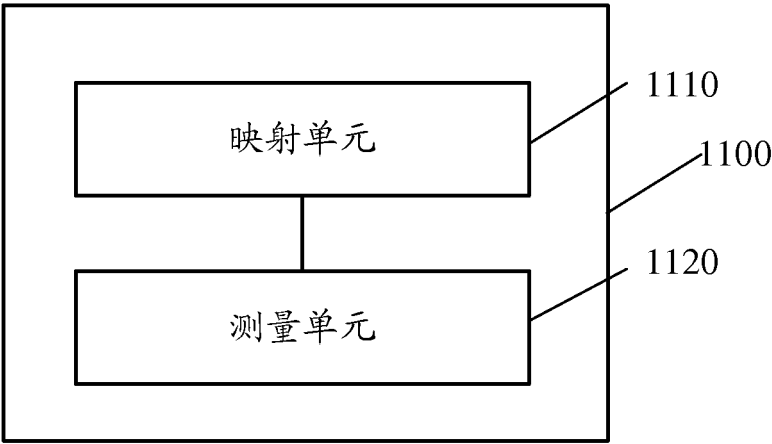


图11

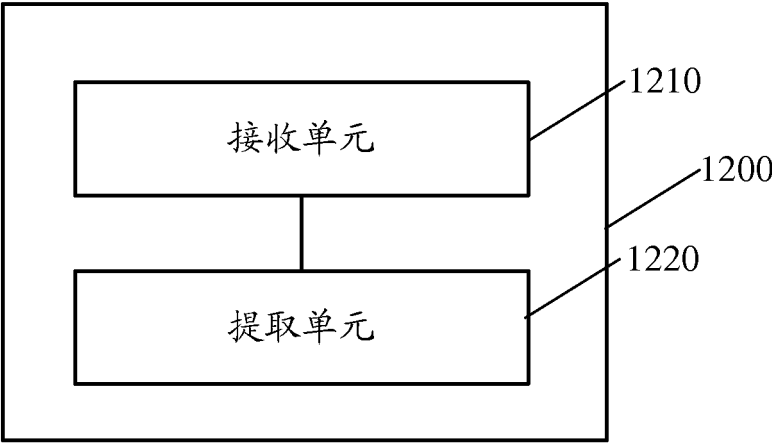


图12

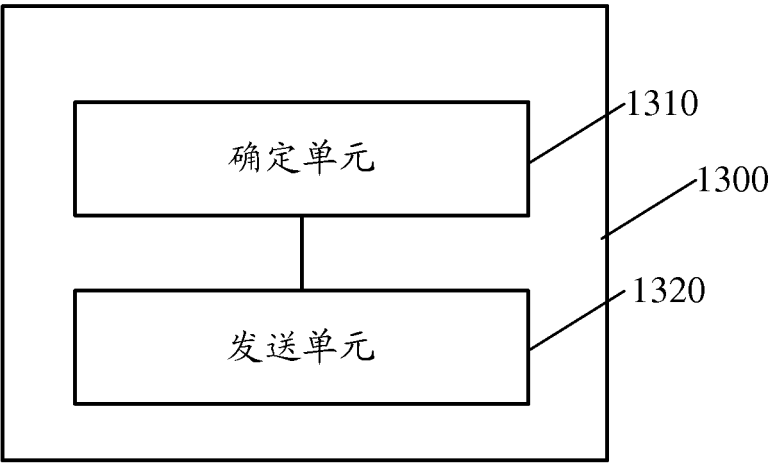


图13

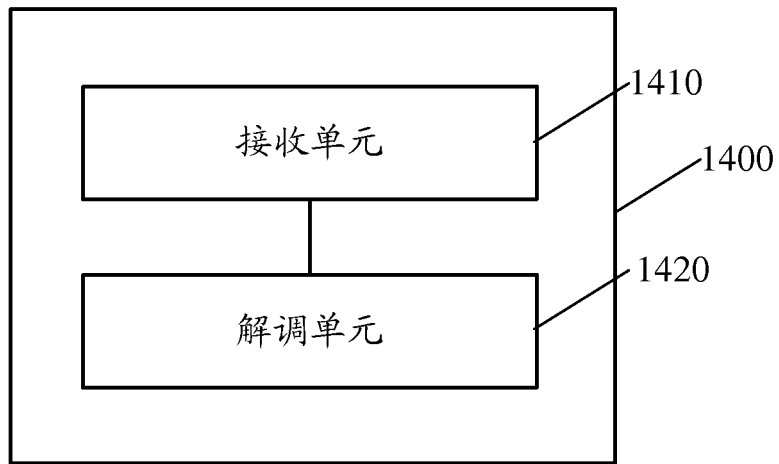


图14

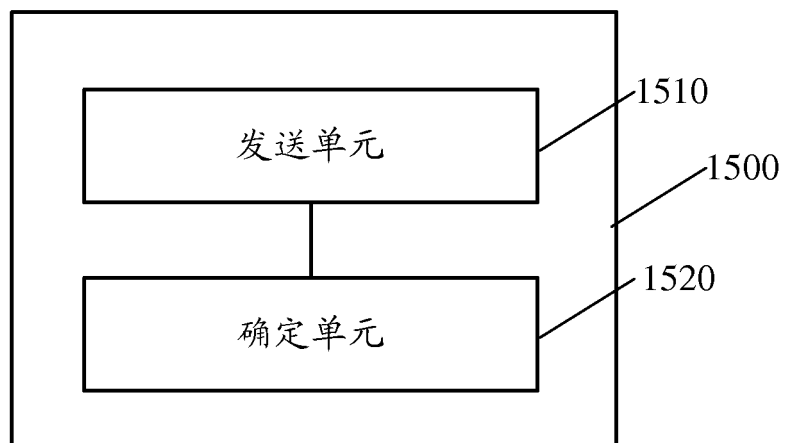


图15

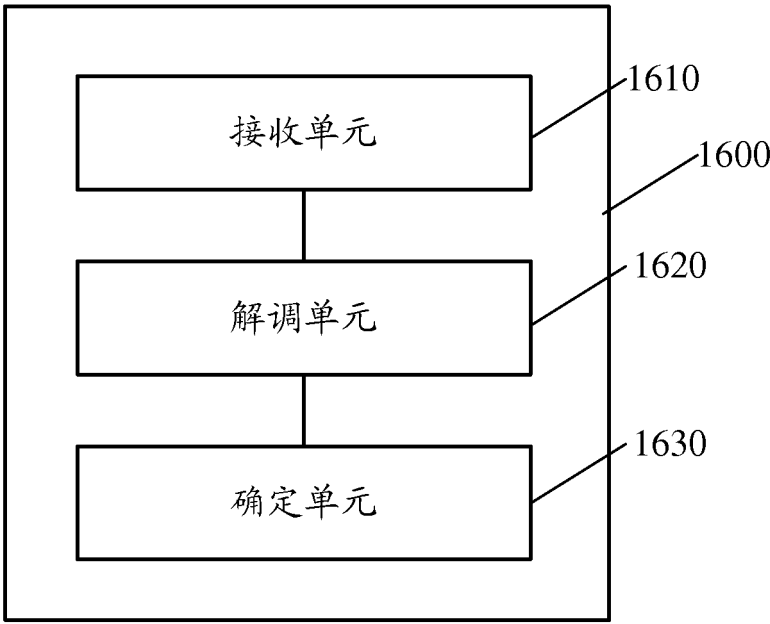


图16

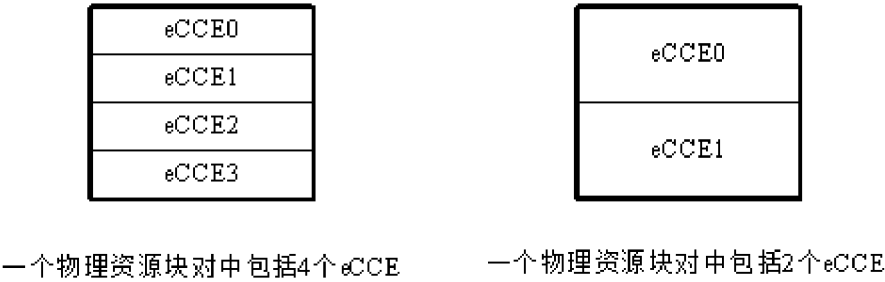


图 17

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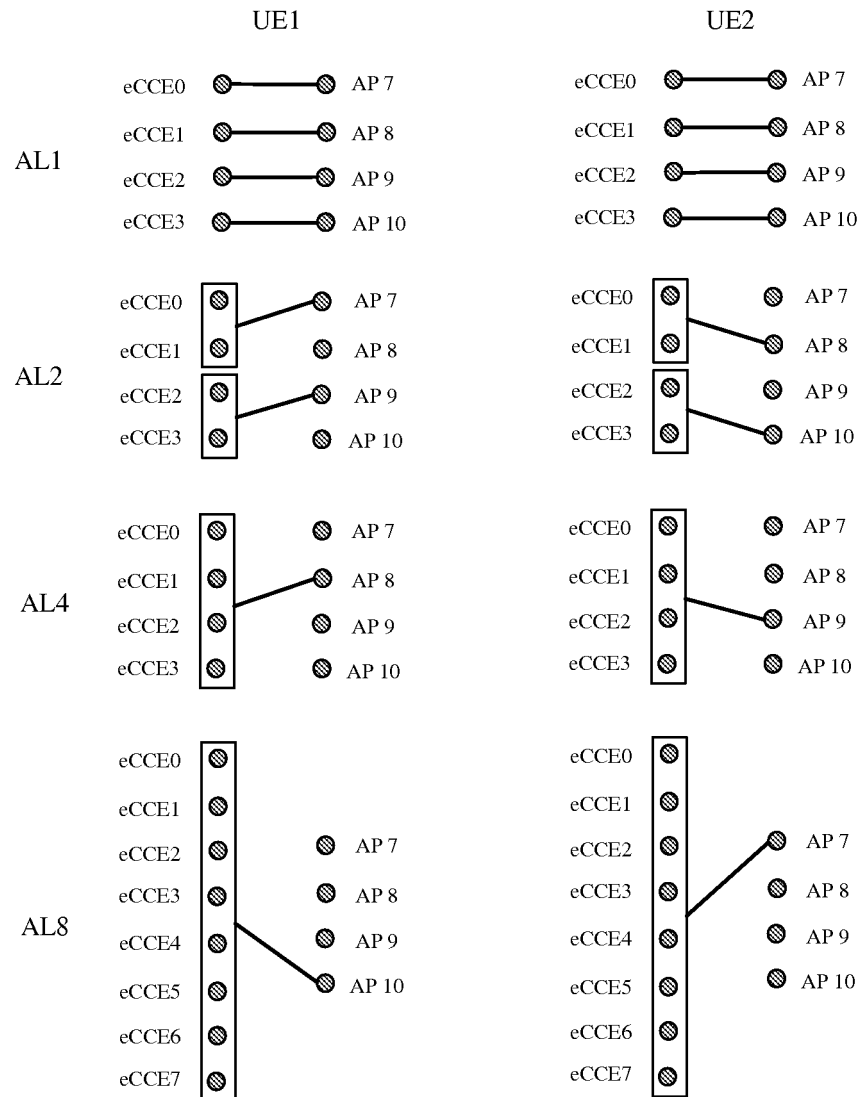


图 18

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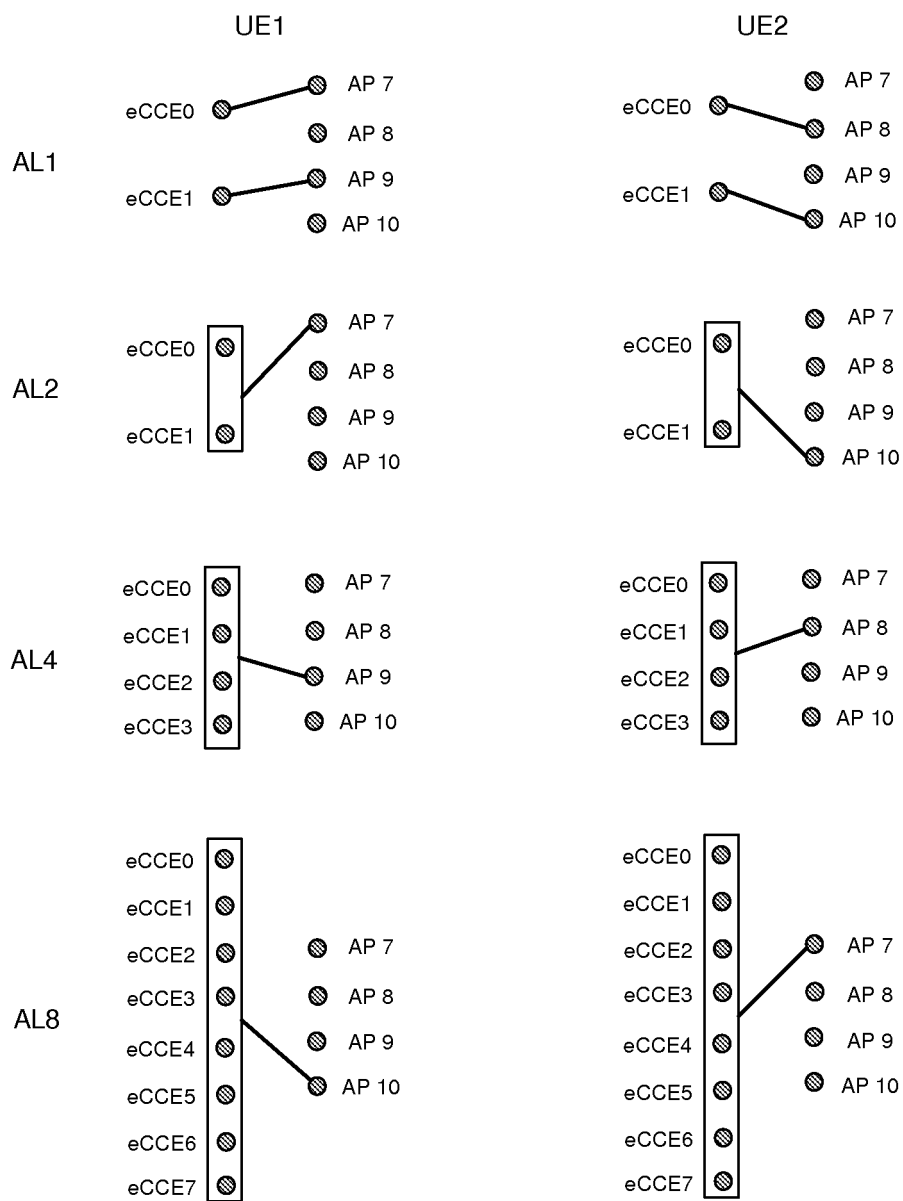


图 19

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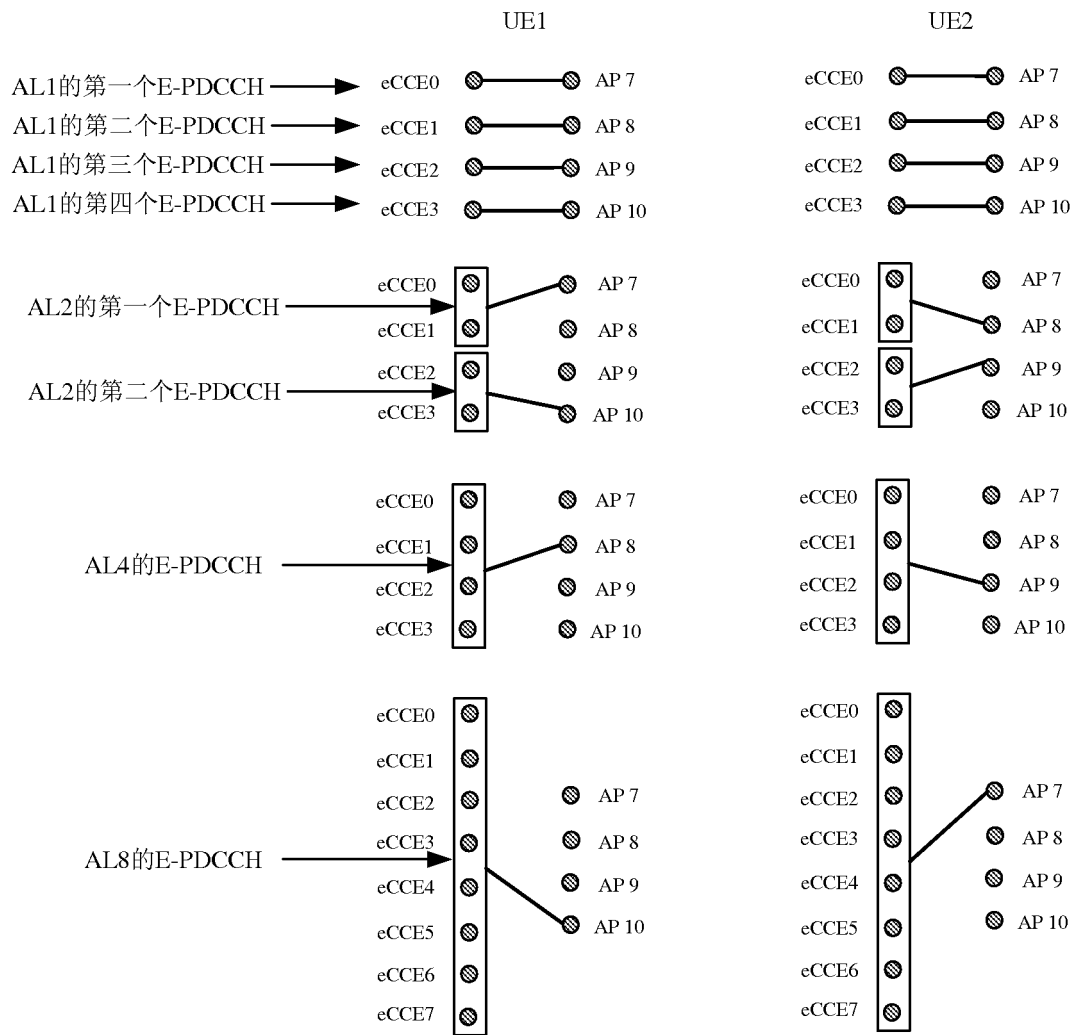


图 20

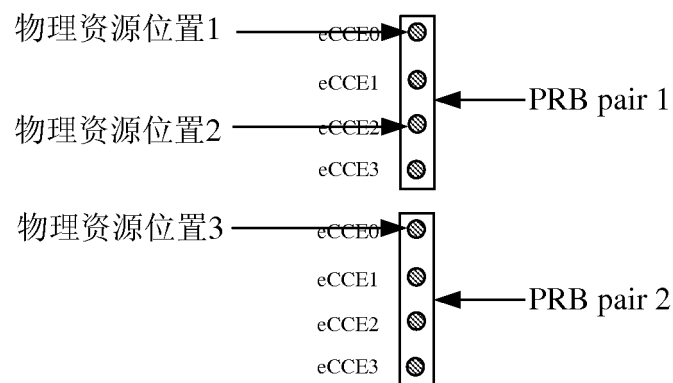


图 21