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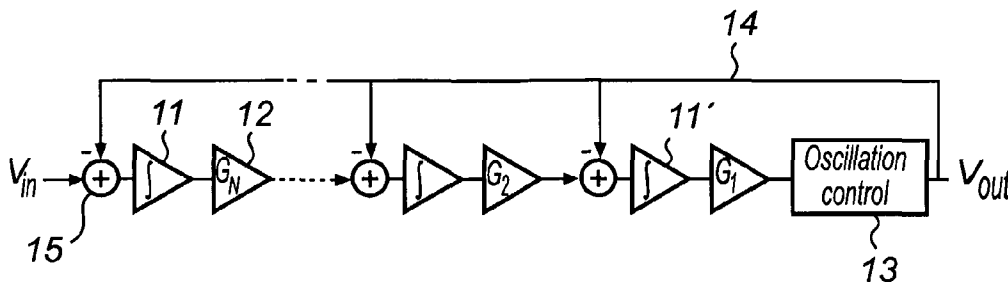
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(54) Title: DIGITAL PULSE WIDTH CONTROLLED OSCILLATION MODULATOR



(57) Abstract: A pulse width modulator (10) for converting a digital signal into a PWM signal, comprising a plurality of integrators (11) with integrator gains (12) arranged in series, a comparator (17) for comparing the output of the last integrator (11') with a reference, and thereby creating the PWM signal. The modulator further has means (13) for realizing self-oscillation at a desired switching frequency, and a feedback path (14) connected to a point down stream said comparator and leading to a plurality of summing points, each preceding one of said integrators, wherein the PWM signal is quantized in time by the clock frequency of the modulator, and wherein the integrator gains (12) are adapted to reduce any quantization noise.