

FIGURE 1

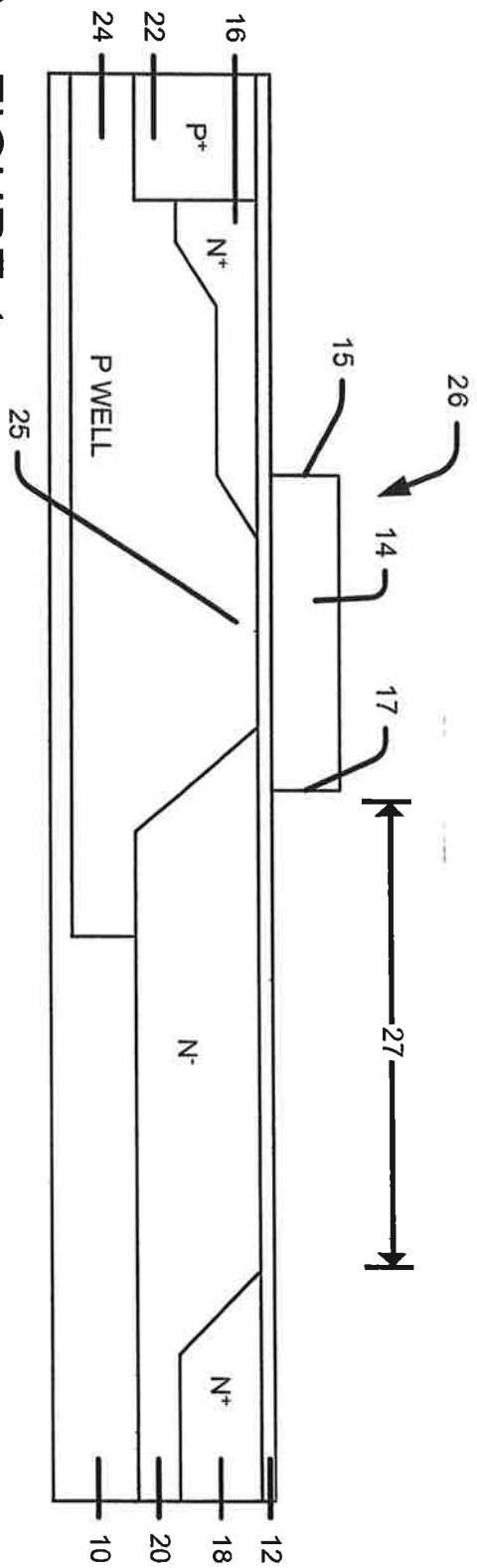
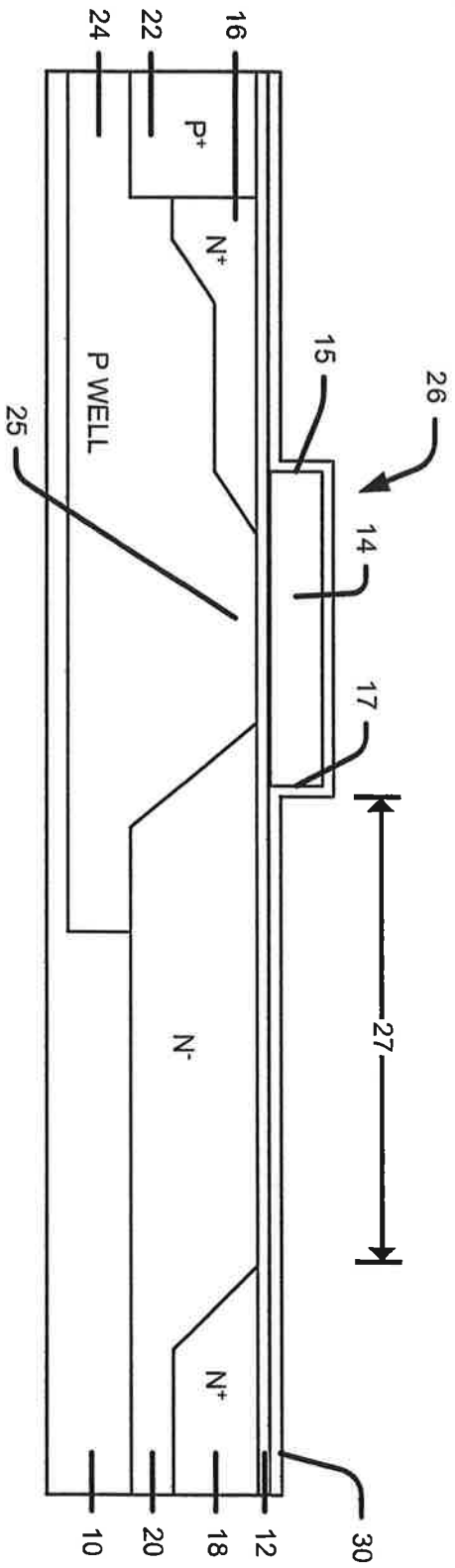


FIGURE 2



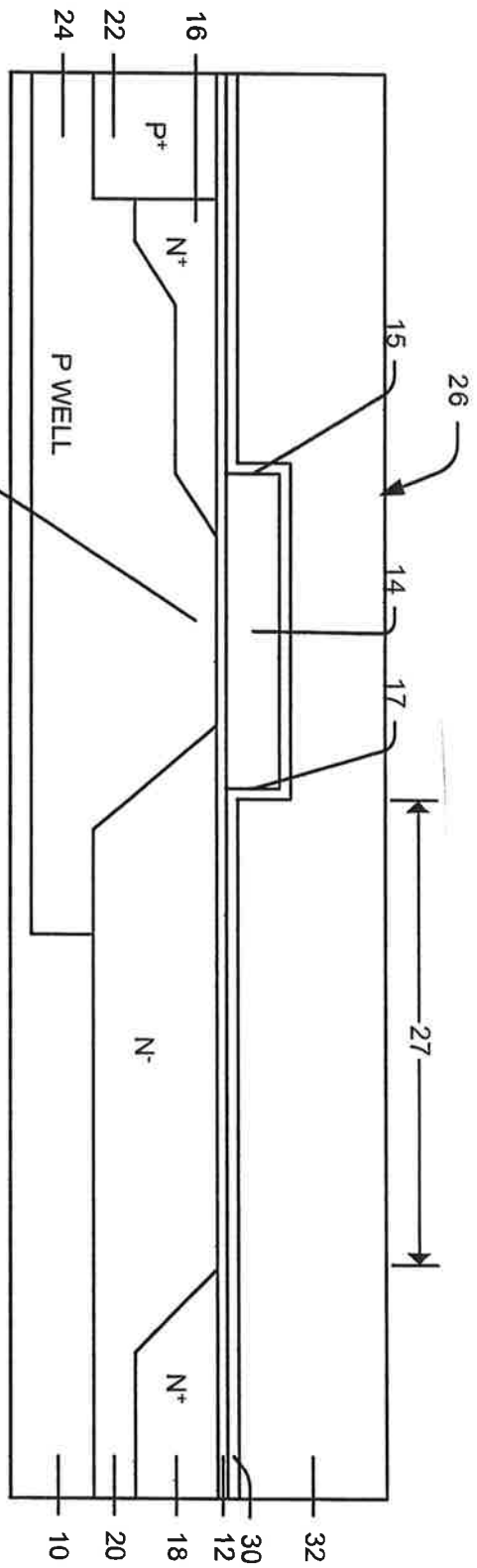


FIGURE 3

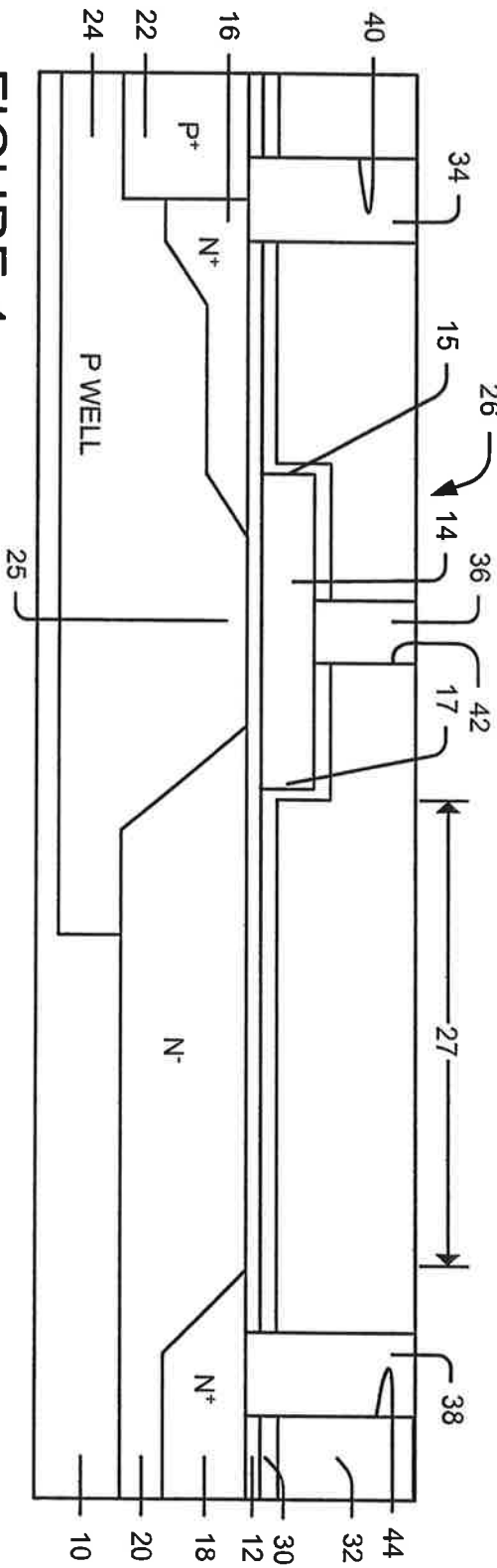


FIGURE 4

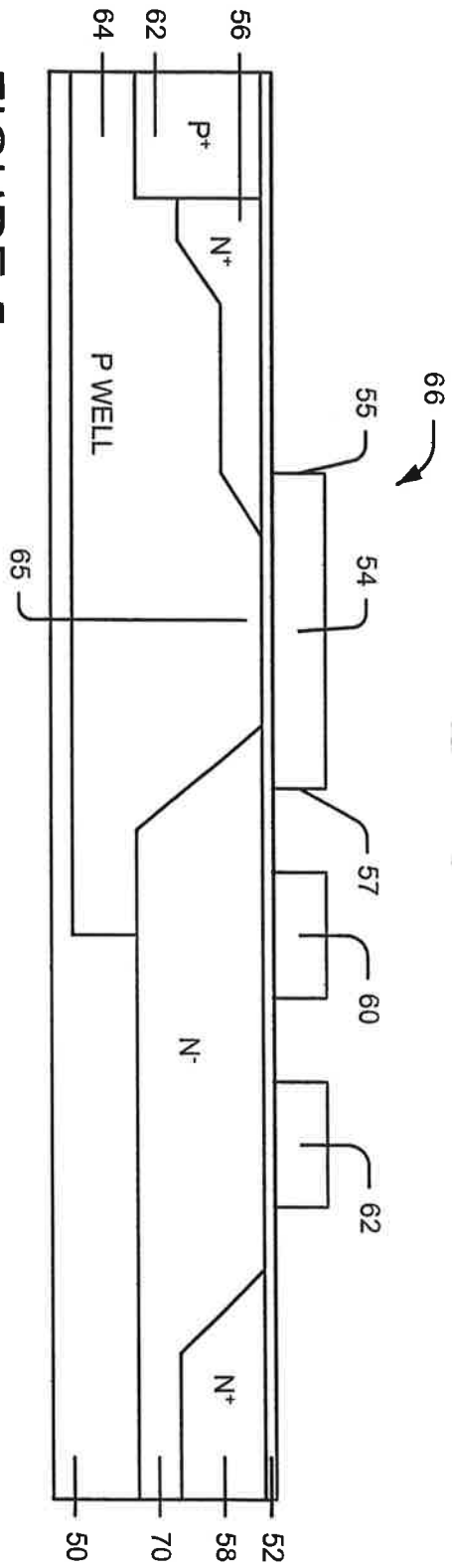


FIGURE 5

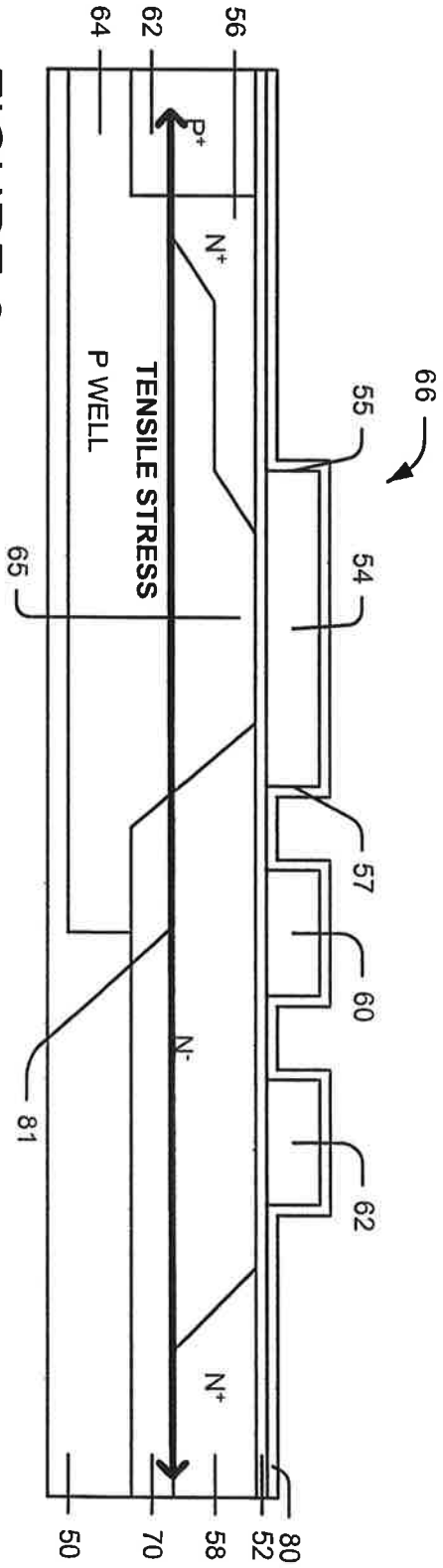


FIGURE 6



FIGURE 7



FIGURE 8

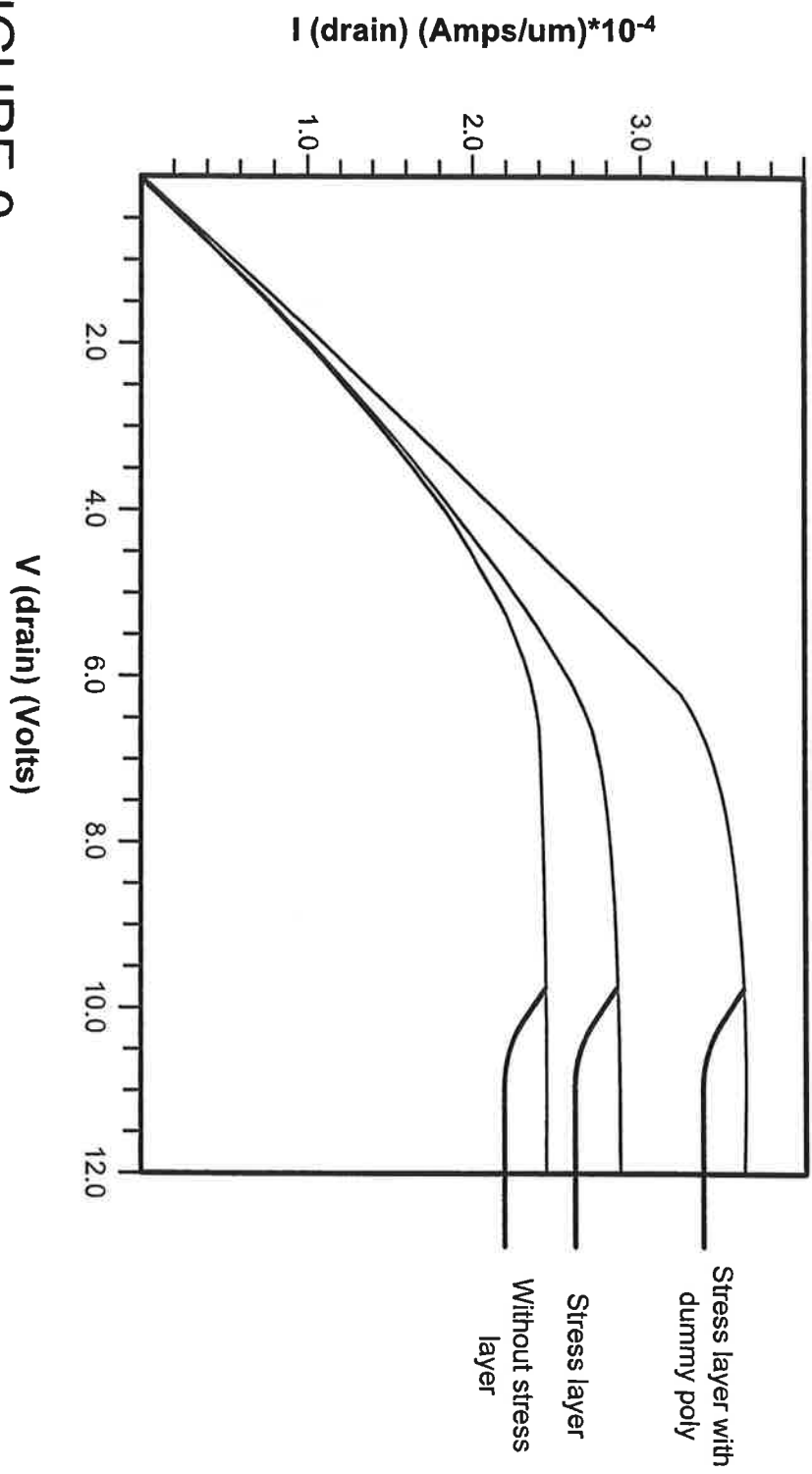


FIGURE 9

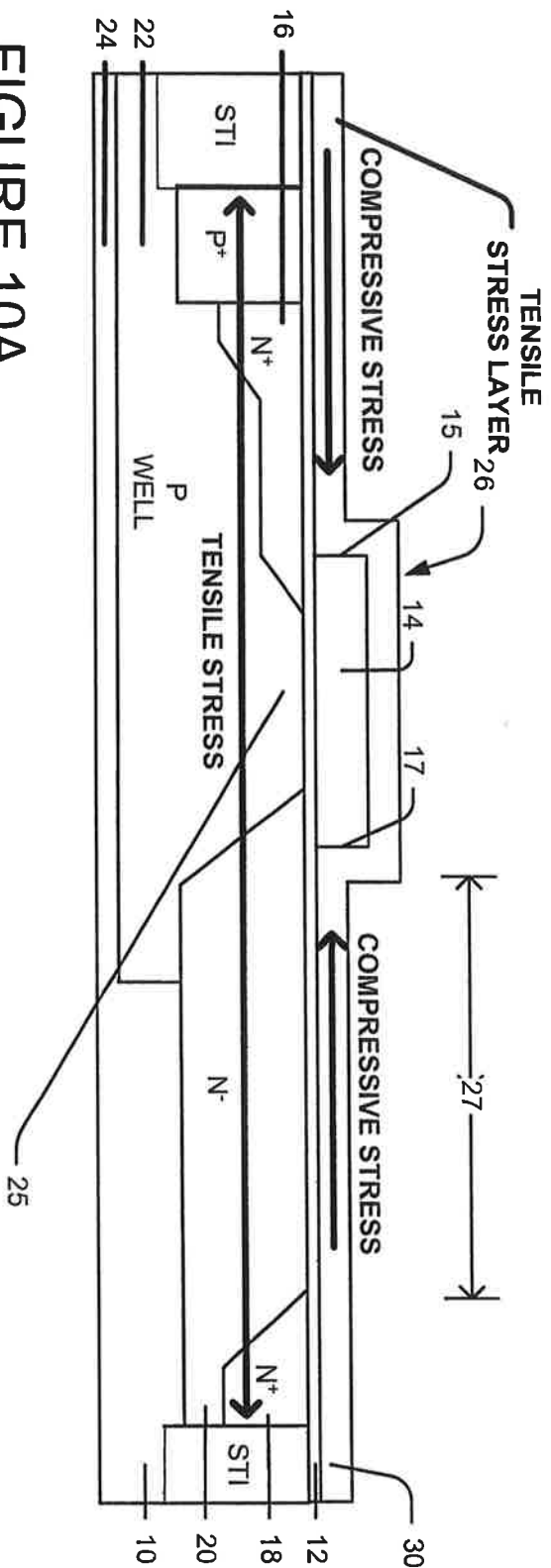


FIGURE 10A



FIGURE 10B

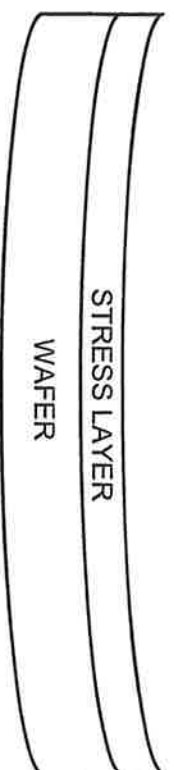


FIGURE 10C

FIGURE 10D

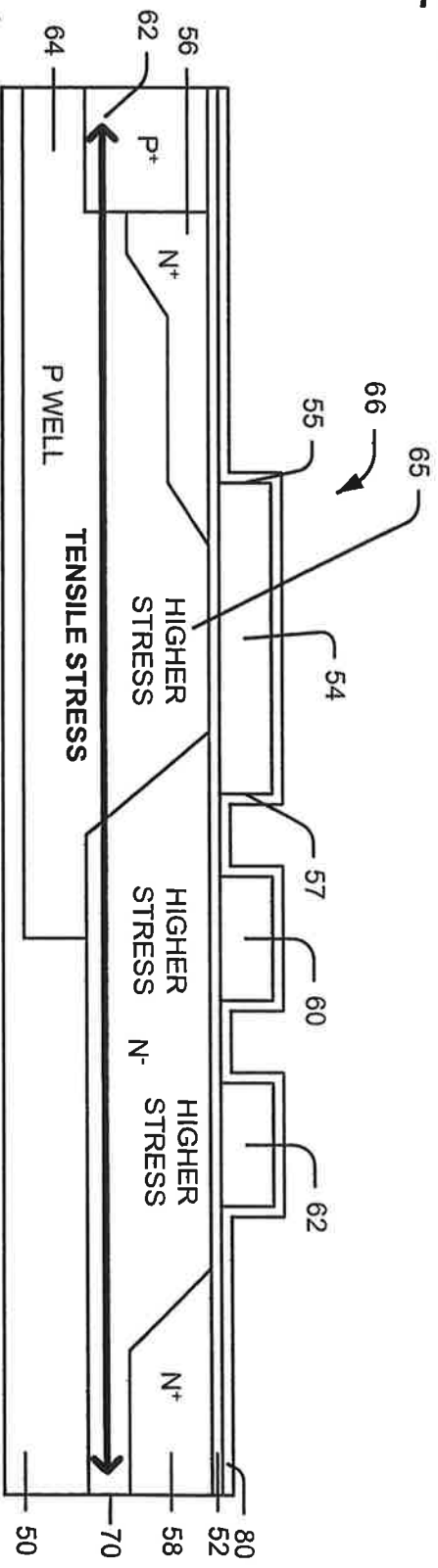
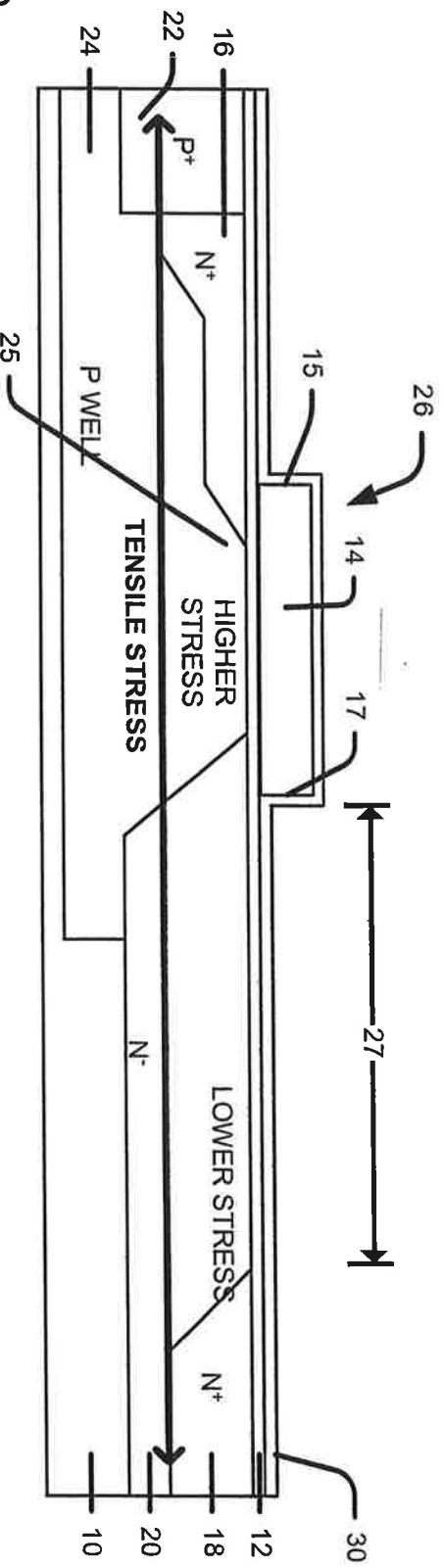


FIGURE 10E

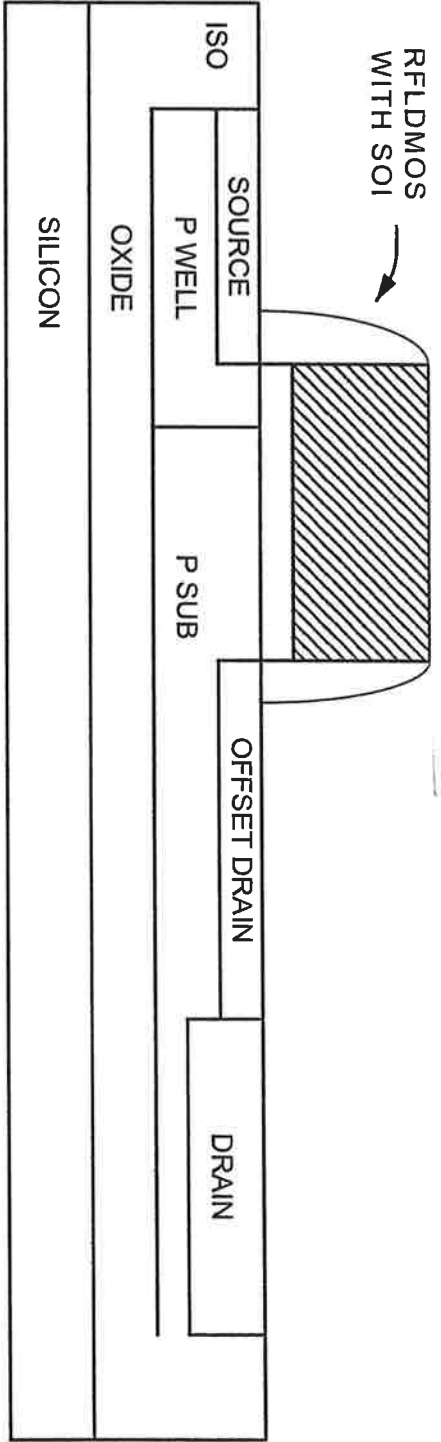


FIGURE 11A

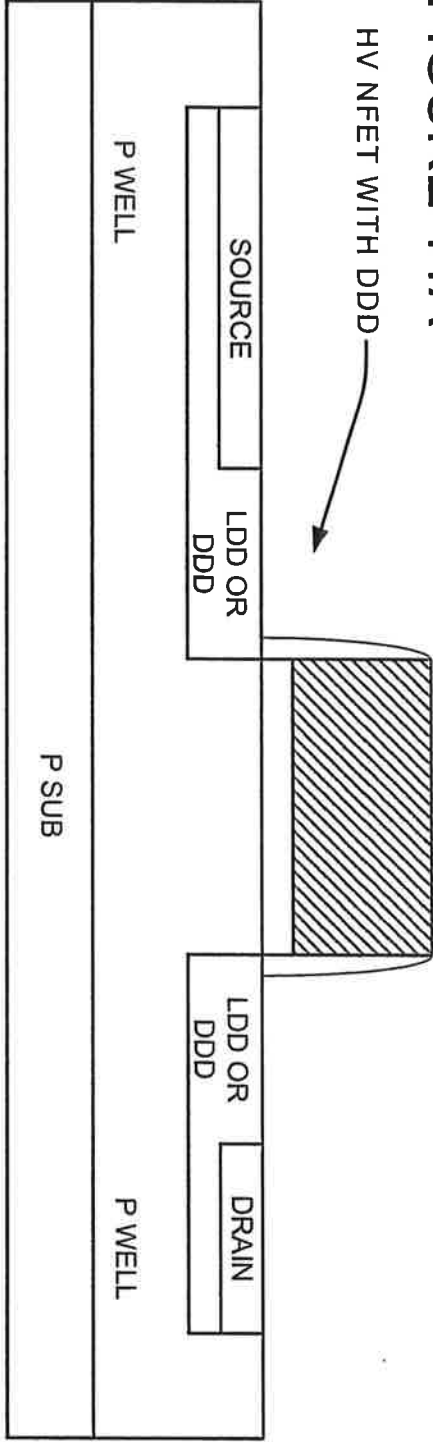


FIGURE 11B

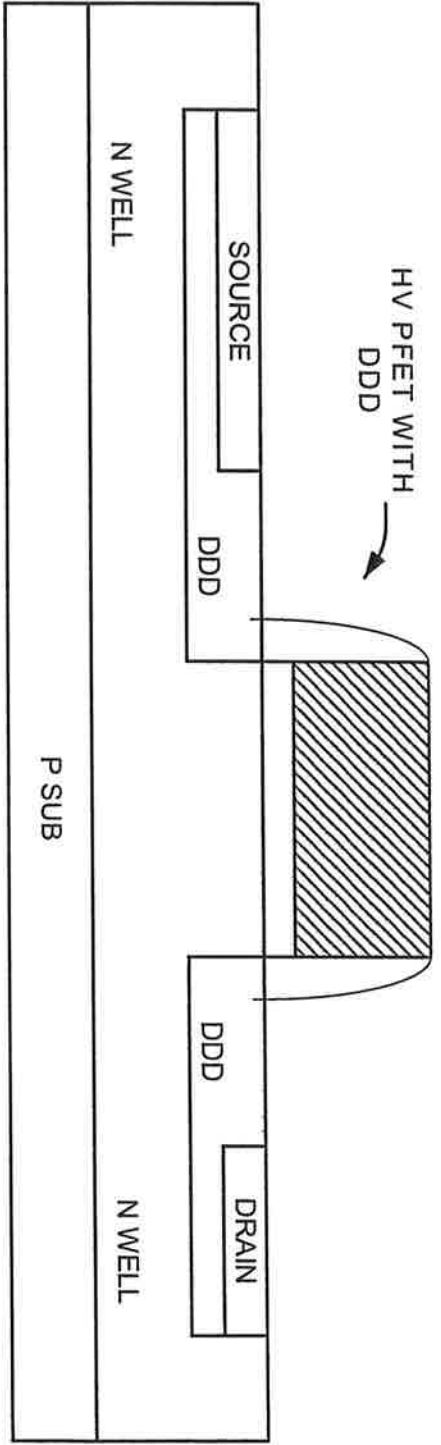


FIGURE 11C

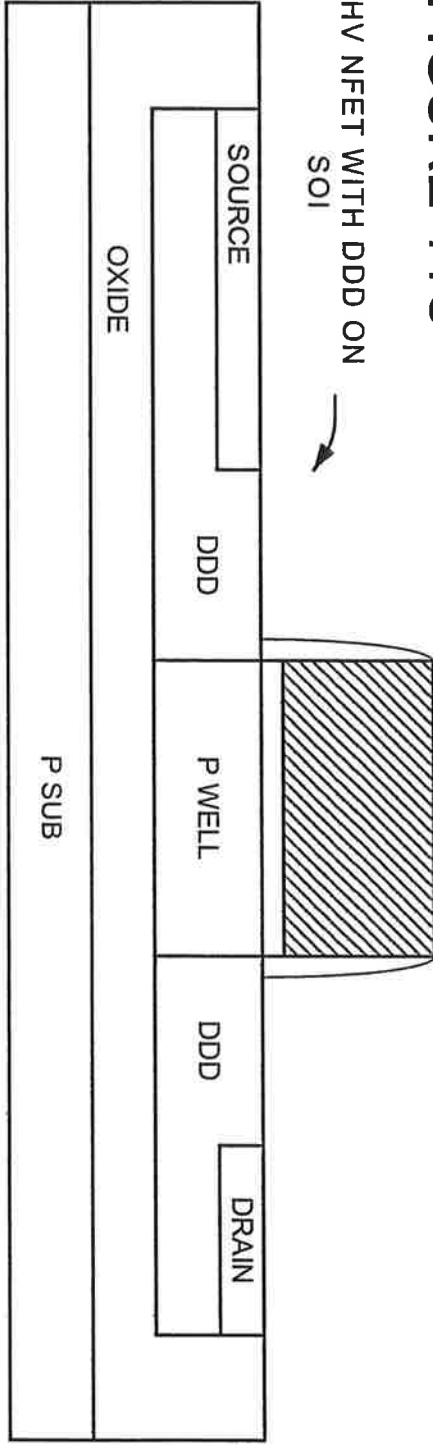


FIGURE 11D

HV PFET
WITH
DDD ON
SOI

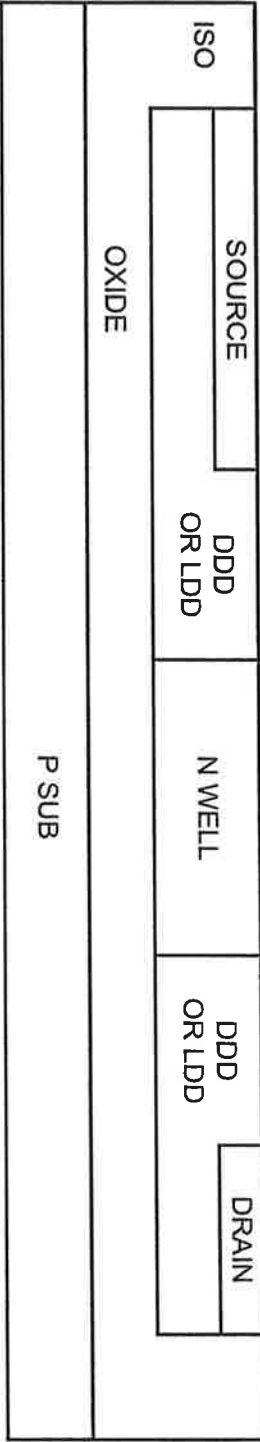
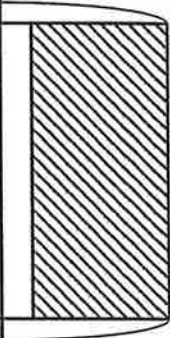


FIGURE 11E

HV PFET
WITH
DDD ON
SOI

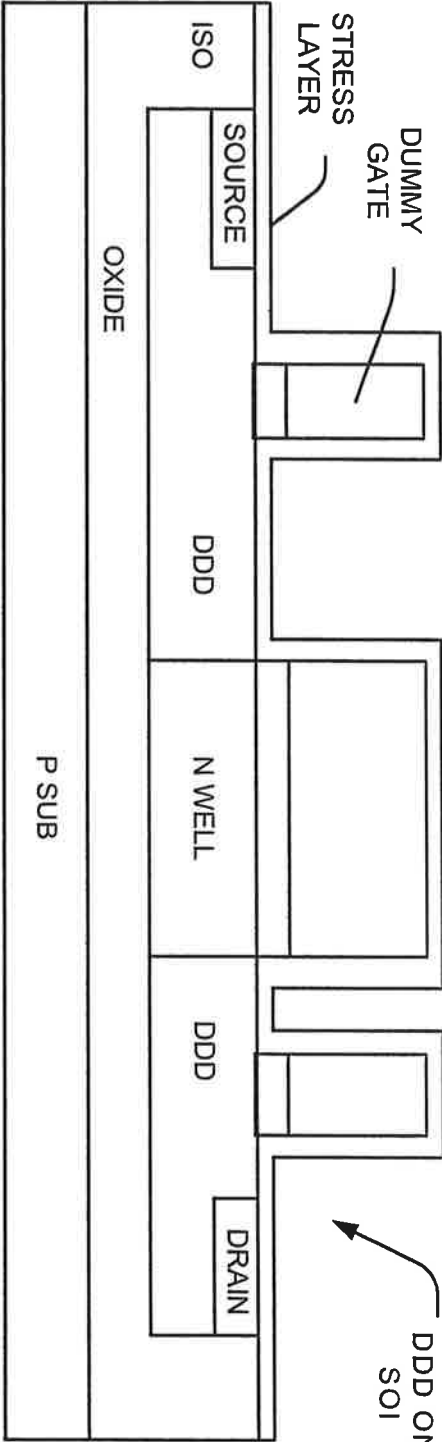


FIGURE 11F