

VIRTUAL NETWORK DEVICE

TECHNICAL FIELD

The present application relates to the field of computer networks and, in particular, to a virtual network device.

BACKGROUND ART

A wide area network (WAN) is an interconnected web of network devices that typically interconnects local area networks or metropolitan area networks over a large geographical area, such as across the state or across the country. WANs allow remotely located computers to communicate with each other via the network devices.

Conventional network devices typically include one or more physical network ports that operate at a predetermined fixed data rate, such as for example, 10/100/1000 Mbps (megabits per second), 10 Gbps (gigabits per second), 40 Gbps, and 100 Gbps connection. As part of enabling communication between the computer systems over a network, conventional network devices negotiate the transfer speed of a network port and, during that process, the transfer speed of the network port is fixed.

One of the disadvantages of conventional network devices is that there is often a requirement for more physical ports than are available, which results in reduced service or costly upgrades. As a result, there is a need for an approach to accommodating the increasing need for ports.

SUMMARY OF THE INVENTION

The present invention provides a virtual network device with virtual ports that effectively increase the number of available physical ports. The virtual network device includes a framing circuit that receives a plurality of input frames, examines the plurality of input frames to determine a frame type for each input frame, and

determines a virtual exit device associated with each input frame based on the frame type. The framing circuit also encapsulates the plurality of input frames to form a plurality of first encapsulated frames. Each virtual exit device has a receive virtual port. The plurality of first encapsulated frames have a plurality of headers. The

5 plurality of headers identifies a plurality of virtual exit devices that are associated with the plurality of input frames. In addition, the virtual network device includes a plurality of transmit virtual ports that are coupled to the framing circuit. The plurality of transmit virtual ports determine a plurality of next hops in a virtual network for the plurality of first encapsulated frames based on the virtual exit devices in the headers

10 of the plurality of first encapsulated frames. The plurality of transmit virtual ports additionally encapsulate the plurality of first encapsulated frames to form a plurality of second encapsulated frames. Each second encapsulated frame has a header. The header of a second encapsulated frame identifies a next hop of the second encapsulated frame based on a next hop of a first encapsulated frame, and the

15 receive virtual port of the associated virtual exit device of an input frame. The transmit virtual ports include a first portion of a memory. Further, the virtual network device includes a transmit virtual switch coupled to the plurality of transmit virtual ports. The transmit virtual switch selectively couples the transmit virtual ports to a network physical port.

20 The present invention also includes a method of operating a virtual network device. The method includes receiving a plurality of input frames, examining the plurality of input frames to determine a frame type for each input frame, and determining a virtual exit device associated with each input frame based on the frame type. Each virtual exit device has a receive virtual port. The method also

25 includes encapsulating the plurality of input frames to form a plurality of first encapsulated frames. The plurality of first encapsulated frames has a plurality of headers. The plurality of headers identifies a plurality of virtual exit devices associated with the plurality of input frames. In addition, the method includes determining a plurality of next hops in a virtual network for the plurality of first

encapsulated frames based on the virtual exit devices in the headers of the plurality of first encapsulated frames. Further, the method includes encapsulating a plurality of first encapsulated frames in a plurality of transmit virtual ports to form a plurality of second encapsulated frames. Each second encapsulated frame has a header. The
5 header of a second encapsulated frame identifies a next hop of the second encapsulated frame based on a next hop of a first encapsulated frame, and the receive virtual port of the associated virtual exit device of an input frame. The transmit virtual ports include a first portion of a shared memory. The method additionally includes selectively coupling the transmit virtual ports to a network
10 physical port.

The present invention also provides a non-transitory computer-readable storage medium having embedded therein program instructions, which when executed by a processor causes the processor to execute a method of operating a virtual network device. The method includes receiving a plurality of input frames,
15 examining the plurality of input frames to determine a frame type for each input frame, and determining a virtual exit device associated with each input frame based on the frame type. Each virtual exit device has a receive virtual port. The method also includes encapsulating the plurality of input frames to form a plurality of first encapsulated frames. The plurality of first encapsulated frames has a plurality of
20 headers. The plurality of headers identifies a plurality of virtual exit devices associated with the plurality of input frames. In addition, the method includes determining a plurality of next hops in a virtual network for the plurality of first encapsulated frames based on the virtual exit devices in the headers of the plurality of first encapsulated frames. Further, the method includes encapsulating a plurality
25 of first encapsulated frames in a plurality of transmit virtual ports to form a plurality of second encapsulated frames. Each second encapsulated frame has a header. The header of a second encapsulated frame identifies a next hop of the second encapsulated frame based on a next hop of a first encapsulated frame, and the receive virtual port of the associated virtual exit device of an input frame. The

method additionally includes selectively coupling the transmit virtual ports to a network physical port.

A better understanding of the features and advantages of the present invention will be obtained by reference to the following detailed description and accompanying drawings which set forth an illustrative embodiment in which the
5 principals of the invention are utilized.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram illustrating an example of a virtual network device
10 100 in accordance with the present invention.

FIG. 2A is a block diagram illustrating an example of a transmit circuit 200 in accordance with the present invention.

FIG. 2B is a block diagram illustrating an example of a transmit circuit 250 in accordance with the present invention.

15 FIG. 3A is a flow chart illustrating an example of a method 300 of operating transmit circuit 200 in accordance with the present invention.

FIG. 3B is a flow chart illustrating an example of a method 350 of operating transmit circuit 200 in accordance with the present invention.

20 FIG. 4 is a block diagram illustrating an example of a transmit circuit 400 in accordance with an alternate embodiment of the present invention.

FIG. 5 shows a block diagram that illustrates an example of a transmit circuit 500 in accordance with an alternate embodiment of the present invention.

FIG. 6 is a block diagram illustrating an example of a receive circuit 600 in accordance with the present invention.

25 FIG. 7 shows a flow chart illustrating an example of a method 700 of operating receive circuit 600 in accordance with the present invention.

FIG. 8 is a block diagram illustrating an example of a receive circuit 800 in accordance with an alternate embodiment of the present invention.

FIG. 9 is a block diagram illustrating an example of a receive circuit 900 in accordance with an alternate embodiment of the present invention.

A better understanding of the features and advantages of the present invention will be obtained by reference to the following detailed description and
5 accompanying drawings which set forth an illustrative embodiment in which the principals of the invention are utilized.

BEST MODE FOR CARRYING OUT THE INVENTION

FIG. 1 shows a block diagram that illustrates an example of a virtual network
10 device 100 in accordance with the present invention. As described in greater detail below, the effective number of local physical ports in virtual network device 100 are increased by converting one or more of the local physical ports into a plurality of virtual local physical ports, and the effective number of network physical ports are increased by converting one or more of the network physical ports into a plurality of
15 virtual network physical ports.

Virtual network device 100 is a component in a virtual network that interconnects a local router/switch with a remote router/switch. The virtual network includes a virtual network device 100, which functions as a virtual entry device into the virtual network, that is coupled to a local router/switch, and a virtual network
20 device 100, which functions as a virtual exit device from the virtual network, that is coupled to a remote router/switch.

As shown in FIG. 1, virtual network device 100 includes a transmit circuit 110 that has a number of local physical ports 112 that each receive frames of data, such as set-top box (STB), personal computer (PC), and video frames of data, from a local
25 network device, such as a router/switch, and a number of network physical ports 114 that each output frames of data to the virtual network.

As further shown in FIG. 1, virtual network device 100 also includes a receive circuit 120 that has a number of local physical ports 122 that each output frames of data to a local network device, such as a router/switch, and a number of network

physical ports 124 that each receive frames of data from the virtual network. In one embodiment, one or more of the local physical ports 112 and 122 can be shared between the transmit and receive circuits 110 and 120, and one or more of the network physical ports 114 and 124 can be shared between the transmit and receive
5 circuits 110 and 120.

In addition, virtual network device 100 includes a shared memory 130 that is coupled to both the transmit circuit 110 and the receive circuit 120. Shared memory 130 includes a transmit queue that temporarily stores frames of data to be output to the virtual network, and a receive queue that temporarily stores frames of data
10 received from the virtual network.

FIG. 2A shows a block diagram that illustrates an example of a transmit circuit 200 in accordance with the present invention. As shown in FIG. 2A, transmit circuit 200 includes a local physical port 210, a framing circuit 212 that is coupled to local physical port 210, and a number of transmit virtual ports vPORTa1-vPORTan that are
15 coupled to framing circuit 212.

Each transmit virtual port vPORTa, in turn, includes a transmit queue and a transmit frame formatting circuit. In addition, transmit circuit 200 also includes a transmit virtual switch 214 that is coupled to each of the transmit virtual ports vPORTa, and a network physical port 216 that is coupled to transmit virtual switch
20 214.

FIG. 3A shows a flow chart that illustrates an example of a method 300 of operating transmit circuit 200 in accordance with the present invention. As shown in FIG. 3A, method 300 begins at 310 with framing circuit 212 receiving a series of input frames from local physical port 210. Method 300 next moves to 312 to
25 examine the series of input frames to determine a frame type (e.g., STB, PC, video) for each input frame, and then moves to 314 to determine a virtual exit device that is associated with each input frame based on the frame type. Each virtual exit device, in turn, has a number of receive virtual ports.

Following this, method 300 moves to 316 where framing circuit 212 encapsulates the series of input frames to form a number of first encapsulated (FE) frames. The FE frames have headers that identify the virtual exit devices that are associated with the series of input frames.

- 5 After this, method 300 moves to 318 where the transmit virtual ports vPORTa1-vPORTan determine the next hops in a virtual network for the FE frames based on the virtual exit devices in the headers of the FE frames. Next, method 300 moves to 320 where the transmit virtual ports vPORTa1-vPORTan encapsulate the FE frames to form second encapsulated (SE) frames. Each SE frame has a header that
- 10 identifies a next hop of the SE frame based on the next hop of a FE frame. The header also identifies the receive virtual port of the associated virtual exit device of an input frame. In addition, the transmit virtual ports occupy a first portion of a shared memory.

- Following this, method 300 moves to 322 where transmit virtual switch 214
- 15 cycles through the transmit virtual ports vPORTa1-vPORTan sequentially forwarding a SE frame from each transmit virtual port vPORTa in a fixed repeating order to output a sequence of SE frames. For example, virtual switch 214 can output a sequence of SE frames where the first SE frame is from vPORT1, the second frame is from vPORT2, the third frame is from vPORT3, and a fourth frame is again from
- 20 vPORT1.

- If a transmit virtual port vPORTa is empty or partially full, then no frame is generated. For example, if transmit virtual port vPORT2 is empty, then network physical port 216 outputs a frame sequence that includes frame 1, no frame, frame 3. Method 300 next moves to 324 where network physical port 216 transmits the
- 25 sequence of SE frames onto the virtual network.

FIG. 3B shows a flow chart that illustrates an example of a method 350 of operating transmit circuit 200 in accordance with an alternate embodiment of the present invention. Method 350 is similar to method 300 and, as a result, utilizes the

same reference numerals to designate the elements that are common to both methods.

As shown in FIG. 3B, method 350 first diverges from method 300 at 352 where virtual switch 214 determines whether a full signal has been received from any of the transmit virtual ports vPORTa. The full signal indicates that the SE frame in a transmit virtual port vPORTa is ready to be transmitted. When virtual switch 214 detects a full signal from a transmit virtual port vPORTa, method 350 moves to 354 where virtual switch 214 forwards the SE frame from the transmit virtual port vPORTa that output the full signal to network physical port 216.

For example, virtual switch 214 could sequentially receive a full signal from transmit virtual port vPORTa1, transmit virtual port vPORTa2, and transmit virtual port vPORTa3. In this case, virtual switch 214 outputs a sequence of SE frames where the first SE frame is from transmit virtual port vPORT1, the second frame is from transmit virtual port vPORT2, and the third frame is from transmit virtual port vPORT3.

Alternately, one of the sources (e.g., the STB, PC, video sources) can have a data rate that is much faster than the data rates of the other sources (e.g., STB, PC, video sources) which, in turn, causes one transmit virtual port vPORTa to output a full signal much more frequently than the other transmit virtual ports vPORTa.

For example, if network physical port 216 transmits frames at a frame rate of five frames per second, transmit virtual port vPORTa2 outputs frames at a rate that is 3X faster than each of the frame rates of transmit virtual ports vPORTa1 and vPORTa3, transmit virtual port vPORTa2 signals full three times before the other ports, and transmit virtual port vPORTa1 signals before vPORTa3 signals, then virtual switch 214 forwards a sequence of frames that includes a first frame from the transmit virtual port vPORT2, a second frame from transmit virtual port vPORT2, a third frame from transmit virtual port vPORT2, a fourth frame from transmit virtual port vPORT1, and a fifth frame from transmit virtual port vPORT3.

In addition to a first-in first-out approach where the order of receiving a full signal determines the order that a SE frame is output from a transmit virtual port vPORTa by virtual switch 214, the transmit virtual ports vPORTa-vPORTan can alternately include a priority scheme that allows frames to be forwarded from a transmit virtual port vPORTa to a network physical port in any amount and in any order.

Referring back to FIG. 3B, after virtual switch 214 forwards the SE frame from the transmit virtual port vPORTa that output the full signal to network physical port 216, method 350 moves to 356 where network physical port 216 transmits the SE frame. In method 300, the frame to be output is predictable, whereas the frame to be output in method 350 is not predictable, although a priority scheme provides a level of predictability.

Referring again to the FIG. 2A example, framing circuit 212 includes a virtual switch 220 and a framer 222 that is coupled to virtual switch 220. Virtual switch 220 detects a type of input frame (e.g., STB, PC, video), determines a route for a frame to a virtual port vPORTa that corresponds with the type of frame from a static forwarding table, and outputs the frame towards the virtual port vPORTa.

In the present example, virtual switch 220 receives the STB frame transmitted by a local source router/switch, and detects the received frame to be a STB frame from the source and/or destination MAC address in the STB frame. Switch 220 then outputs the STB frame on a first virtual port line P1 routed towards virtual port vPORTa1, which was pre-selected to receive STB frames.

Similarly, virtual switch 220 receives the PC frame transmitted by the local source router/switch, and detects the received frame to be a PC frame from the source and/or destination MAC address in the PC frame. Switch 220 then outputs the PC frame on a second virtual port line P2 routed towards virtual port vPORTa2, which was pre-selected to receive PC frames.

Virtual switch 220 also receives the video frame transmitted by the local router/switch, detects the received frame to be a video frame from the source and/or

destination MAC address in the video frame, and then outputs the video frame on a third virtual port line P3 routed towards virtual port vPORTa3, which was pre-selected to receive video frames.

5 Framer 222 receives the STB frame on virtual port line P1, encapsulates the STB frame to form a first encapsulated (FE) STB frame, and then forwards the FE STB frame to the transmit queue of virtual port vPORTa1. Similarly, framer 222 receives the PC frame on virtual port line P2, encapsulates the PC frame to form a first encapsulated (FE) PC frame, and then forwards the FE PC frame to the transmit queue of virtual port vPORTa2. Framer 222 also receives the video frame on virtual
10 port line P3, encapsulates the video frame to form a first encapsulated (FE) video frame, and then forwards the FE video frame to the transmit queue of virtual port vPORTa3.

 Framer 222 can utilize a conventional protocol, such as the provider backbone bridge-traffic engineering (PBB-TE) protocol or the transport multiprotocol label
15 switching (T-MPLS) protocol, to generate the encapsulated frames. In addition, the FE STB frame, the FE PC frame, and the FE video frame each has a header which has a number of fields that include an identification of the virtual exit device.

 For example, the header of a FE frame can include an exit address field for the MAC address of the virtual exit device, an I-Tag field, or a similar field. The
20 header can also include other fields, such as the MAC address of the virtual entry device. In the present example, the MAC address of the virtual exit device is administratively provided to the virtual entry device.

 The frame formatting circuit in virtual port vPORTa1 of transmit circuit 200 receives the FE STB frame, determines a next hop in the virtual network for the FE
25 STB frame from a static forwarding table based on the identification of the virtual exit device, such as the MAC address of the virtual exit device, in the header of the FE STB frame, and encapsulates the FE STB frame to form a second encapsulated (SE) STB frame.

Similarly, the frame formatting circuit in virtual port vPORTa2 of transmit circuit 200 receives the FE PC frame, determines a next hop in the virtual network for the FE PC frame from the static forwarding table based on the identification of the virtual exit device, such as the MAC address of the virtual exit device, in the header of the FE PC frame, and encapsulates the FE PC frame to form a second encapsulated (SE) PC frame.

In addition, the frame formatting circuit in virtual port vPORTa3 of transmit circuit 200 receives the FE video frame, determines a next hop in the virtual network for the FE video frame from the static forwarding table based on the identification of the virtual exit device, such as the MAC address of the virtual exit device, in the header of the FE video frame, and encapsulates the FE video frame to form a second encapsulated (SE) video frame.

The SE STB frame, the SE PC frame, and the SE video frame each includes a header with a next hop field that identifies the MAC address of the next hop in the virtual network, a source field Src_vID that identifies the virtual port number of the virtual entry device, and a destination field Dst_vID that identifies a virtual port number of the virtual exit device that corresponds with the virtual port number of the virtual entry device. In the present example, the source field Src_vID for the SE STB frame is virtual port vPORTa1. Other fields, such as a last hop field, can also be included.

Further, virtual switch 214 cycles through the virtual ports vPORTa1-vPORTan sequentially forwarding a second encapsulated (SE) frame from each virtual port vPORTa to output a series of SE frames to physical port 216. In the present example, switch 214 forwards a SE STB frame from virtual port vPORTa1 to physical port 216, followed by forwarding a SE PC frame from virtual port vPORTa2 to physical port 216, followed by forwarding a SE video frame from virtual port vPORTa3 to physical port 216, followed by forwarding a SE STB frame from virtual port vPORTa1 to physical port 216, and continuing in the same manner, with physical port 216 outputting the frames. Although FIG. 2 illustrates transmit circuit 200 as receiving and operating

with input from a single local router/switch, transmit circuit 200 can alternately receive and operate with input from multiple router/switches.

FIG. 2B shows a block diagram that illustrates an example of a transmit circuit 250 in accordance with the present invention. Transmit circuit 250 is similar to
5 transmit circuit 200 and, as a result, utilizes the same reference numerals to designate the elements that are common to both transmit circuit 200 and transmit circuit 250.

As shown in FIG. 2B, transmit circuit 250 differs from transmit circuit 200 in that transmit circuit 250 includes a first network physical port 216A and a second
10 network physical port 216B, both of which are coupled to virtual switch 214. In addition, virtual switch 214 provides a continuous connection between transmit virtual port vPORTa1 and network physical port 216A. Further, an additional transmit virtual port vPORTa4 is shown.

Transmit circuit 250 operates substantially the same as transmit circuit 200,
15 except that one or more of the sources (e.g., STB, PC, or video source) outputs frames of data at a frame rate that is greater than the maximum frame rate of the network physical ports 216A and 216B. For example, each of the network physical ports 216A and 216B can have a maximum frame rate of five frames per second.

In the FIG. 2B example, a set-top box outputs seven STB frames per second,
20 while a personal computer outputs two PC frames per second and a video device outputs one video frame per second. (The numbers cited are for illustration purposes only.) As shown in FIG. 2B, five of the seven STB frames are transmitted from network physical port 216A, while the remaining two STB frames, two PC frames, and one video frame are transmitted from network physical port 216B in the
25 manner illustrated by methods 300 and 350. One the advantages of transmit circuit 250 is that transmit circuit 250 can handle incoming frame rates that are greater than the maximum frame rate of the network physical ports.

FIG. 4 shows a block diagram that illustrates an example of a transmit circuit 400 in accordance with an alternate embodiment of the present invention. Transmit

circuit 400 is similar to transmit circuit 200 and, as a result, utilizes the same reference numerals to designate the structures that are common to both circuits.

As shown in FIG. 4, transmit circuit 400 differs from transmit circuit 200 in that framing circuit 212 of transmit circuit 400 utilizes a serial-to-serial framer 410
5 followed by a serial-to-parallel virtual switch 412 that is coupled to the virtual ports vPORTa1-vPORTan in lieu of virtual switch 220 followed by framer 222.

In a further alternate embodiment, framer 410 and virtual switch 412 of transmit circuit 400 can be physically separated, with framer 410 being incorporated into a local router/switch.

10 FIG. 5 shows a block diagram that illustrates an example of a transmit circuit 500 in accordance with the present invention. Transmit circuit 500 is similar to transmit circuit 400 and, as a result, utilizes the same reference numerals to designate the structures that are common to both circuit 400 and circuit 500. As shown in the example illustrated in FIG. 5, a local framer router/switch 510 is utilized
15 with transmit circuit 500 in lieu of a local router/switch that receives and outputs STB, PC, and video frames.

FIG. 6 shows a block diagram that illustrates an example of a receive circuit 600 in accordance with the present invention. As shown in FIG. 6, receive circuit 600 includes a network physical port 610 and a receive virtual switch 612 that is coupled
20 to network physical port 610. Receive circuit 600 also includes a number of receive virtual ports vPORTb1-vPORTbn that are coupled to switch 612. Each receive virtual port vPORTb, in turn, includes a receive queue and a receive frame formatting circuit. Receive circuit 600 further includes a de-framing circuit 614 that is coupled to each of the receive virtual ports vPORTb, and a local physical port 616 that is coupled to
25 de-framing circuit 614.

FIG. 7 shows a flow chart that illustrates an example of a method 700 of operating receive circuit 600 in accordance with the present invention. As shown in FIG. 7, method 700 begins at 710 with network physical port 610 receiving a series

of third encapsulated (TE) frames from the virtual network. The TE frames have headers that include next hop addresses and receive virtual port identifiers.

Next, method 700 moves to 712 where network physical port 610 examines the TE frames to determine next hop addresses, and compares the next hop
5 addresses to a stored address. After this, method 700 moves to 714 where network physical port 610 forwards the TE frames with matching next hop addresses as matching encapsulated (ME) frames. In addition, port 610 drops the received TE frame when the identity of the next hop address does not match the stored address.

After this, method 700 moves to 716 where receive virtual switch 612
10 switchably passes the ME frames based on the receive virtual port identifiers in the headers of the ME frames. Method 700 then moves to 718 where the receive virtual ports vPORTb1-vPORTbn unpack the switchably-passed ME frames to extract fourth encapsulated frames from the switchably-passed ME frames such that each receive virtual port vPORTb unpacks a ME frame to extract a fourth encapsulated frame.
15 The receive virtual ports occupy a second portion of the shared memory.

Following this, method 700 moves to 720 where de-framing circuit 614 unpacks the fourth encapsulated frames to extract the original STB, PC, and video input frames from the fourth encapsulated frames. The original STB, PC, and video input frames, which are from a remote router/switch, have a number of frame types.
20 Further, each input frame has a header that identifies a destination router/switch. Method 700 then moves to 722 where de-framing circuit 614 forwards the STB, PC, and video frames to local physical port 616, which outputs the original STB, PC, and video frames to a local router/switch.

In the present example, virtual switch 612 receives a ME STB frame from
25 network physical port 610, and determines that the destination virtual port is virtual port vPORTb1 from the destination virtual port number Dst_VID in the header of the ME STB frame. In addition, switch 612 determines a route to virtual port vPORTb1 from the static forwarding table, and then outputs the ME STB frame on a first virtual port line routed towards virtual port vPORTb1.

Similarly, virtual switch 612 receives a ME PC frame from network physical port 610, and determines that the destination virtual port is virtual port vPORTb2 from the destination virtual port number Dst_vID in the header of the ME PC frame. Further, switch 612 determines a route to virtual port vPORTb2 from the static forwarding table, and then outputs the ME PC frame on a second virtual port line routed towards virtual port vPORTb2.

In addition, virtual switch 612 receives a ME video frame from network physical port 610, and determines that the destination virtual port is virtual port vPORTb3 from the destination virtual port number Dst_vID in the header of the ME video frame. Switch 612 determines a route to virtual port vPORTb3 from the static forwarding table, and then outputs the ME video frame on a third virtual port line routed towards virtual port vPORTb3.

The virtual ports vPORTb1-vPORTbn receive the ME frames, and unpack the ME frames to extract the fourth encapsulated frames, such as a fourth encapsulated STB frame, a fourth encapsulated PC frame, and a fourth encapsulated video frame, from the ME frames. In the FIG. 6 example, the receive queue of a first virtual port vPORTb1 receives a ME STB frame, while the frame formatting circuit of virtual port vPORTb1 unpacks the ME STB frame to extract the fourth encapsulated STB frame, which has a header that includes the identity of the virtual exit device.

Similarly, the receive queue of a second virtual port vPORTb2 receives a ME PC frame, while the frame formatting circuit of virtual port vPORTb2 unpacks the ME PC frame to extract the fourth encapsulated PC frame, which has a header that includes the identity of the virtual exit device. In addition, the receive queue of a third virtual port vPORTb3 receives a ME video frame, while the frame formatting circuit of virtual port vPORTb3 unpacks the ME video frame to extract the fourth encapsulated video frame, which has a header that includes the identity of the virtual exit device.

De-framing circuit 614 receives the plurality of fourth encapsulated frames, and extracts the original STB, PC, and video input frames from the fourth

encapsulated frames. The input frames have a number of frame types, e.g., STB, PC, video. Each input frame has a header that includes the identity of a destination router/switch. For each received fourth encapsulated frame, de-framing circuit 614 unpacks a fourth encapsulated frame to extract an input frame, determines an
5 identity of a destination router/switch from the header of the input frame, and outputs the input frame to local physical port 616, which outputs the input frame to the destination router/switch.

As shown in FIG. 6, de-framing circuit 614 includes a de-framer 620 and a virtual switch 622 that is coupled to de-framer 620. In operation, de-framer 620
10 receives the fourth encapsulated frames from the plurality of receive virtual ports vPORTb1-vPORTbn, and unpacks the fourth encapsulated frames to extract the original input frames, e.g., the STB frame, the PC frame, and the video frame, and forwards the STB frame, the PC frame, and the video frame to virtual switch 622.

In the FIG. 6 example, de-framer 620 receives the fourth encapsulated STB
15 frame from receive virtual port vPORTb1, unpacks the fourth encapsulated frame to extract the STB frame, and forwards the STB frame to virtual switch 622. Similarly, de-framer 620 receives the fourth encapsulated PC frame from receive virtual port vPORTb2, unpacks the fourth encapsulated frame to extract the PC frame, and forwards the PC frame to virtual switch 622. In addition, de-framer 620 receives the
20 fourth encapsulated video frame from receive virtual port vPORTb3, unpacks the fourth encapsulated frame to extract the video frame, and forwards the video frame to virtual switch 622. De-framer 620 can utilize the same or different protocol as framer 222.

Virtual switch 622 cycles through the outputs of de-framer 620 sequentially
25 receiving and forwarding output frames to local physical port 616. In the present example, virtual switch 622 receives the STB frame from de-framer 620, detects the MAC address of a destination router/switch, and outputs the STB frame to local physical port 616. Similarly, virtual switch 622 receives the PC frame from de-framer 620, detects the MAC address of a destination router/switch, and outputs the PC

frame to local physical port 616. In addition, virtual switch 622 receives the video frame from de-framer 620, detects the MAC address of a destination router/switch, and outputs the video frame to local physical port 616. Local physical port 616, in turn, outputs the frames to a local router/switch.

5 The FIG. 6 example illustrates de-framing circuit 614 with a parallel-to-parallel de-framer 620 followed by a parallel-to-serial virtual switch 622. De-framing circuit 614 can be alternately realized with other circuit arrangements. For example, de-framing circuit 614 can be implemented with a serial-to-parallel virtual switch that is coupled to the virtual ports vPORTb1-vPORTbn followed by a serial-to-serial framer.

10 FIG. 8 shows a block diagram that illustrates an example of a receive circuit 800 in accordance with an alternate embodiment of the present invention. Receive circuit 800 is similar to receive circuit 600 and, as a result, utilizes the same reference numerals to designate the structures that are common to both circuits.

 As shown in FIG. 8, receive circuit 800 differs from receive circuit 600 in that
15 framing circuit 614 of receive circuit 800 includes a parallel-to-serial virtual switch 810 that is coupled to the virtual ports vPORTb1-vPORTbn, followed by a serial-to-serial de-framer 812. The implementations of framing circuit 212 and de-framing circuit 614 can be interchanged. For example, virtual network device 100 can utilize framing circuit 212 implemented with virtual switch 220 and framer 222, while de-
20 framing circuit 614 can be implemented with virtual switch 810 and de-framer 812.

 In a further alternate embodiment, virtual switch 810 and de-framer 812 can be physically separated, with de-framer 812 being incorporated into a local router/switch.

 FIG. 9 shows a block diagram that illustrates an example of a receive circuit
25 900 in accordance with the present invention. Receive circuit 900 is similar to receive circuit 800 and, as a result, utilizes the same reference numerals to designate the structures that are common to both circuit 800 and circuit 900. As shown in the example illustrated in FIG. 9, a local de-framer router/switch 910 is utilized in receive circuit 900 in lieu of a local router switch.

One of the advantages of the present invention is that the present invention combines multiple streams of STB, PC, and video frames into a single virtual frame stream via a number of virtual ports which, in turn, effectively increases the number of available physical ports.

5 Reference has now been made in detail to the various embodiments of the present disclosure, examples of which are illustrated in the accompanying drawings. While described in conjunction with the various embodiments, it will be understood that these various embodiments are not intended to limit the present disclosure. On the contrary, the present disclosure is intended to cover alternatives, modifications
10 and equivalents, which may be included within the scope of the present disclosure as construed according to the claims.

 Furthermore, in the preceding detailed description of various embodiments of the present disclosure, numerous specific details are set forth in order to provide a thorough understanding of the present disclosure. However, it will be recognized by
15 one of ordinary skill in the art that the present disclosure may be practiced without these specific details or with equivalents thereof. In other instances, well-known methods, procedures, components, and circuits have not been described in detail so as not to unnecessarily obscure aspects of various embodiments of the present disclosure.

20 It is noted that although a method may be depicted herein as a sequence of numbered operations for clarity, the numbering does not necessarily dictate the order of the operations. It should be understood that some of the operations may be skipped, performed in parallel, or performed without the requirement of maintaining a strict order of sequence.

25 The drawings showing various embodiments in accordance with the present disclosure are semi-diagrammatic and not to scale and, particularly, some of the dimensions are for the clarity of presentation and are shown exaggerated in the drawing Figures. Similarly, although the views in the drawings for the ease of description generally show similar orientations, this depiction in the figures is

arbitrary for the most part. Generally, the various embodiments in accordance with the present disclosure can be operated in any orientation.

Some portions of the detailed descriptions are presented in terms of procedures, logic blocks, processing, and other symbolic representations of operations on data bits within a computer memory. These descriptions and representations are used by those skilled in the data processing arts to effectively convey the substance of their work to others skilled in the art.

In the present disclosure, a procedure, logic block, process, or the like, is conceived to be a self-consistent sequence of operations or instructions leading to a desired result. The operations are those utilizing physical manipulations of physical quantities. Usually, although not necessarily, these quantities take the form of electrical or magnetic signals capable of being stored, transferred, combined, compared, and otherwise manipulated in a computing system. It has proven convenient at times, principally for reasons of common usage, to refer to these signals as transactions, bits, values, elements, symbols, characters, samples, pixels, or the like.

It should be borne in mind, however, that all of these and similar terms are to be associated with the appropriate physical quantities and are merely convenient labels applied to these quantities. Unless specifically stated otherwise as apparent from the following discussions, it is appreciated that throughout the present disclosure, discussions utilizing terms such as "generating," "determining," "assigning," "aggregating," "utilizing," "virtualizing," "processing," "accessing," "executing," "storing," or the like, refer to the action and processes of a computer system, or similar electronic computing device or processor.

The computing system, or similar electronic computing device or processor manipulates and transforms data represented as physical (electronic) quantities within the computer system memories, registers, other such information storage, and/or other computer readable media into other data similarly represented as

physical quantities within the computer system memories or registers or other such information storage, transmission or display devices.

The technical solutions in the embodiments of the present application have been clearly and completely described in the prior sections with reference to the
5 drawings of the embodiments of the present application. It should be noted that the terms "first," "second," and the like in the description and claims of the present invention and in the above drawings are used to distinguish similar objects and are not necessarily used to describe a specific sequence or order. It should be understood
10 that these numbers may be interchanged where appropriate so that the embodiments of the present invention described herein can be implemented in orders other than those illustrated or described herein.

The functions described in the method of the present embodiment, if implemented in the form of a software functional unit and sold or used as a standalone product, can be stored in a computing device readable storage medium.
15 Based on such understanding, a portion of the embodiments of the present application that contributes to the prior art or a portion of the technical solution may be embodied in the form of a software product stored in a storage medium, including a plurality of instructions for causing a computing device (which may be a personal computer, a server, a mobile computing device, or a network device, and so on) to
20 perform all or part of the steps of the methods described in various embodiments of the present application. The foregoing storage medium includes: a USB drive, a portable hard disk, a read-only memory (ROM), a random-access memory (RAM), a magnetic disk, an optical disk, and the like, which can store program code.

The various embodiments in the specification of the present application are
25 described in a progressive manner, and each embodiment focuses on its difference from other embodiments, and the same or similar parts between the various embodiments may be referred to another case. The described embodiments are only a part of the embodiments, rather than all of the embodiments of the present application. All other embodiments obtained by a person of ordinary skill in the art

based on the embodiments of the present application without departing from the inventive skills are within the scope of the present application.

The above description of the disclosed embodiments enables a person skilled in the art to make or use the present application. Various modifications to these
5 embodiments are obvious to a person skilled in the art, and the general principles defined herein may be implemented in other embodiments without departing from the spirit or scope of the present application. Therefore, the present application is not limited to the embodiments shown herein, but the broadest scope consistent with the principles and novel features disclosed herein.

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