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(54) Title: SIGNAL PROCESSING SYSTEM

(57) Abstract

A signal processing system contains a source apparatus coupled to a destination apparatus via bus operable according to a time-slot allocation protocol. The source apparatus is arranged for supplying a sequence of packets, each comprising a time-stamp via the bus to the destination apparatus. The destination apparatus comprises a clock and is arranged for receiving the packets, for detecting when the time-value of the clock corresponds to the time-stamp in a particular packet and for thereupon presenting data from that particular packet at an output operable according to a time-slot allocation protocol. The source apparatus is arranged for supplying a first and a second part of at least one of the packets in different time-slots, the destination apparatus being arranged for presenting data from the first and second part together upon detecting when the time-value of the clock corresponds to the time-stamp in at least one of the packets.

