

COMMUNICATION APPARATUS AND SIGNAL SAMPLING METHOD

ABSTRACT

A communication apparatus and a signal sampling method are provided. The communication apparatus includes a clock control circuit, a data sampling circuit, and a clock recovery circuit. The clock control circuit is configured to determine a sampling clock deviation of the data sampling circuit, and generate a first clock control signal based on the sampling clock deviation, where the first clock control signal is used to adjust a sampling clock of the data sampling circuit. The clock recovery circuit is configured to adjust the sampling clock of the data sampling circuit based on the first clock control signal, and send a clock signal to the data sampling circuit. The data sampling circuit is configured to sample an input analog signal based on the clock signal. By using the communication apparatus, the sampling clock of the data sampling circuit can be locked at an optimal sampling moment, so that a digital signal obtained through sampling can retain wanted information in a raw analog signal without oversampling the raw analog signal, and complexity of the data sampling circuit is not increased even in a high-bandwidth scenario.

FIG. 2