

SEMICONDUCTOR STORAGE DEVICE AND INFORMATION APPARATUS

ABSTRACT OF THE DISCLOSURE

A semiconductor storage device includes: a memory array including a plurality of memory cells; a reference array including a plurality of reference cells; a decoder section for selecting a memory cell from the memory cells and a reference cell from the reference cells based on address information; and a comparison/output section for comparing a read voltage level from the memory cell selected by the decoder section and a read voltage level from the reference cell selected by the decoder section so as to output a result of the comparison in the form of data, wherein the decoder section simultaneously outputs a selection signal to a word line of the memory cell and a selection signal to a word line of the reference cell.

Figure 1



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