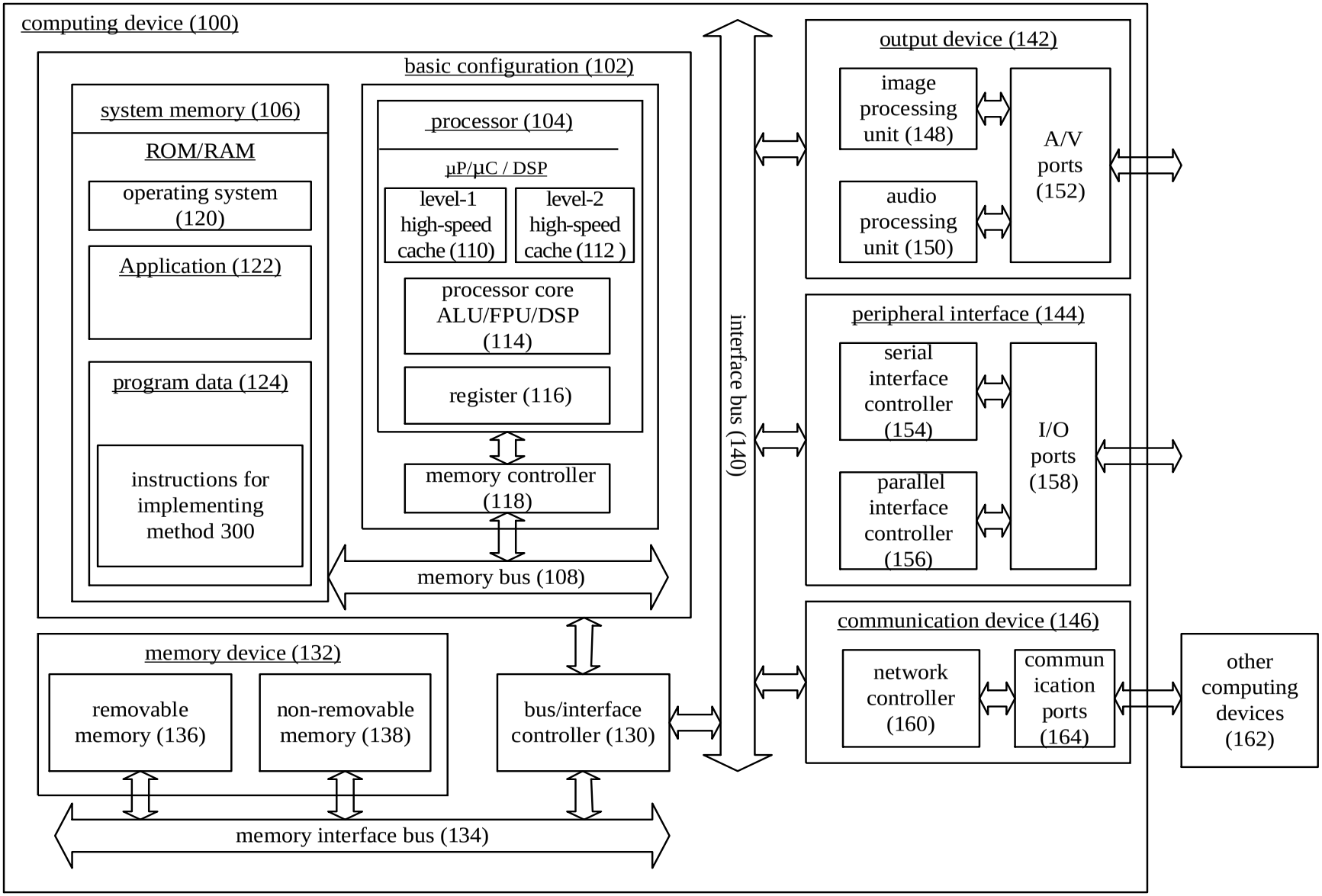

computing device (100)
basic configuration (102)
system memory (106)
ROM/RAM
operating system (120)
Application (122)
program data (124)
instructions for implementing method 300
processor (104)
µP/µC / DSP
level-1 high-speed cache (110)
level-2 high-speed cache (112)
processor core ALU/FPU/DSP (114)
register (116)
memory controller (118)
memory bus (108)
memory device (132)
removable memory (136)
non-removable memory (138)
bus/interface controller (130)
memory interface bus (134)
interface bus (140)
output device (142)
image processing unit (148)
audio processing unit (150)
A/V ports (152)
peripheral interface (144)
serial interface controller (154)
parallel interface controller (156)
I/O ports (158)
communication device (146)
network controller (160)
communication ports (164)
other computing devices (162)

Fig.1

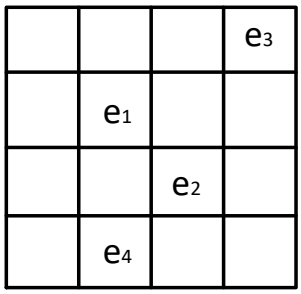


Fig. 2A

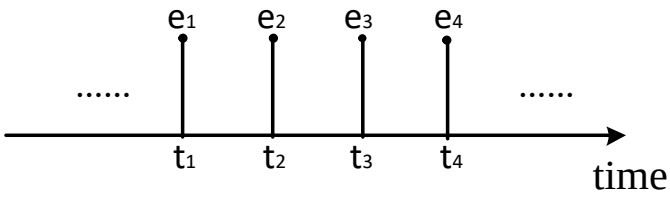


Fig. 2B

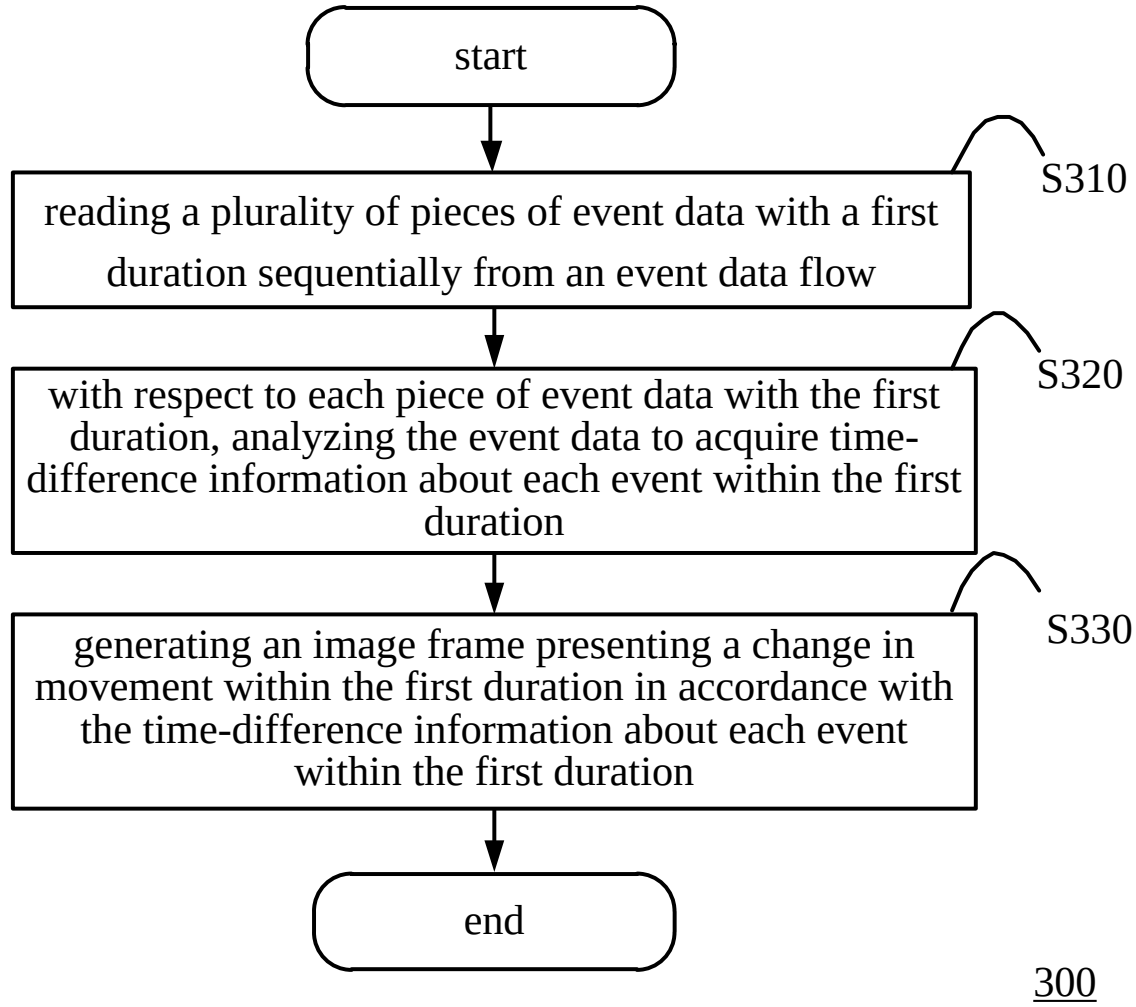

start
reading a plurality of pieces of event data with a first duration sequentially from an event data flow
S310
with respect to each piece of event data with the first duration, analyzing the event data to acquire time-difference information about each event within the first duration
S320
generating an image frame presenting a change in movement within the first duration in accordance with the time-difference information about each event within the first duration
S330
end
300

Fig. 3

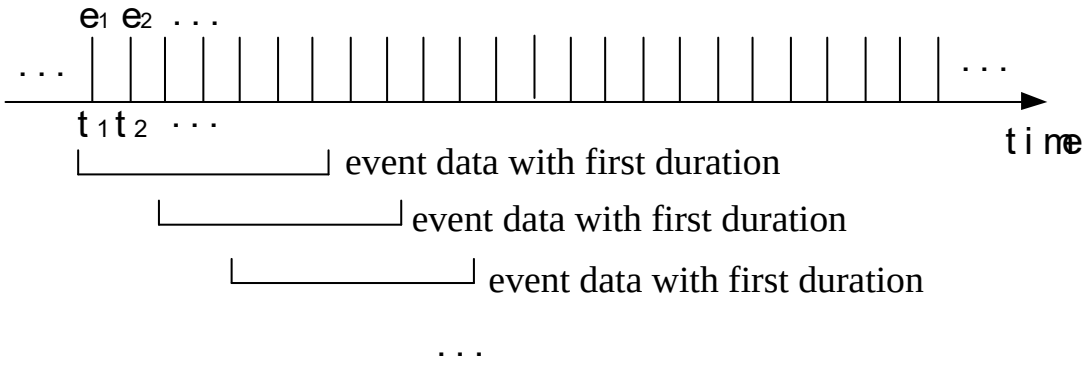

e1 e2 . . .
. . .
. . .
t1 t2 . . .
time
event data with first duration
event data with first duration
event data with first duration
. . .

Fig. 4

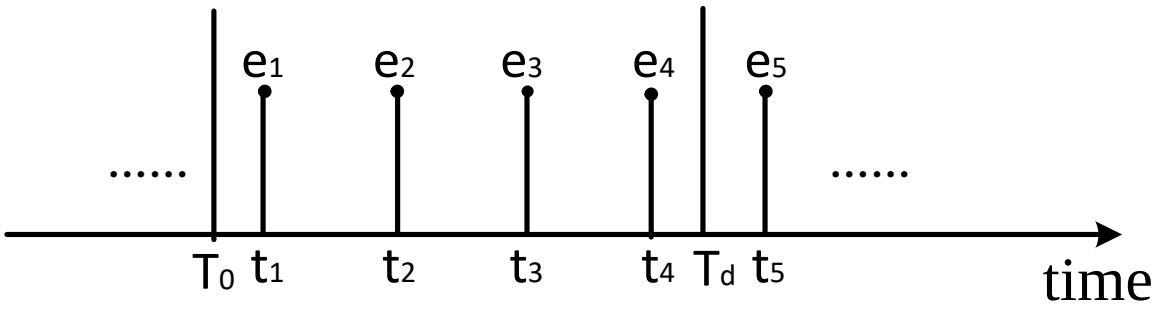
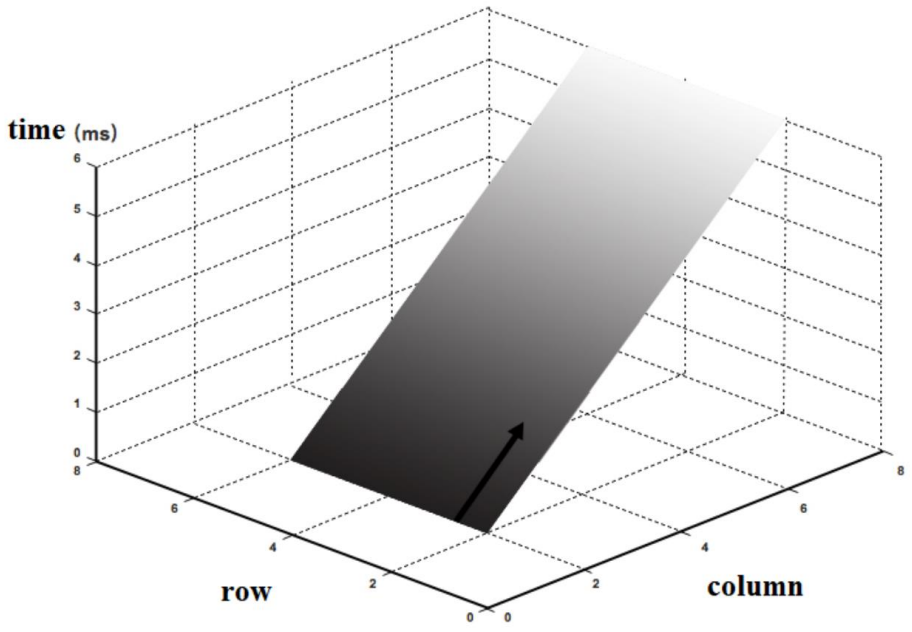
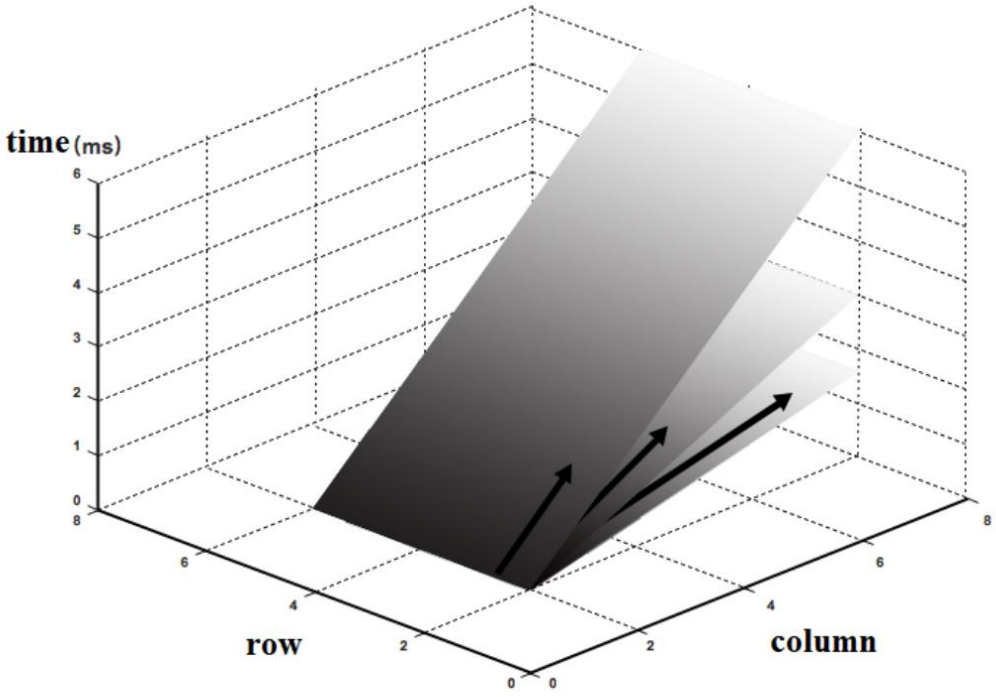


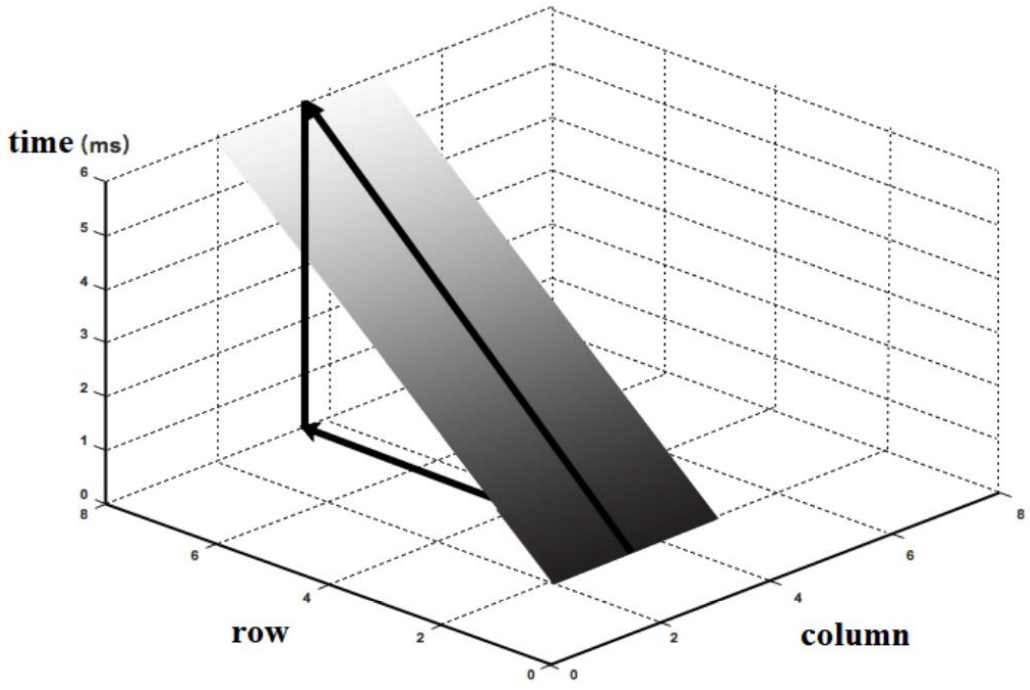
Fig. 5


time (ms)
6
5
4
3
2
1
0
8
6
4
2
0
row
0
2
4
6
8
column

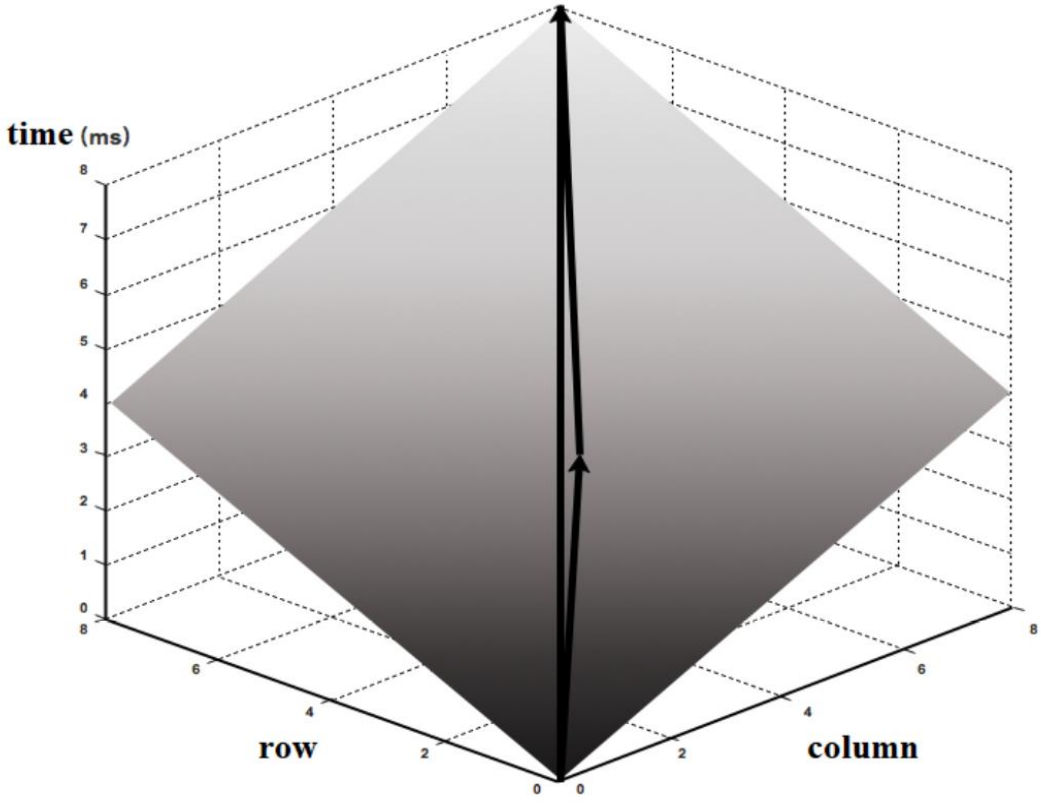
(1)


time (ms)
6
5
4
3
2
1
0
8
6
4
2
0
row
0
2
4
6
8
column

(2)



(3)



(4)

Fig. 6