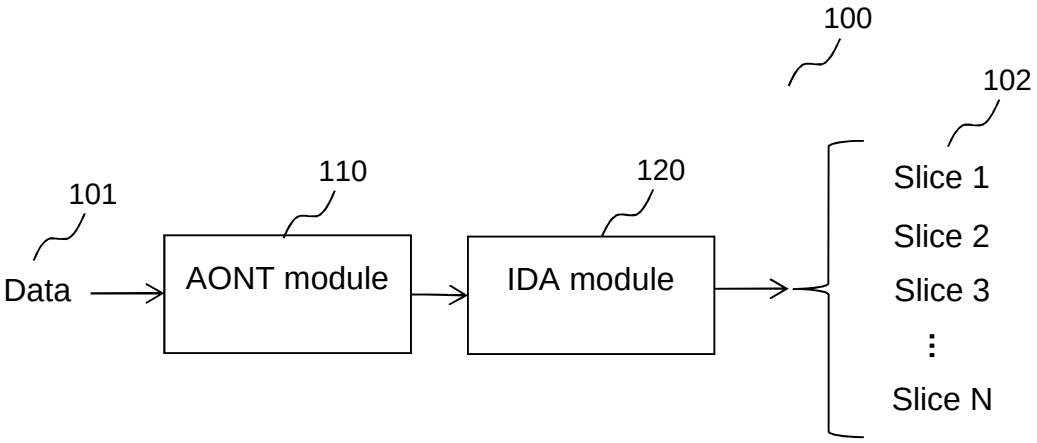

Figure 1

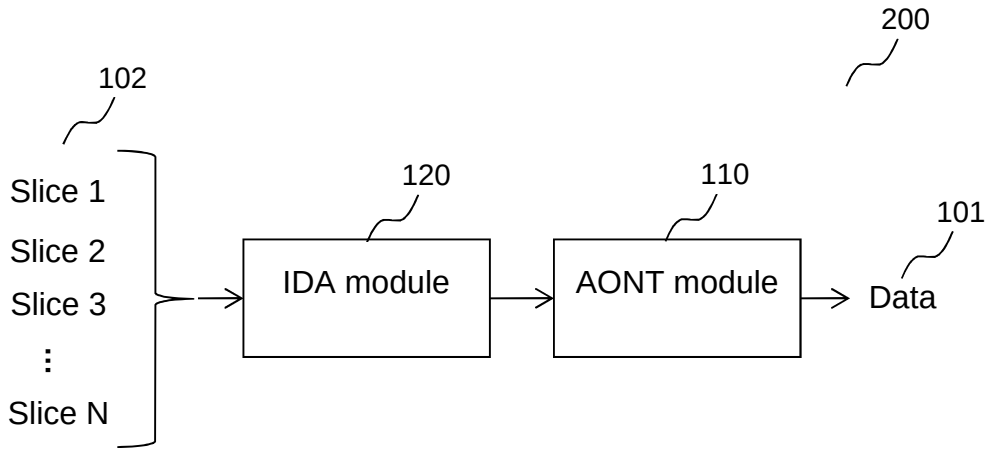

Figure 2

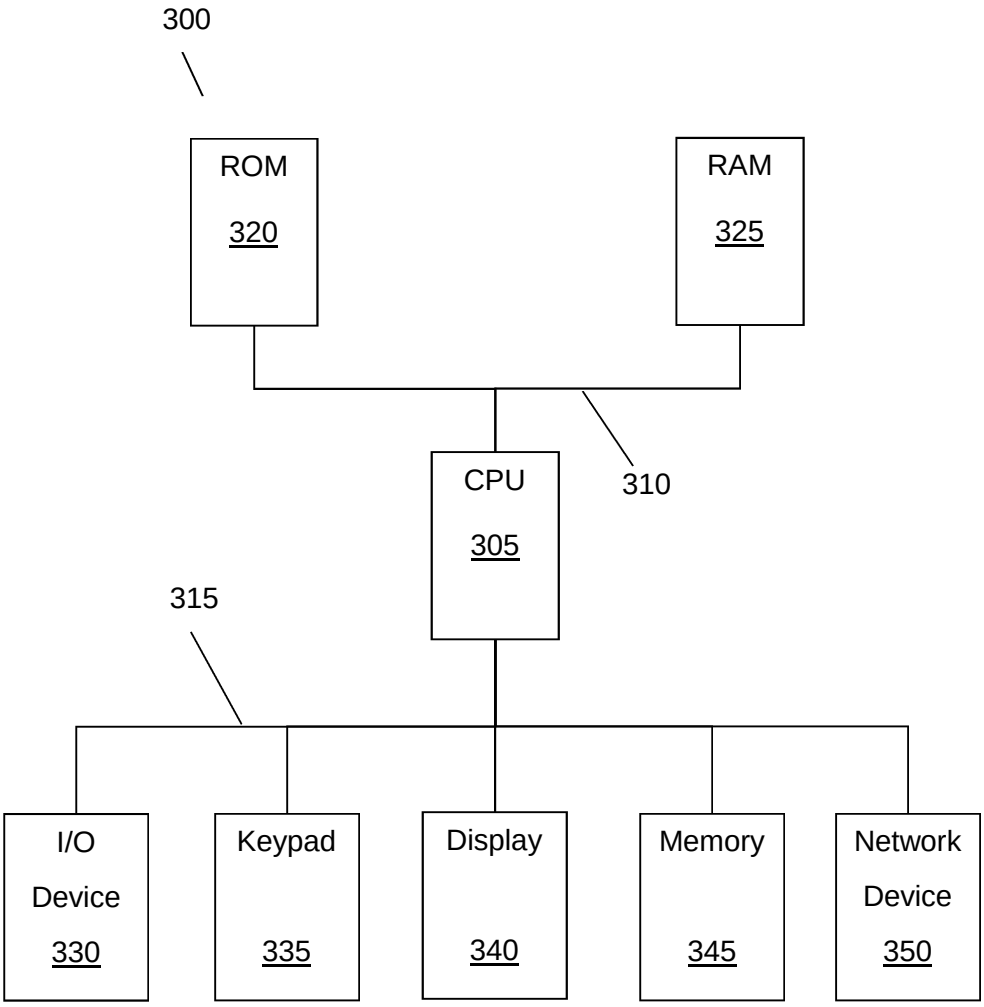
300
ROM
320
RAM
325
CPU
305
310
315
I/O
Device
330
Keypad
335
Display
340
Memory
345
Network
Device
350

Figure 3

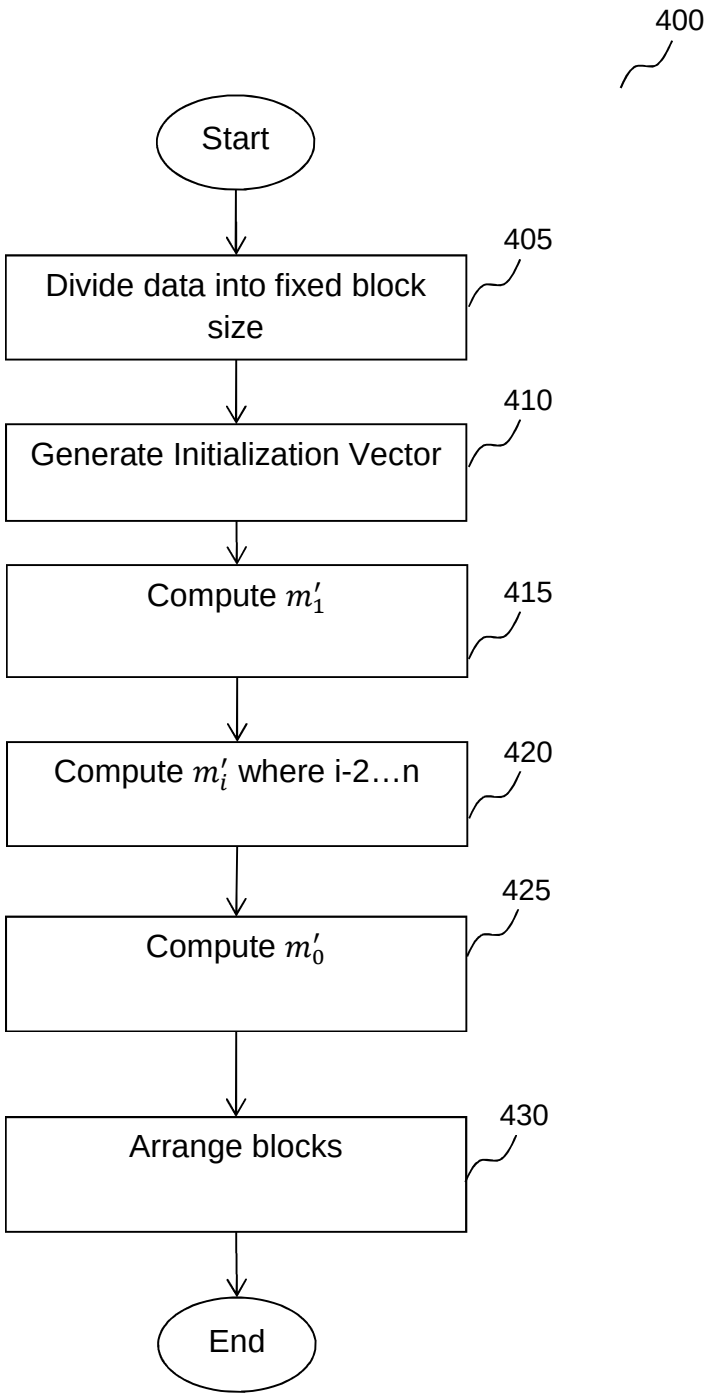
400
Start
405
Divide data into fixed block size
410
Generate Initialization Vector
415
Compute m_1'
420
Compute m_i' where i-2…n
425
Compute m_0'
430
Arrange blocks
End

Figure 4.1

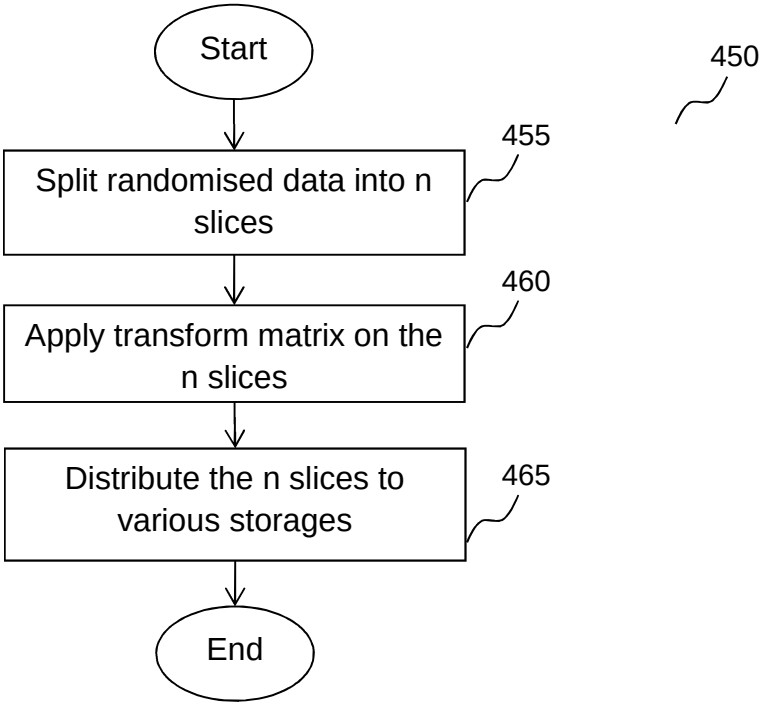

Figure 4.2

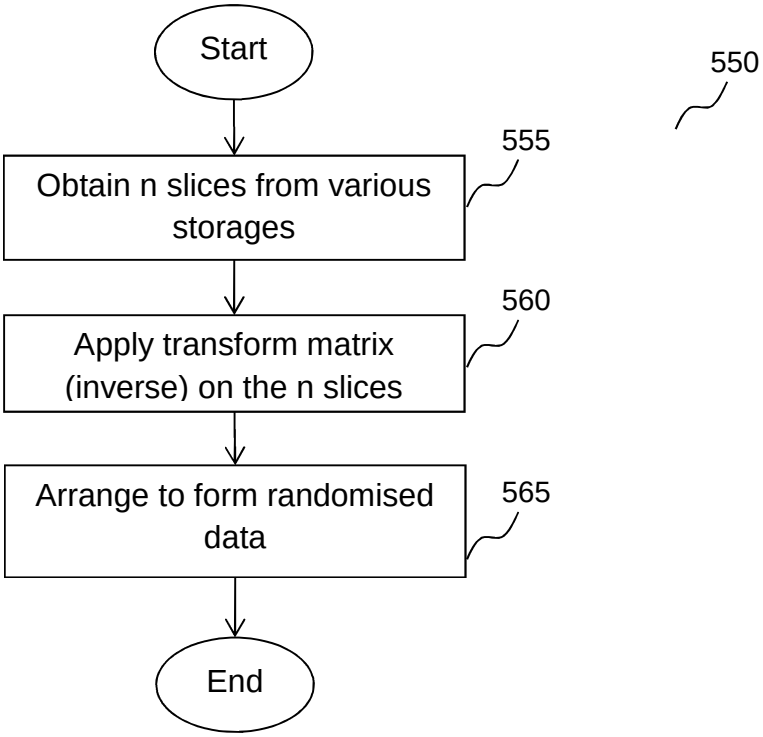

Figure 5.1

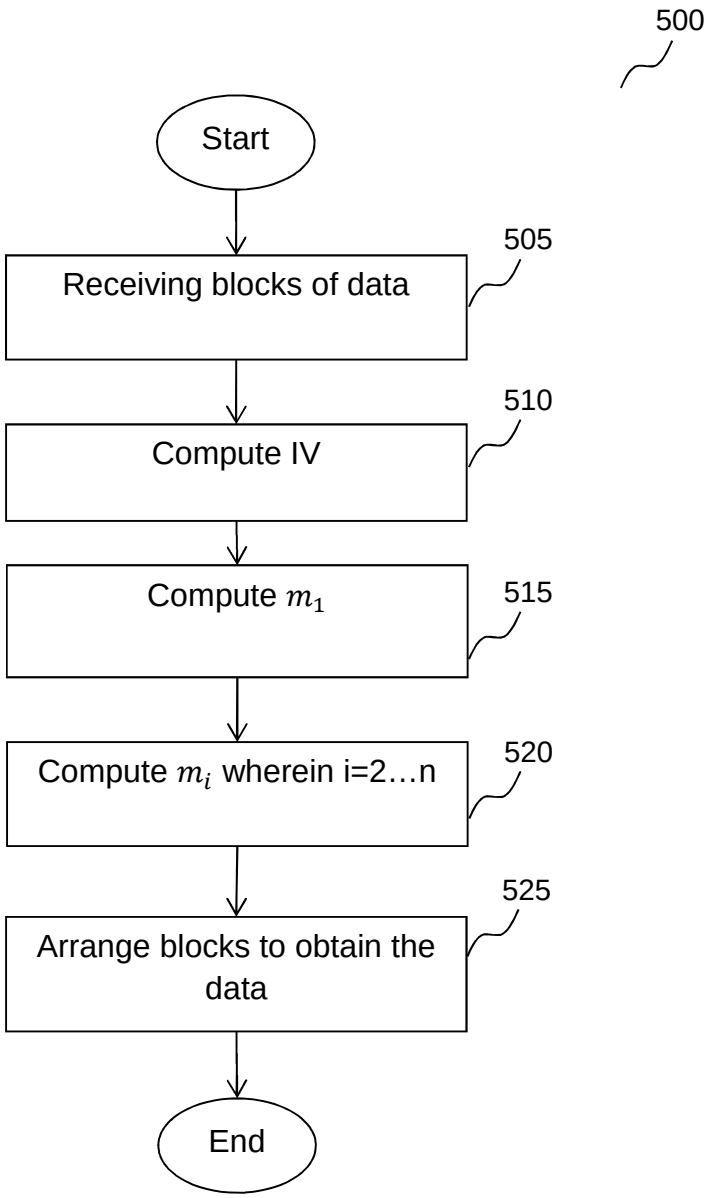
500
Start
505
Receiving blocks of data
510
Compute IV
515
Compute m_1
520
Compute m_i wherein i=2…n
525
Arrange blocks to obtain the data
End

Figure 5.2