FIG. 1

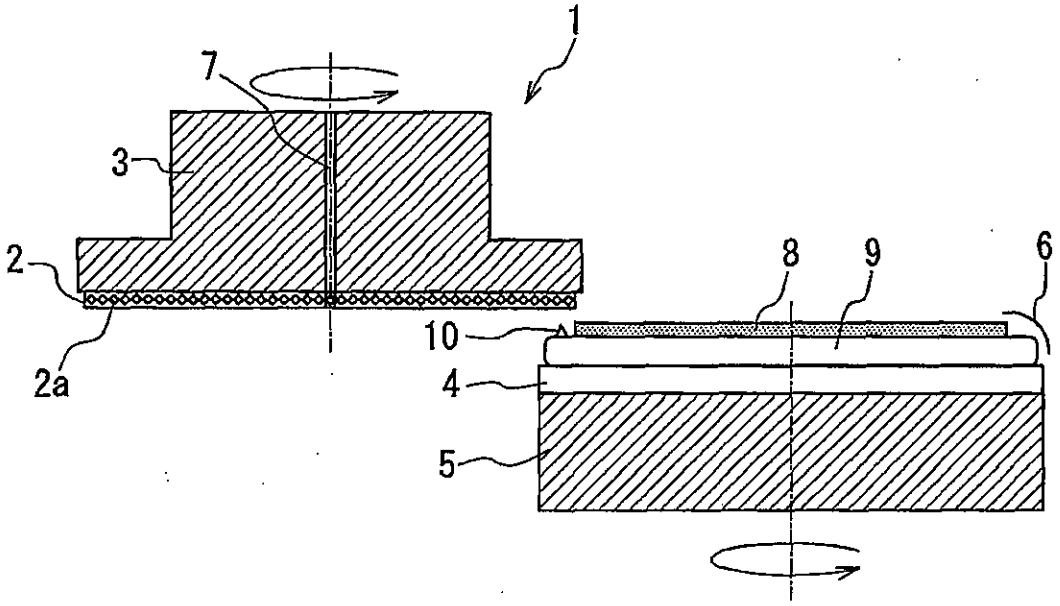


FIG. 2

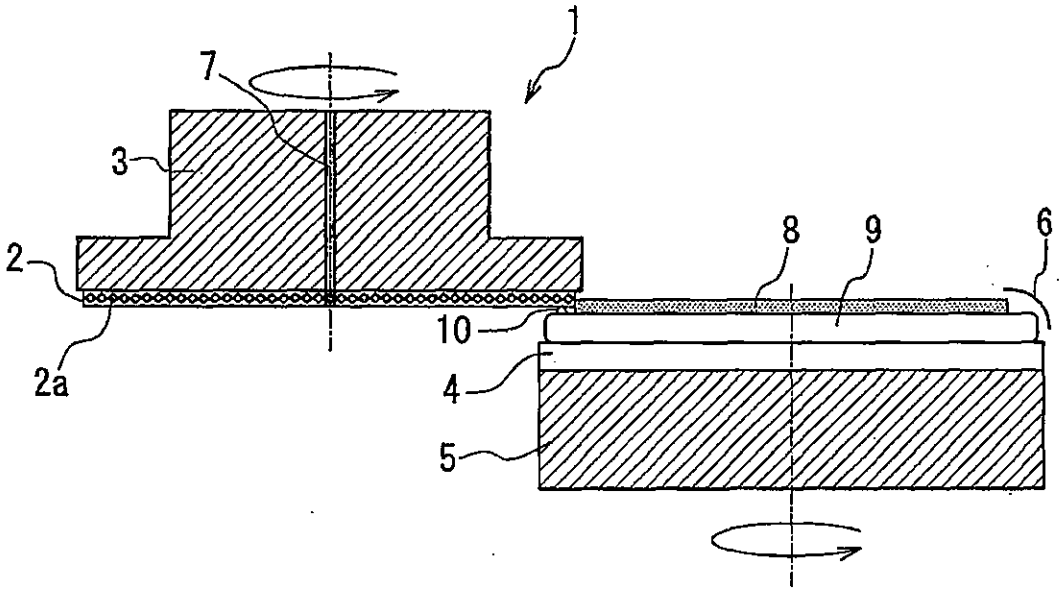


FIG. 3

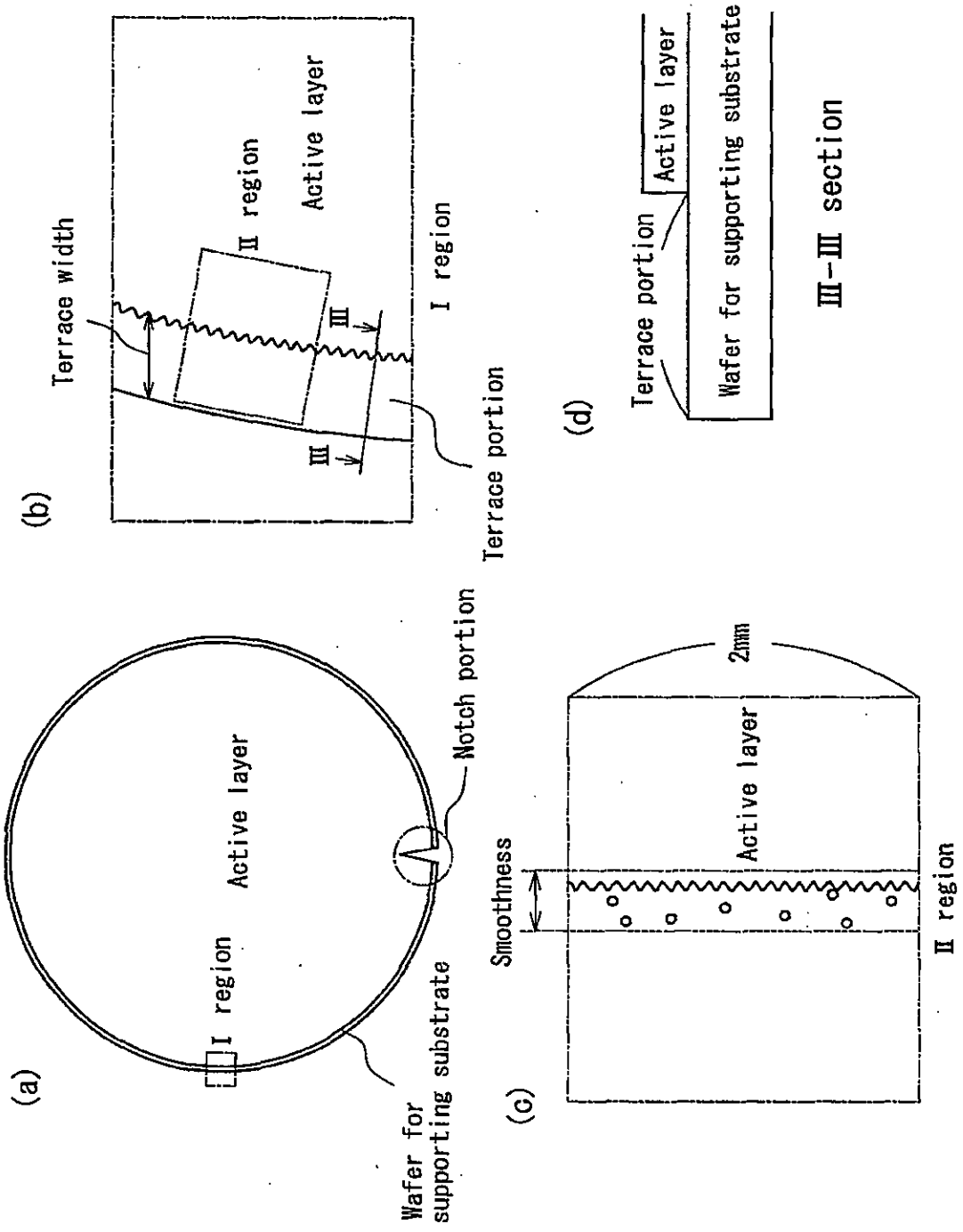

(a)
Active layer
I region
Notch portion
Wafer for supporting substrate
(b)
Terrace width
II region
Active layer
III
III
I region
Terrace portion
(c)
2mm
Smoothness
Active layer
II region
(d)
Terrace portion
Active layer
Wafer for supporting substrate
III-III section

FIG. 4

