

ABSTRACT**METHOD FOR SYNCHRONIZING DATA CONVERTERS BY MEANS OF A SIGNAL**

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TRANSMITTED FROM ONE TO THE NEXT

In an architecture for processing data comprising a control unit and converters CN_j to be synchronized to an active front of a common reference clock CLK, the synchronizing method makes provision for the converters to
10 be arranged in at least one series chain, and for a procedure for synchronizing the converters by propagating a synchronizing signal SYNC-m emitted by the control unit, said signal being retransmitted as output OUT by each converter, after resynchronization to a clock active front, to a synchronization input IN of a following converter in the chain. Each converter
15 comprises a synchronization configuration register REG containing at least one polarity parameter Sel-edge_j that sets the polarity of the reference-clock front for reliable detection of a synchronizing signal received via the input of the converter. A phase parameter Sel-shift_j furthermore allows the phase of the sampling clocks of n converting cores of the converters, working at a
20 sampling frequency obtained by dividing by n the CLK reference-clock frequency, to be synchronized.

Figure 1

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