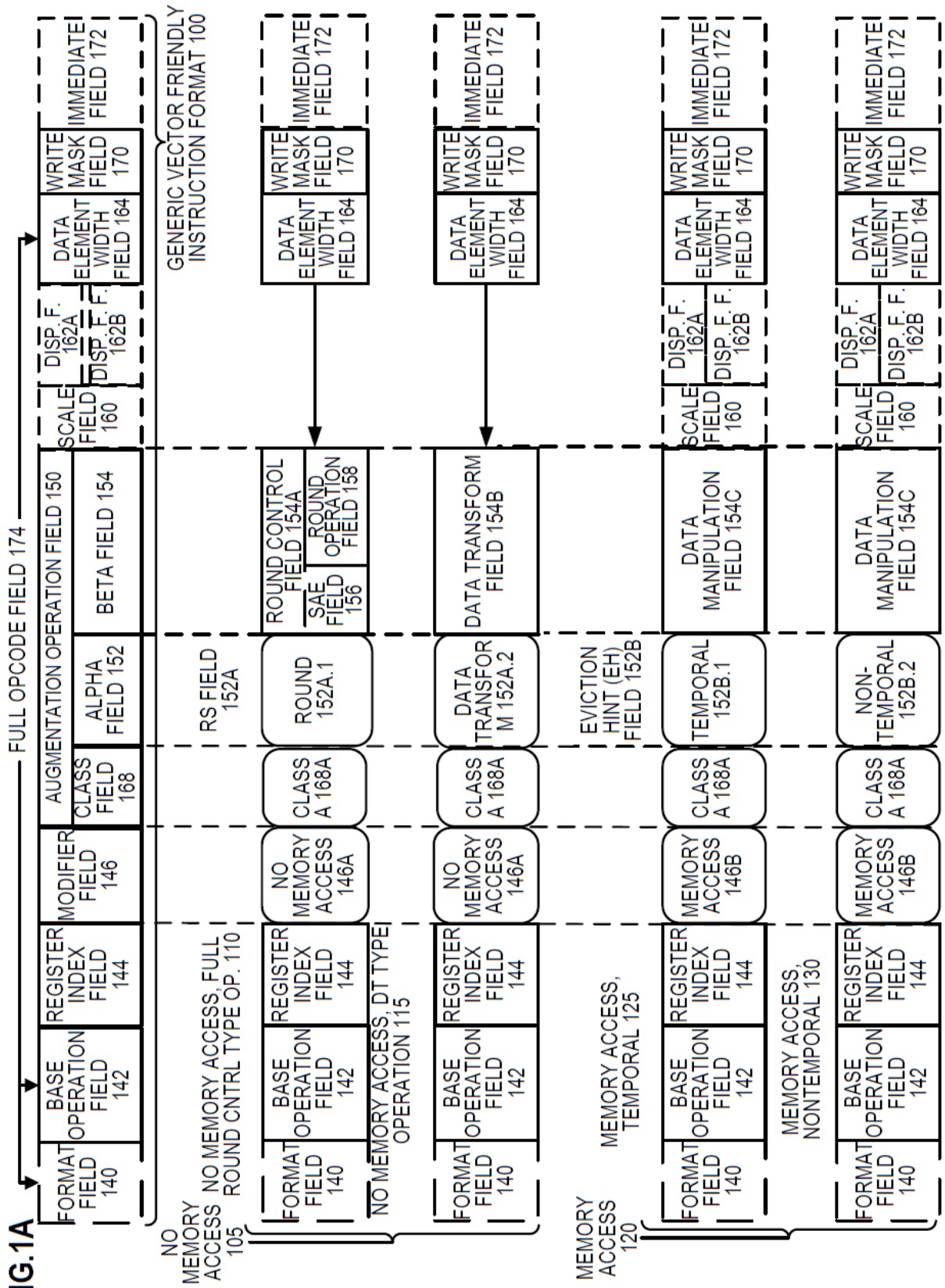
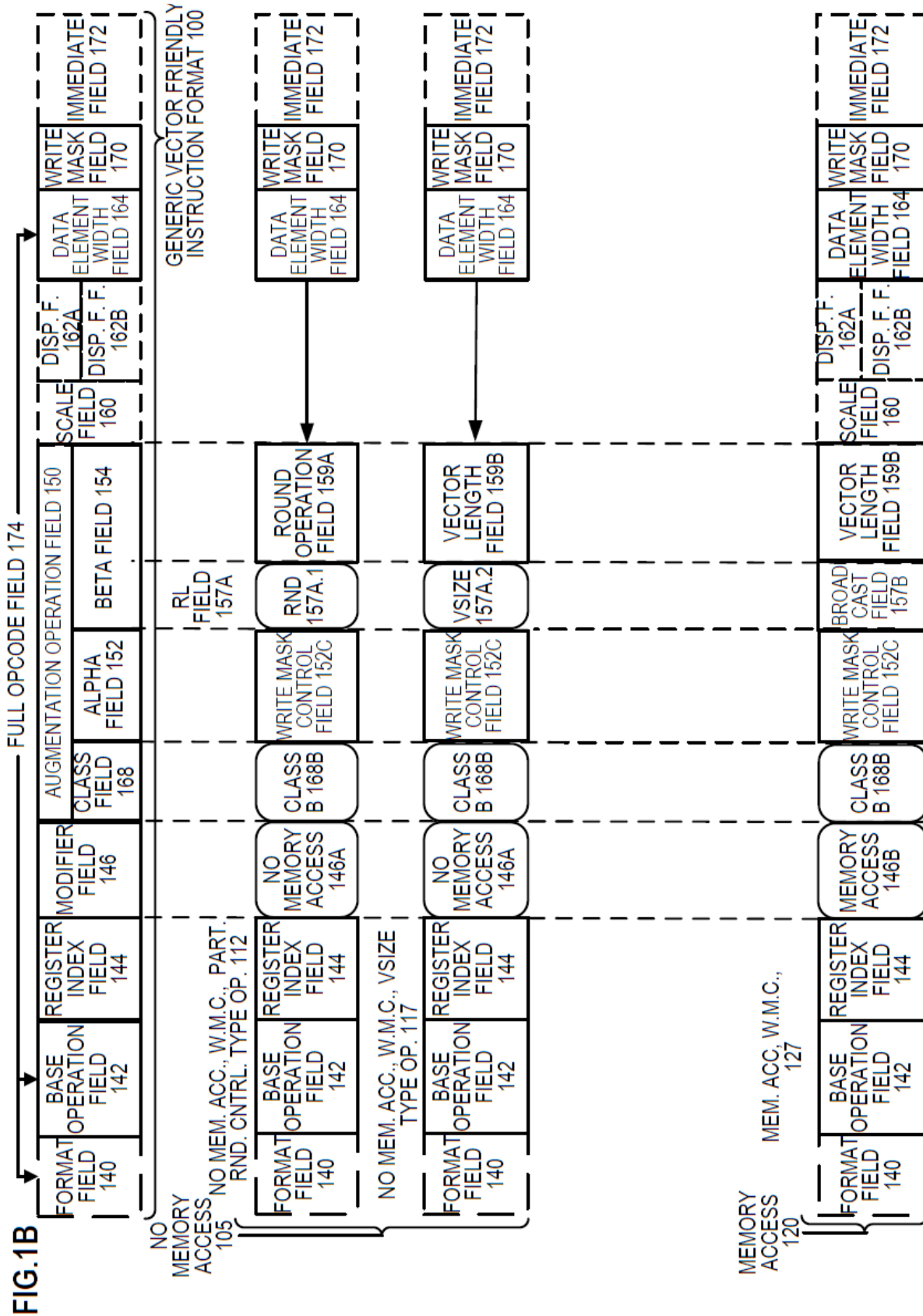


FIG. 1A





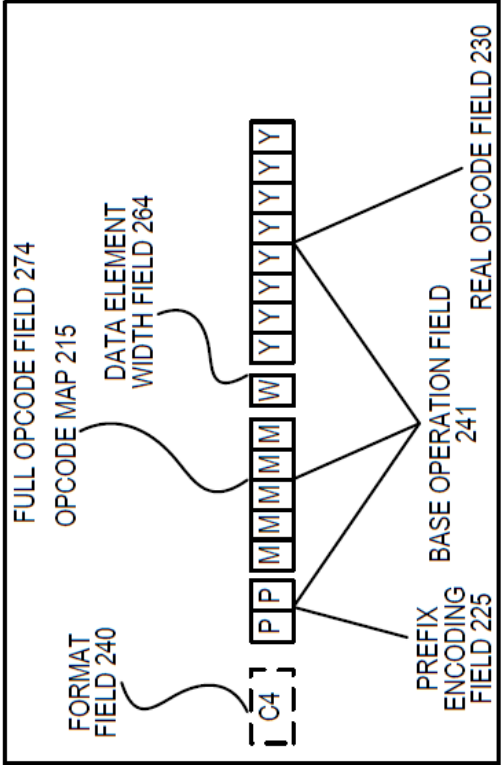
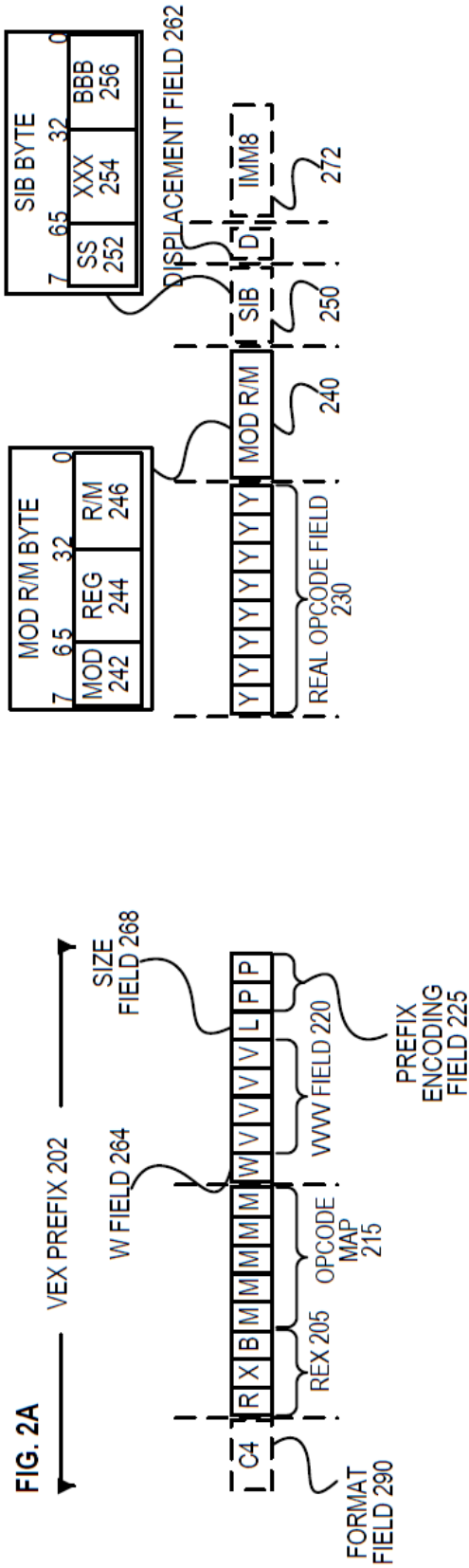


FIG. 2C



REGISTER ARCHITECTURE 300

General Purpose Registers 325
16 X 64 BITS

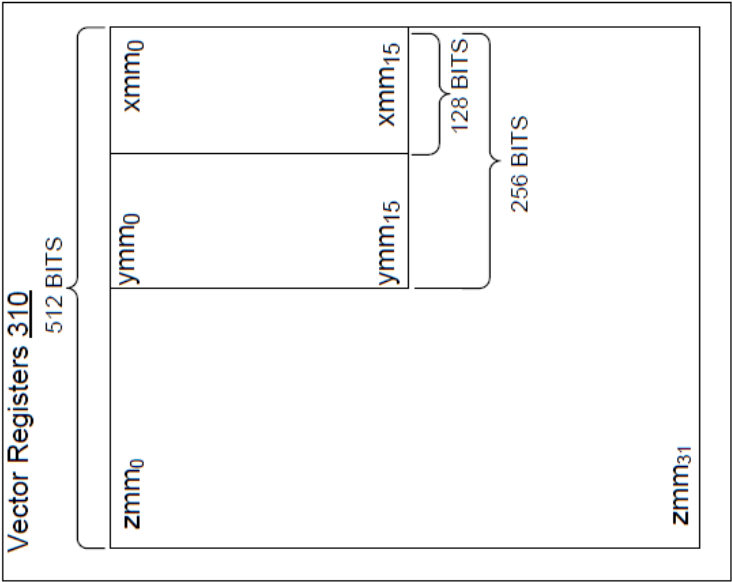


FIG. 3



FIG. 4A

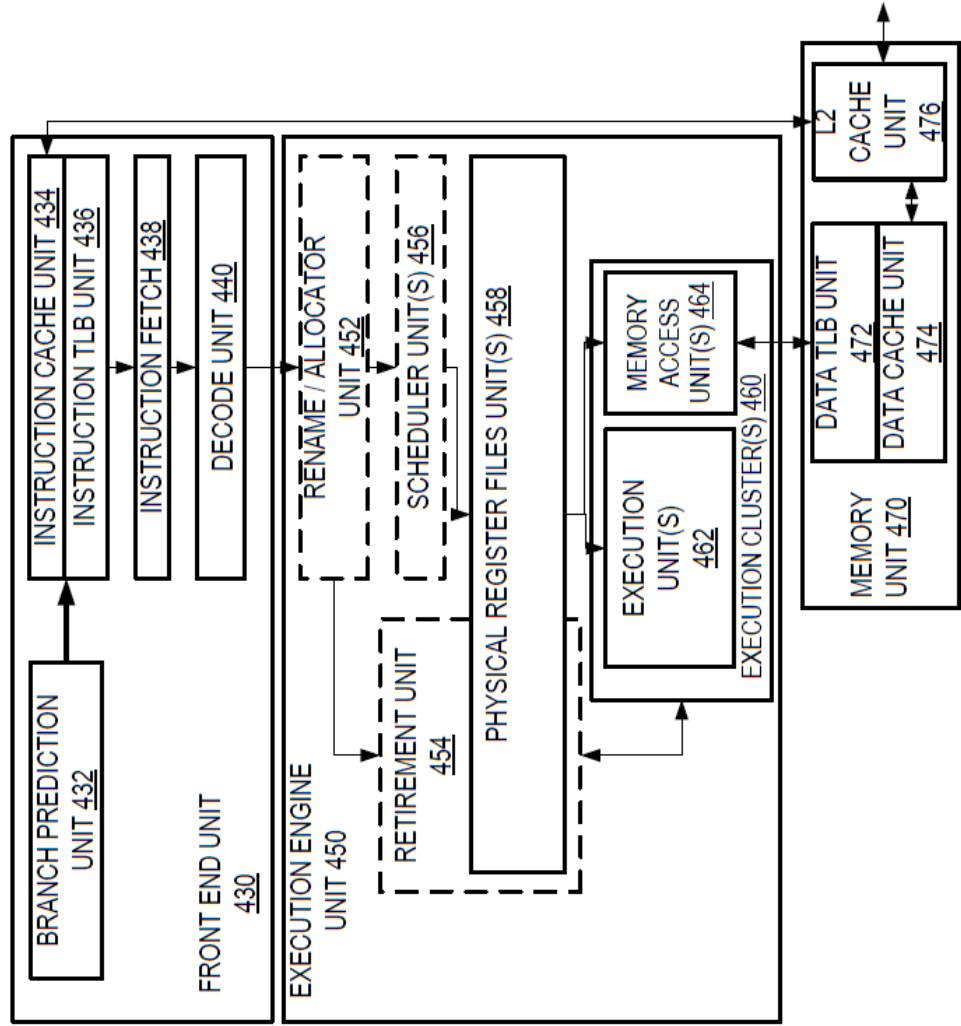


FIG. 4B

FIG. 5A

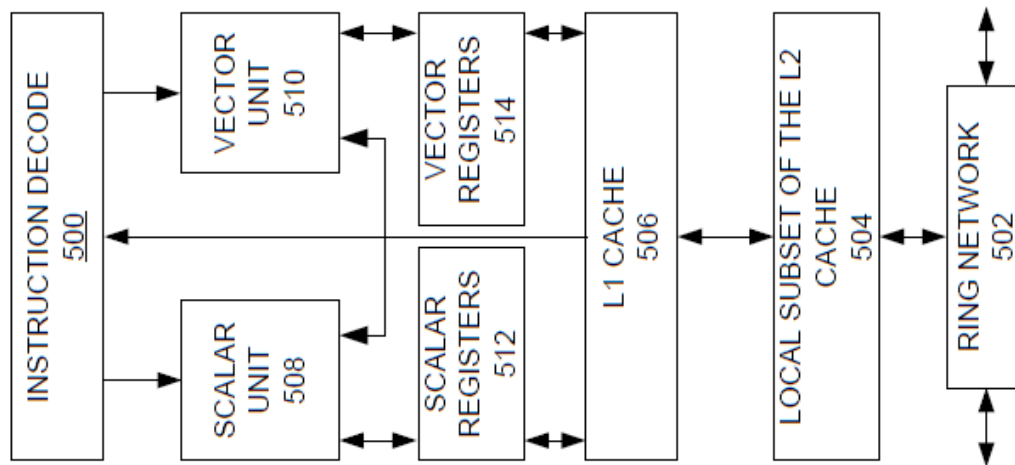
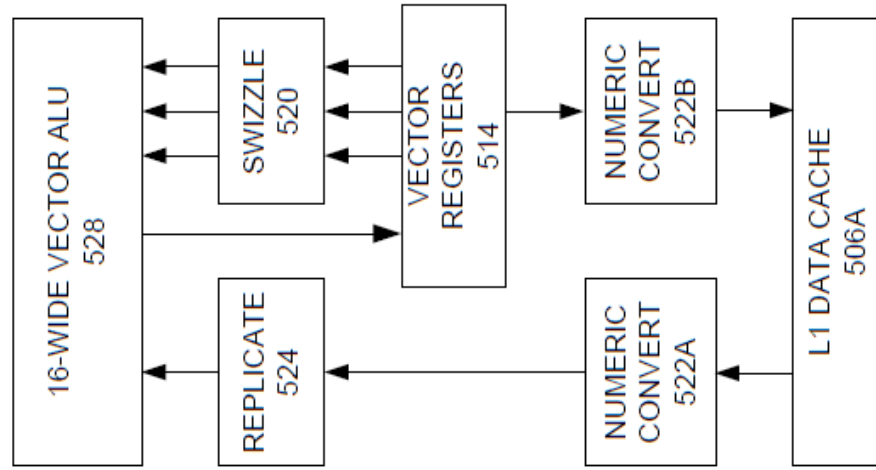
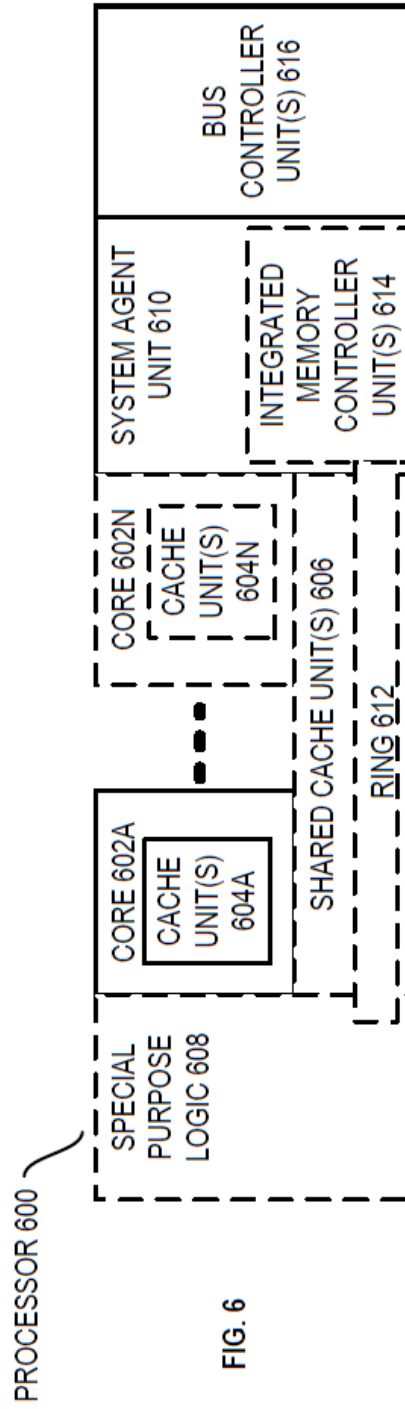


FIG. 5B





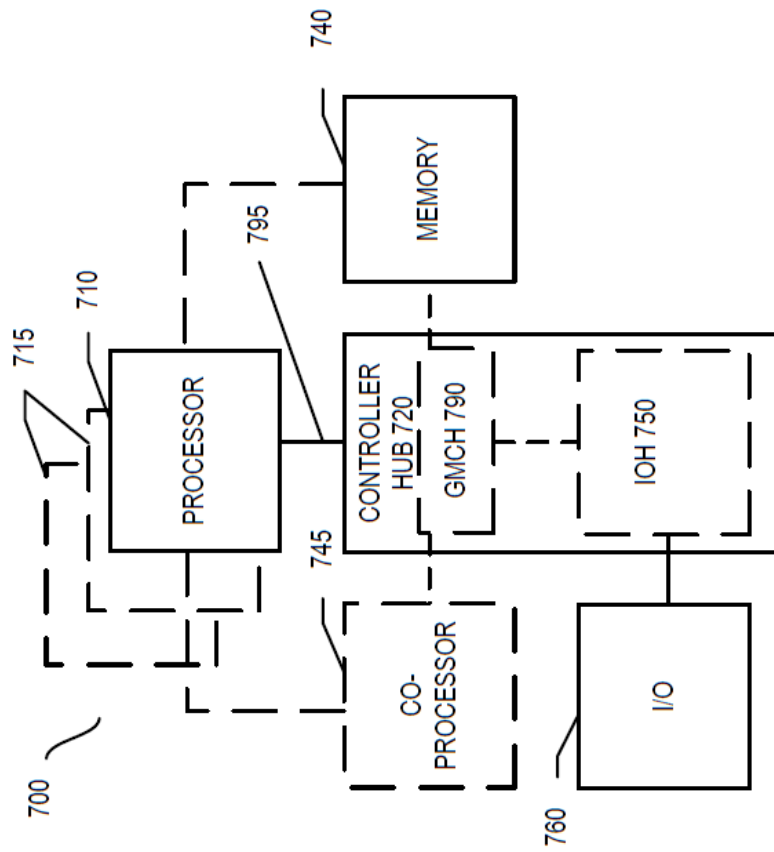


FIG. 7

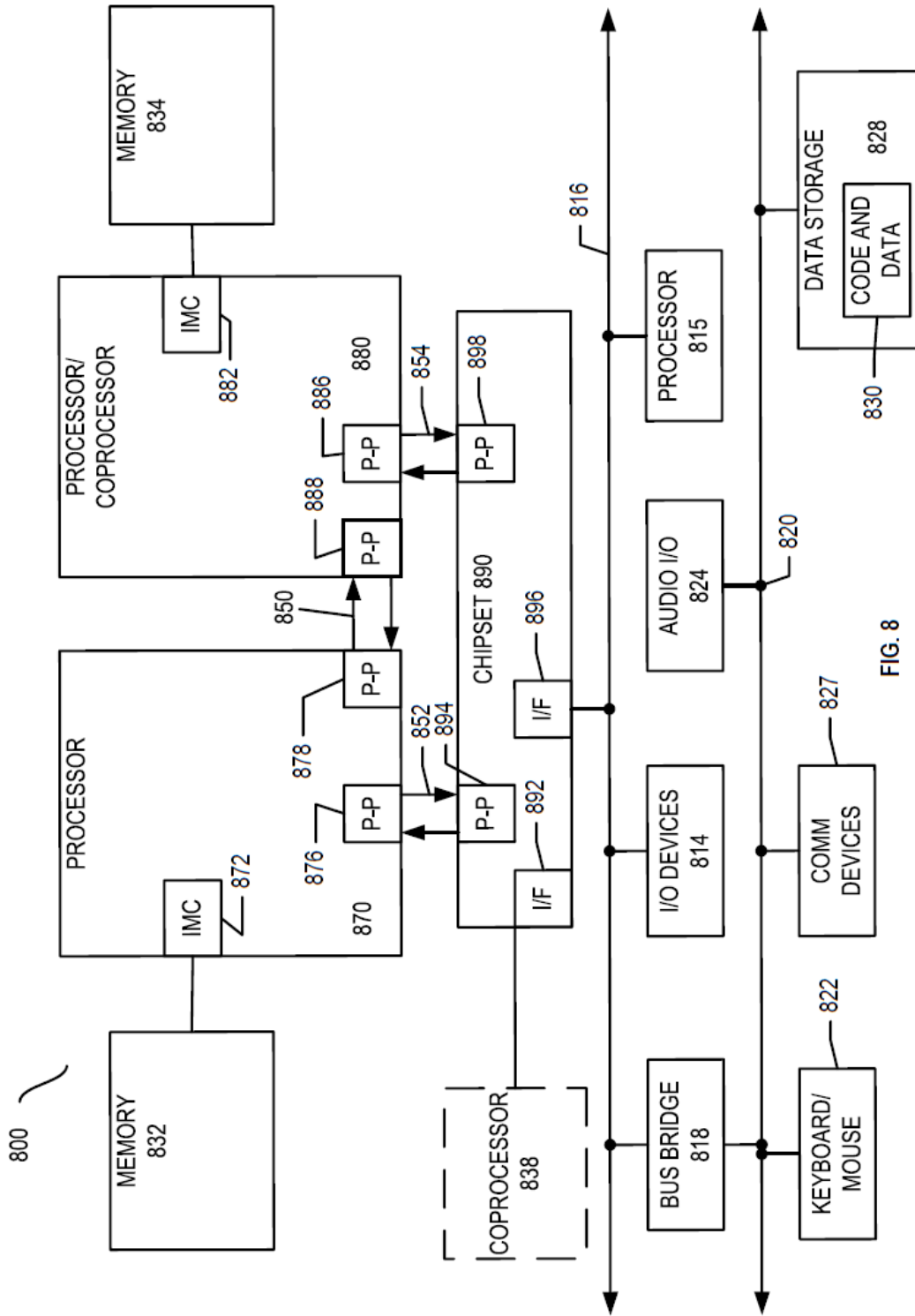


FIG. 8

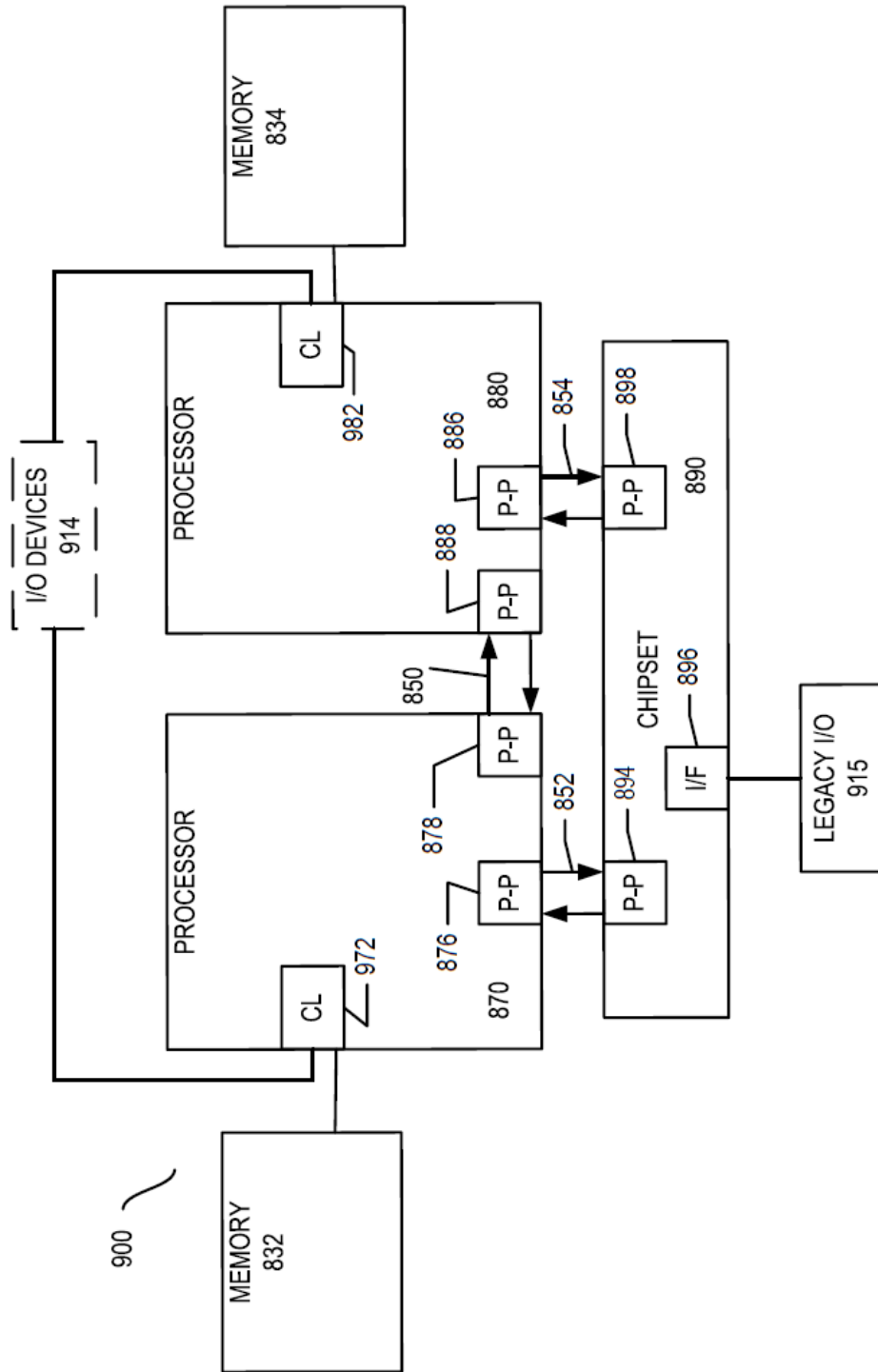


FIG. 9

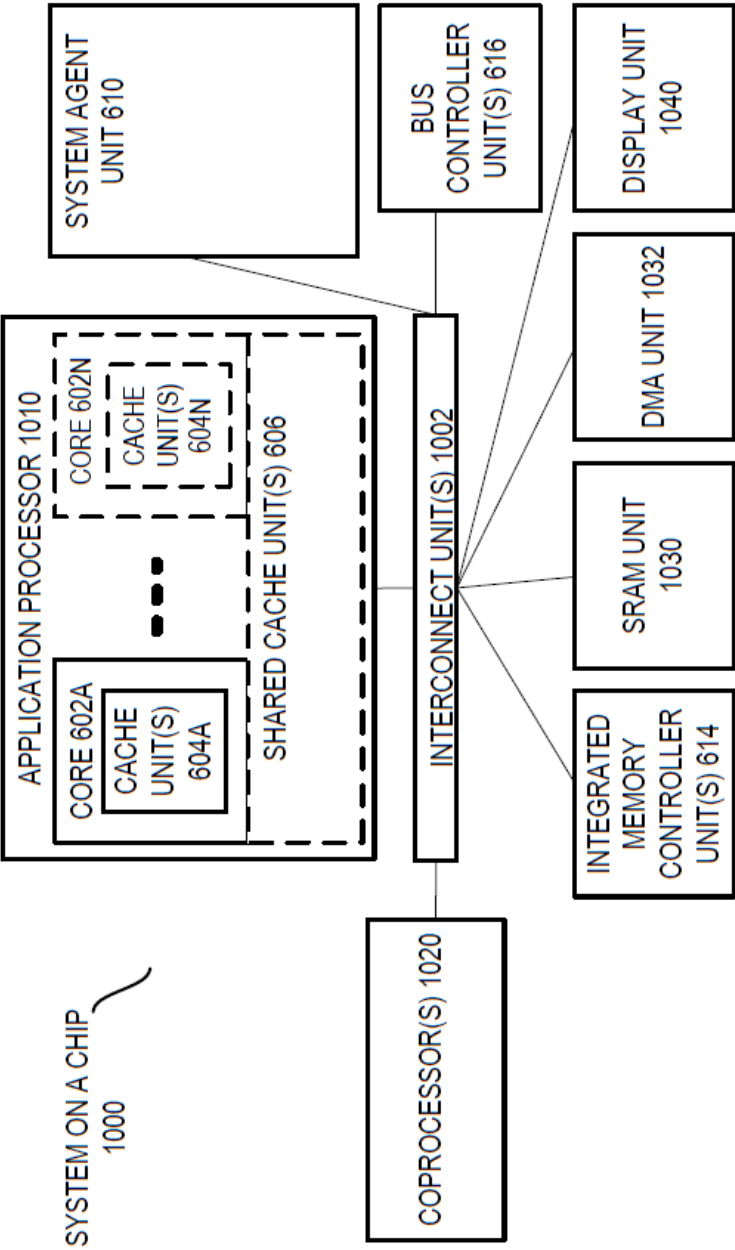
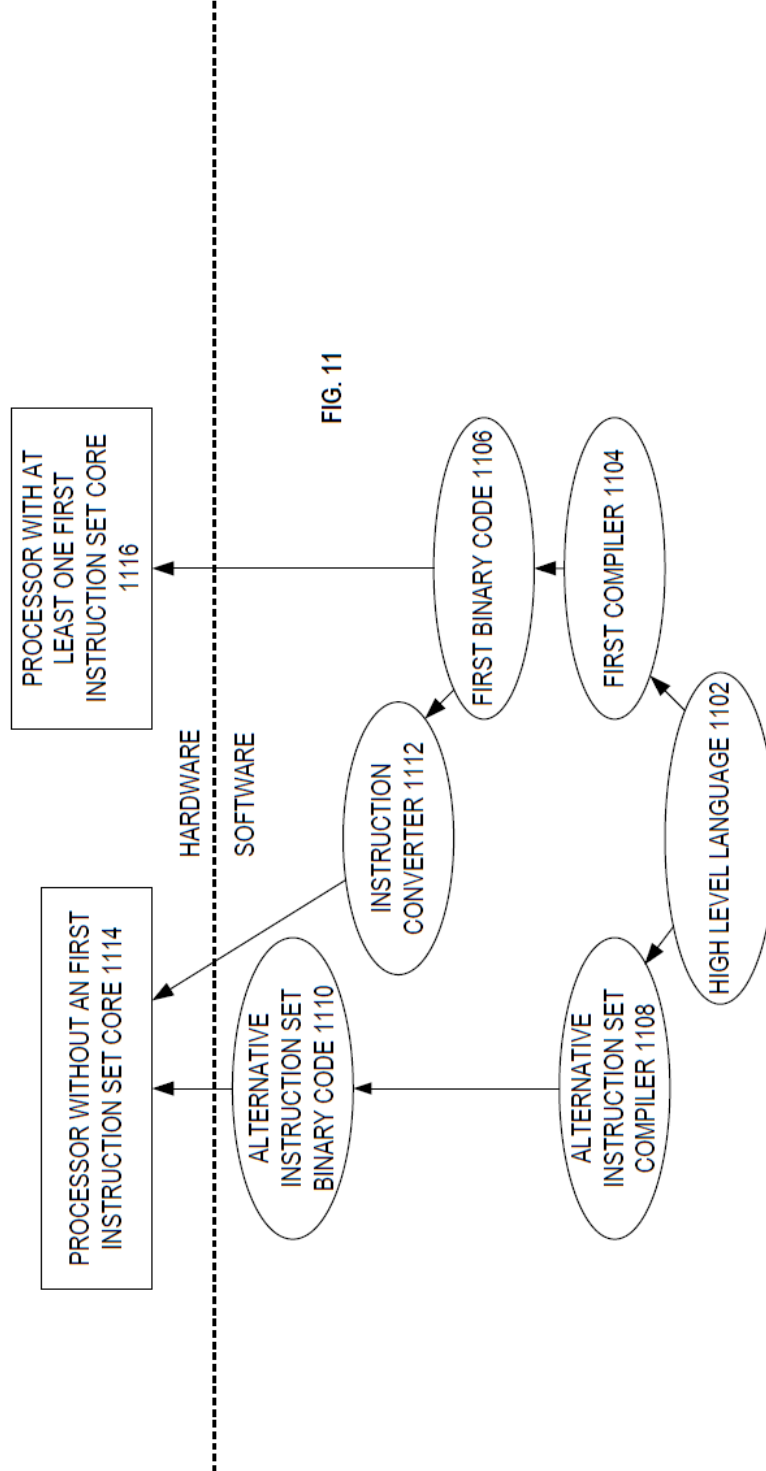


FIG. 10



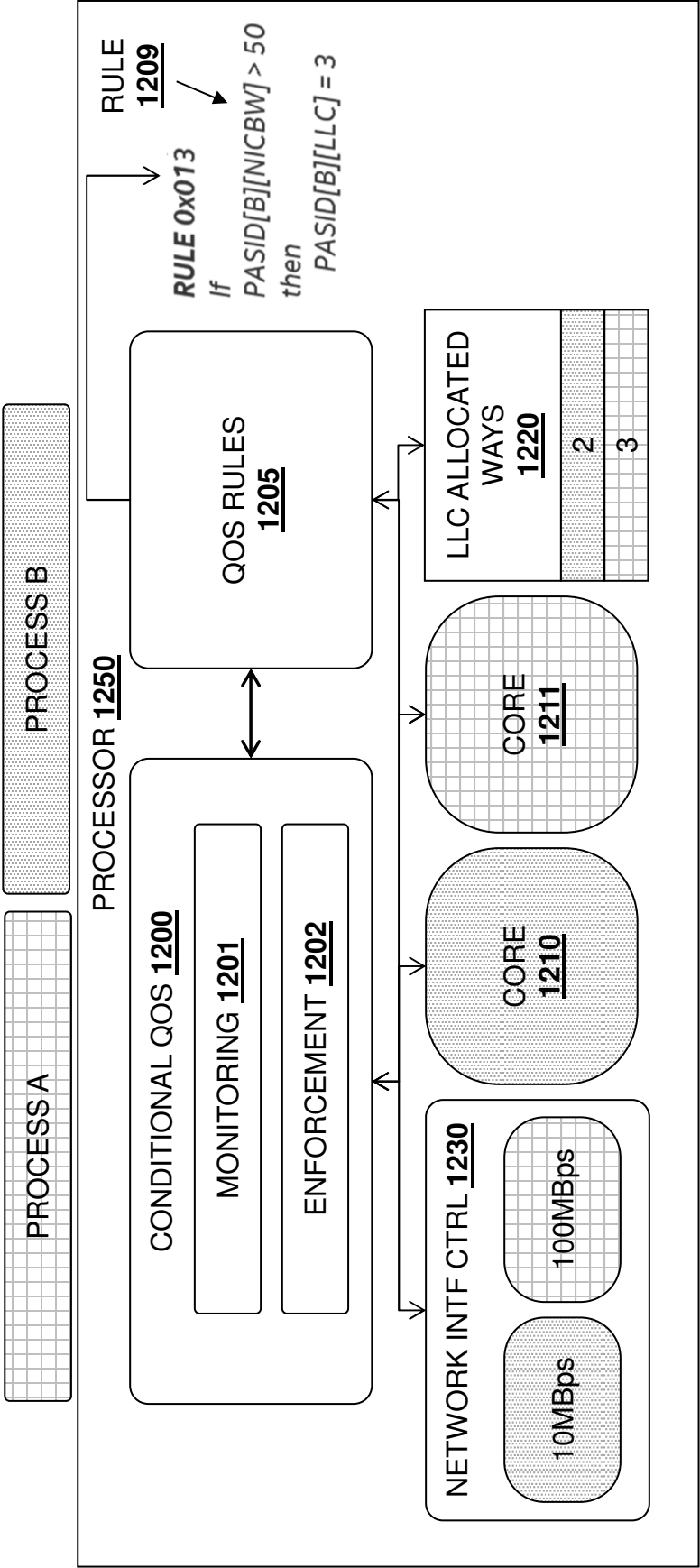


Fig. 12A

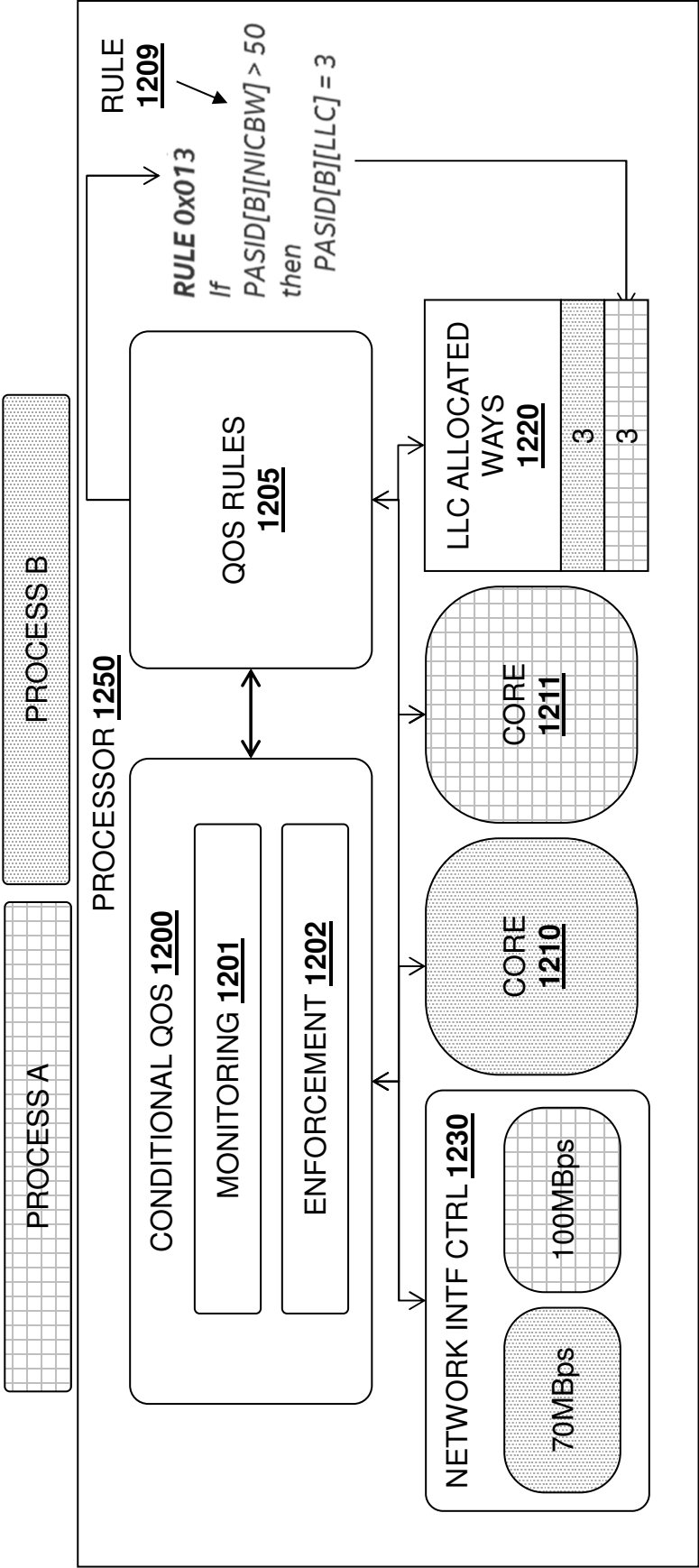


Fig. 12B

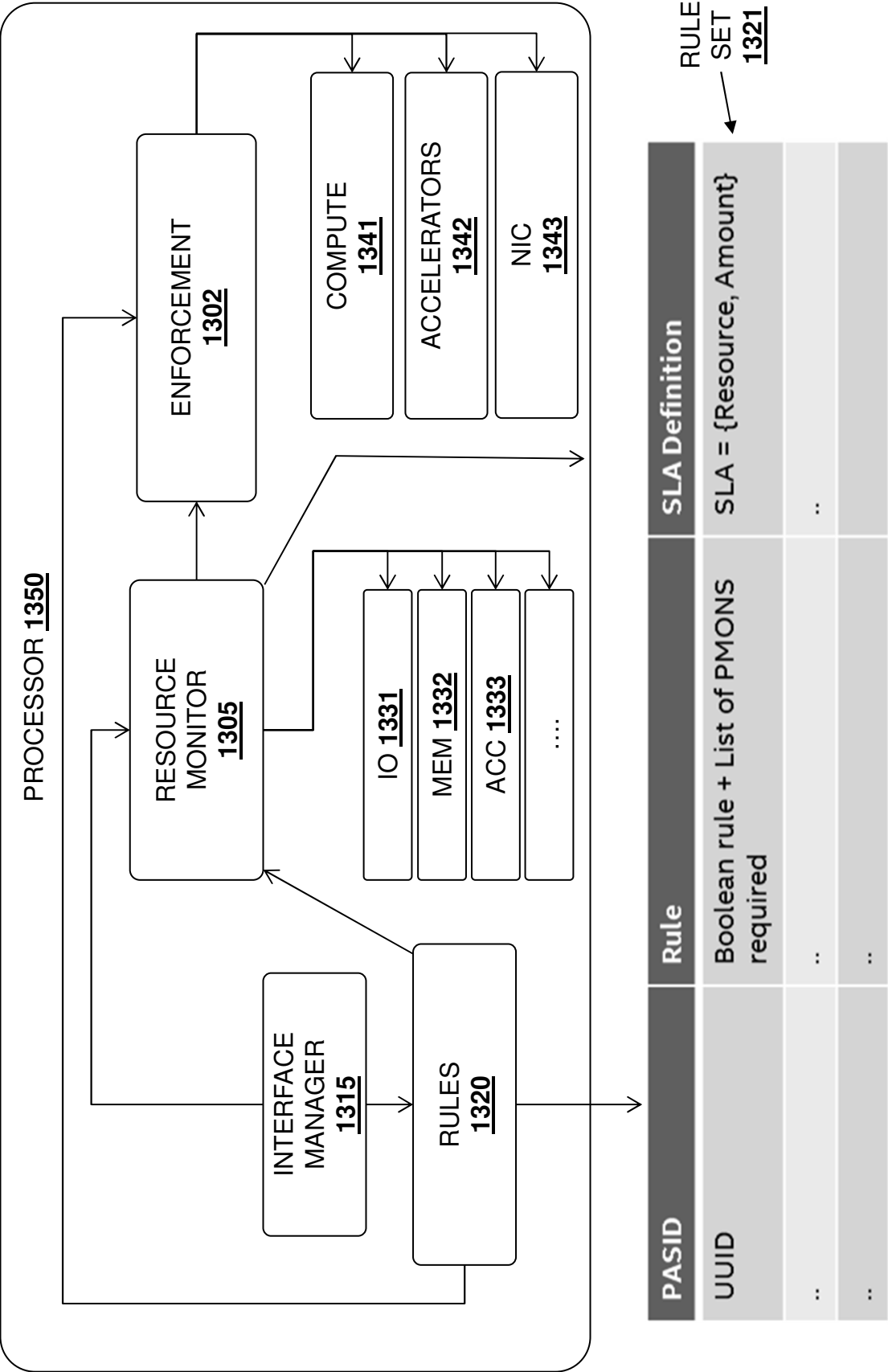
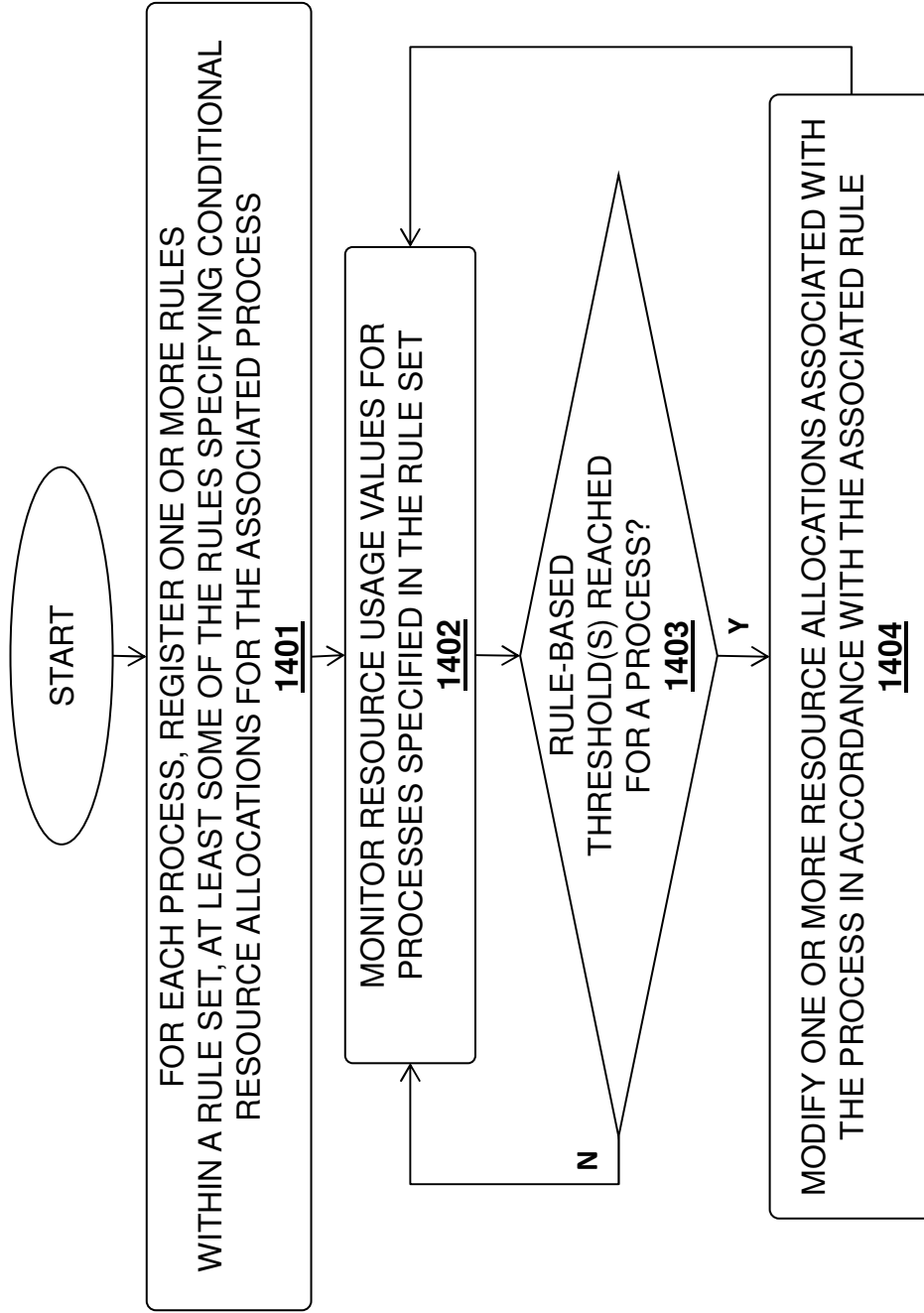


Fig. 13

**Fig. 14**

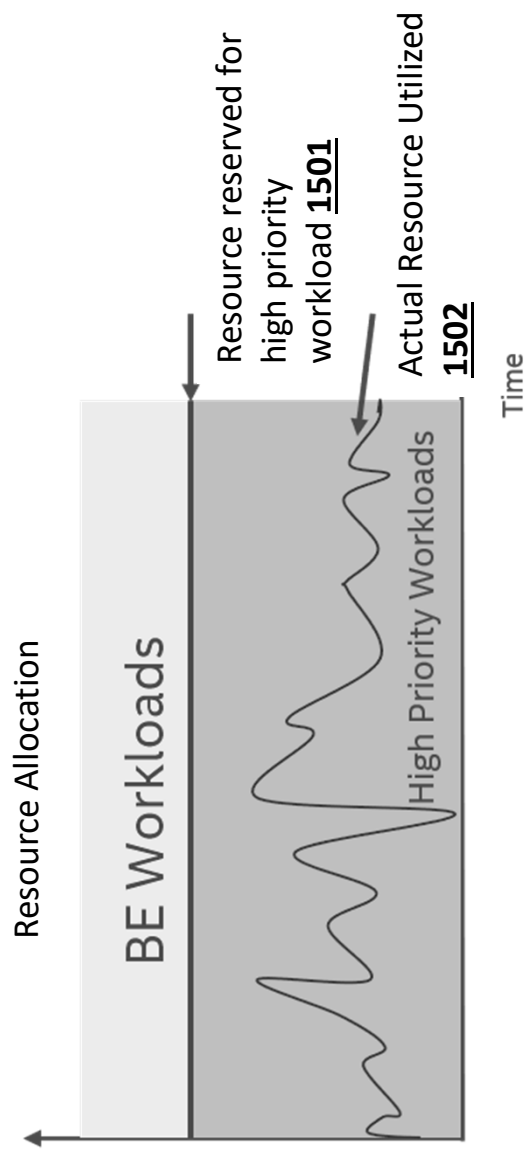


Fig. 15A

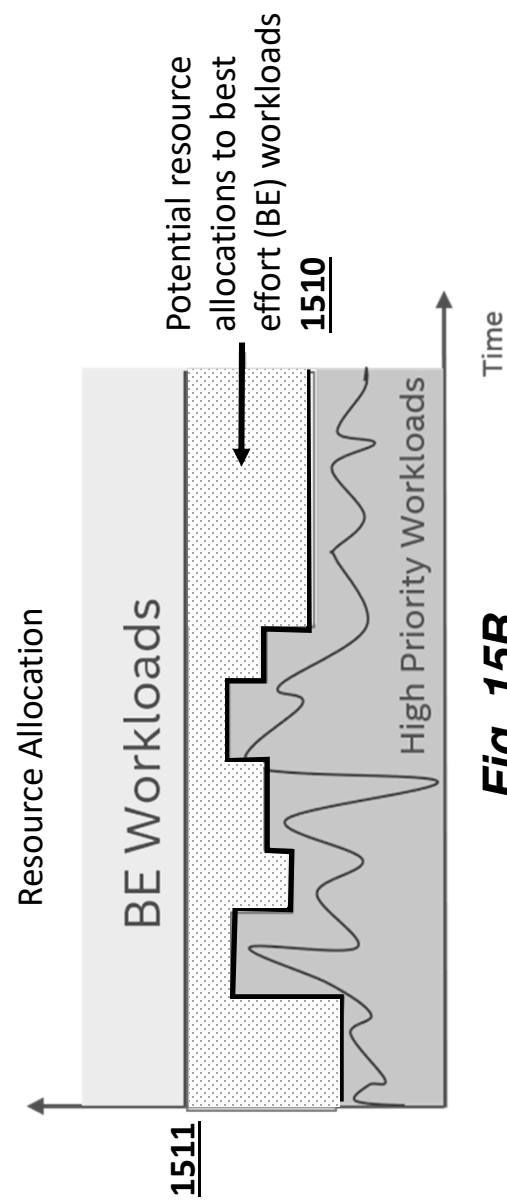


Fig. 15B

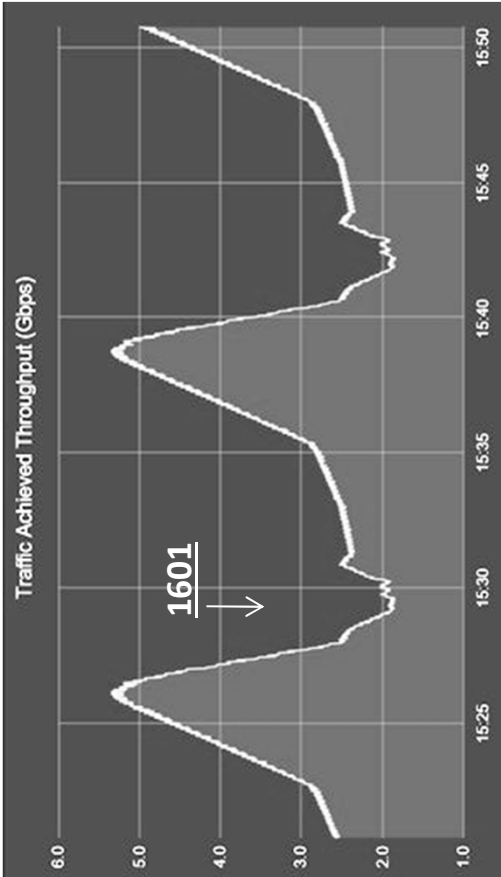


Fig. 16

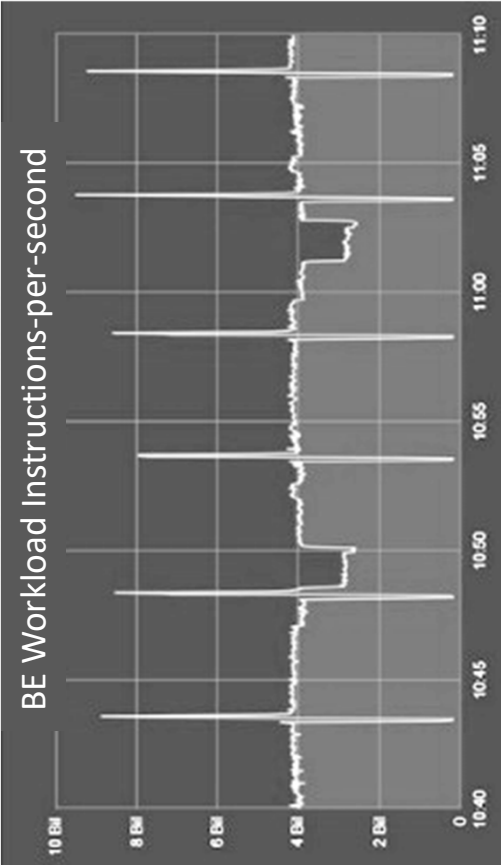


Fig. 17B

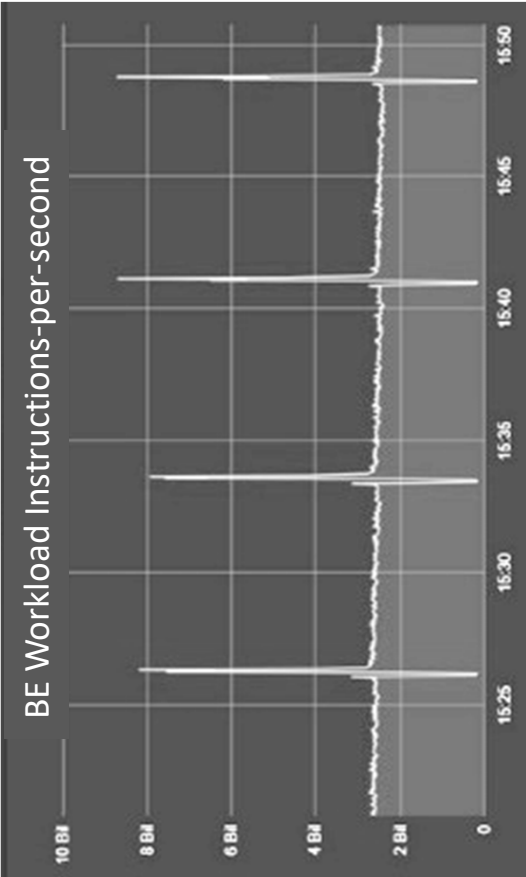


Fig. 17A

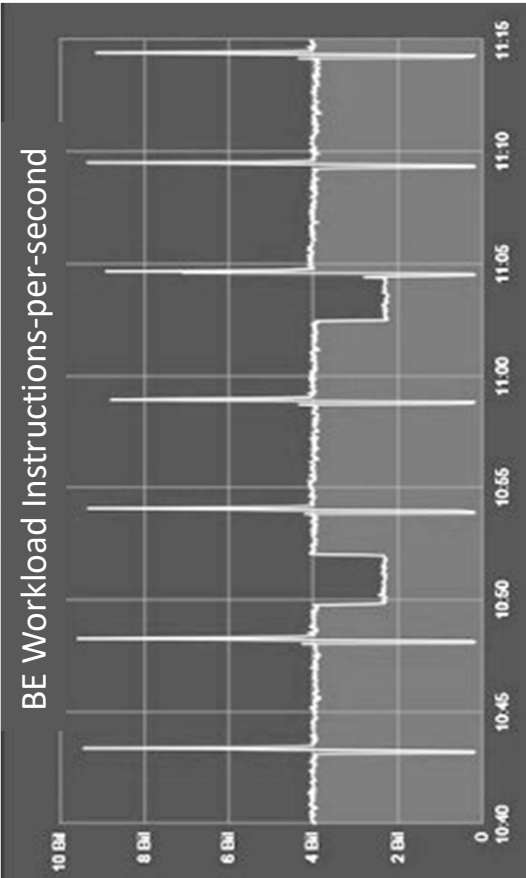


Fig. 17C

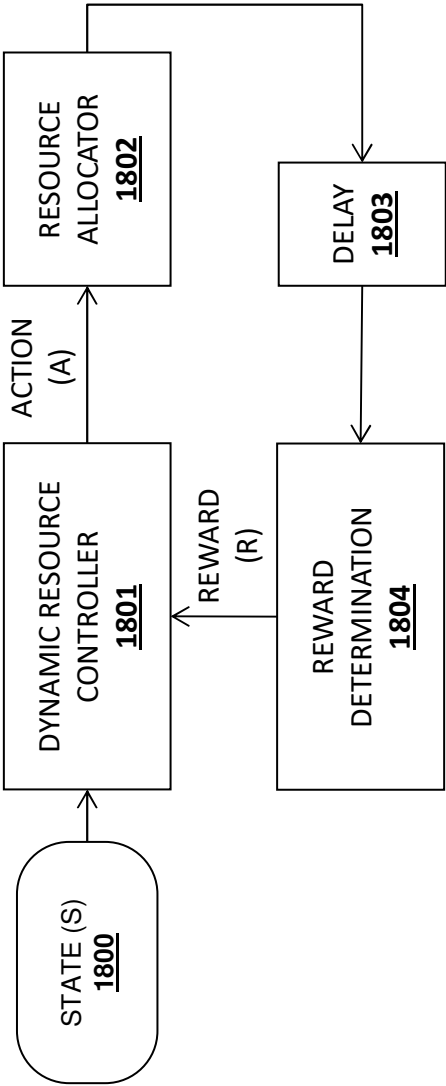


Fig. 18

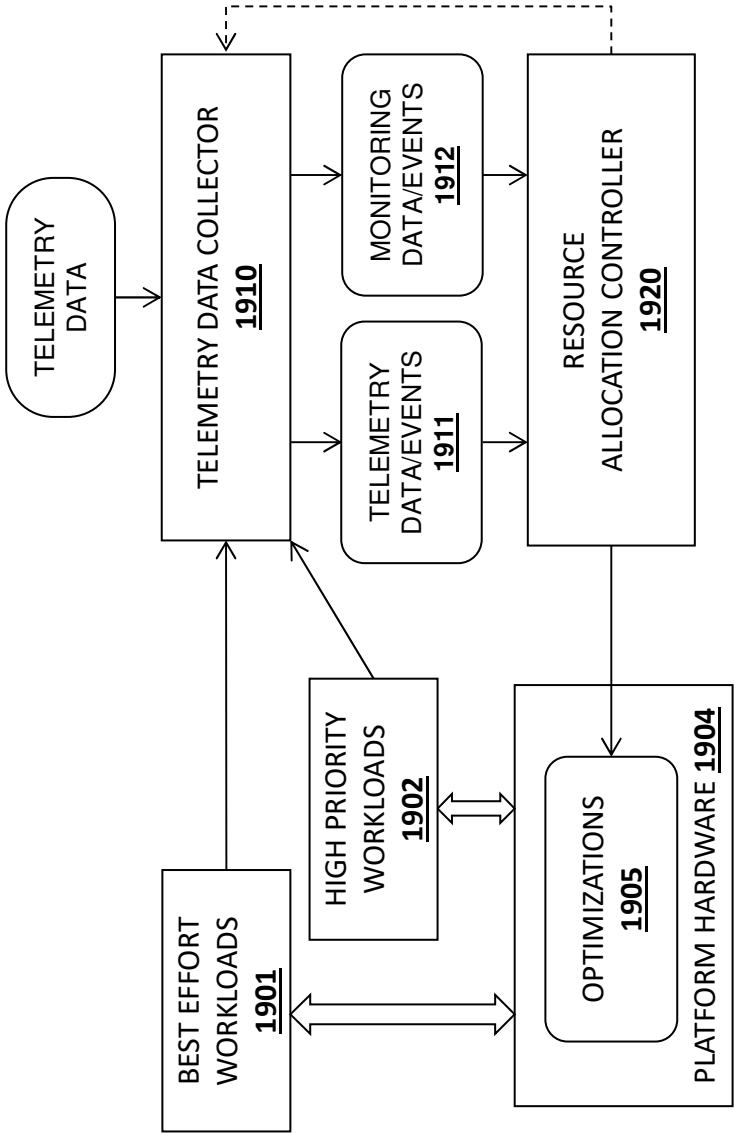


Fig. 19

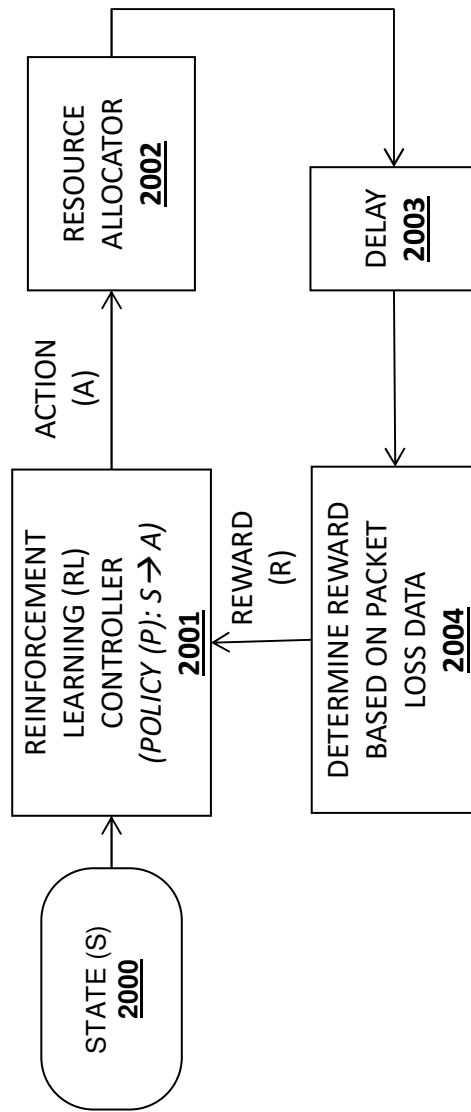
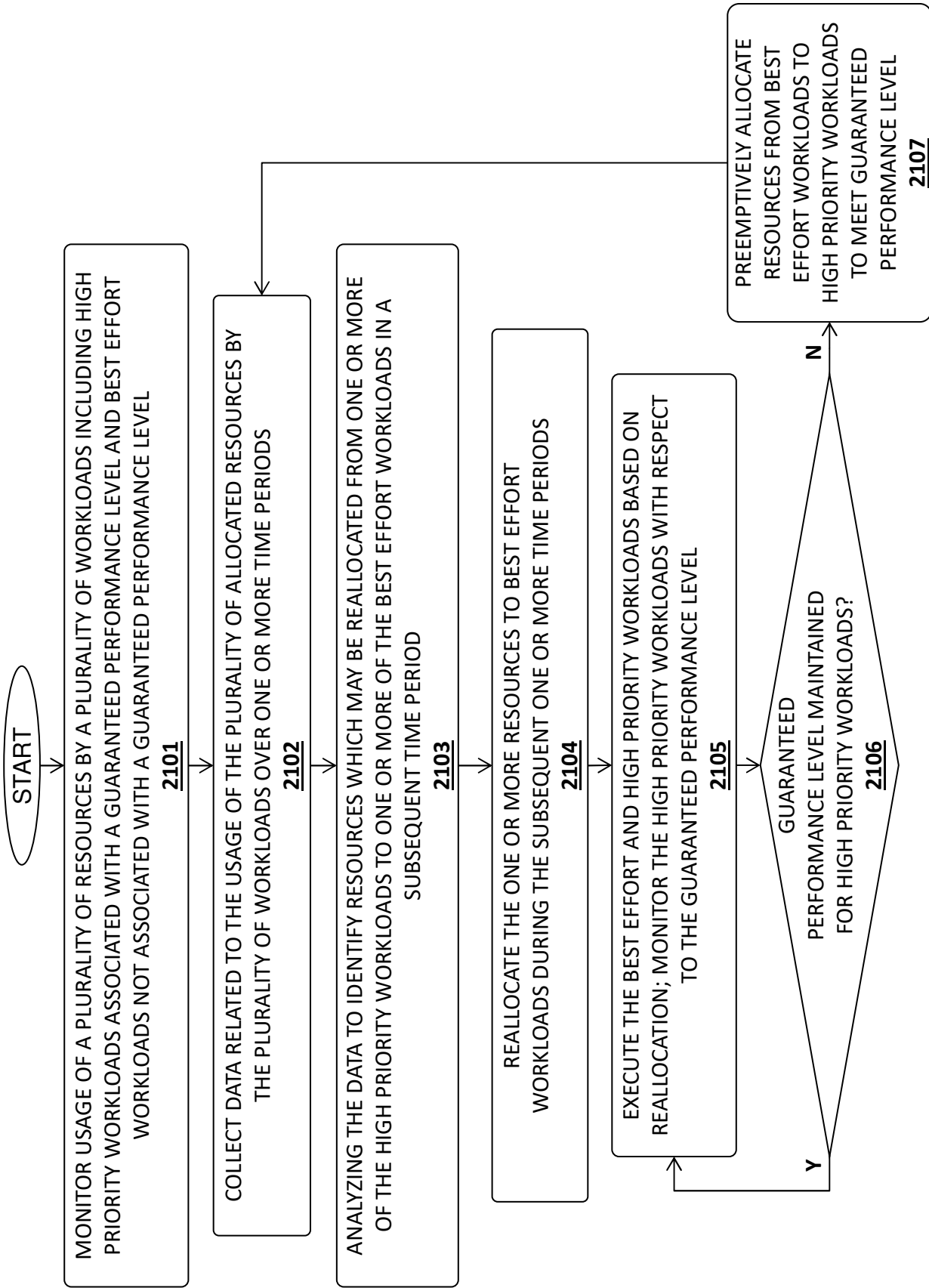


Fig. 20

**Fig. 21**

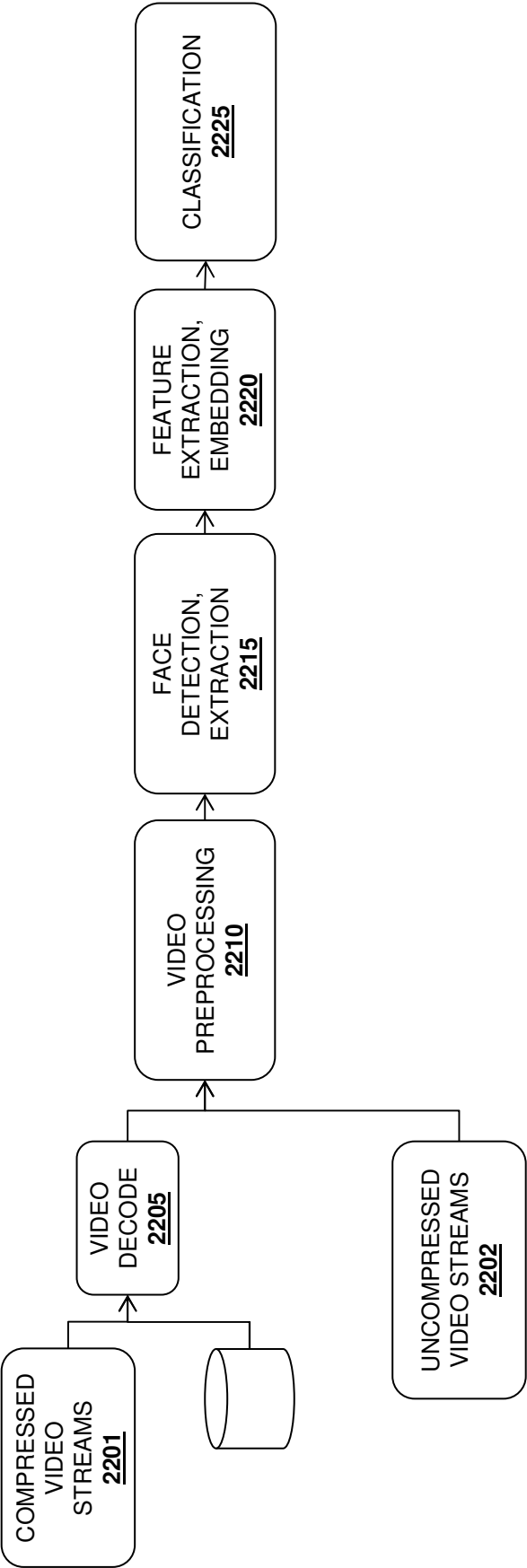
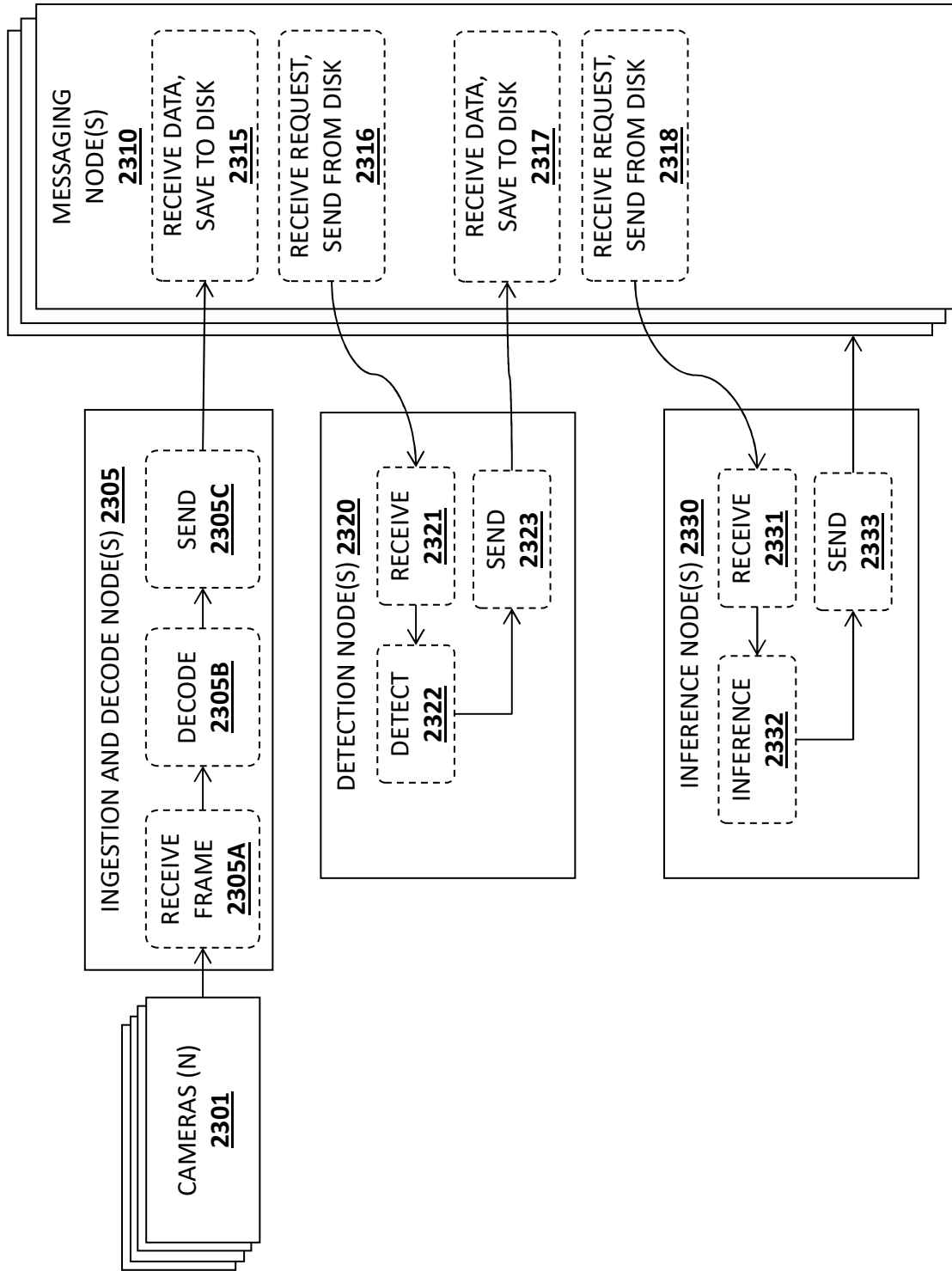


Fig. 22

**Fig. 23**

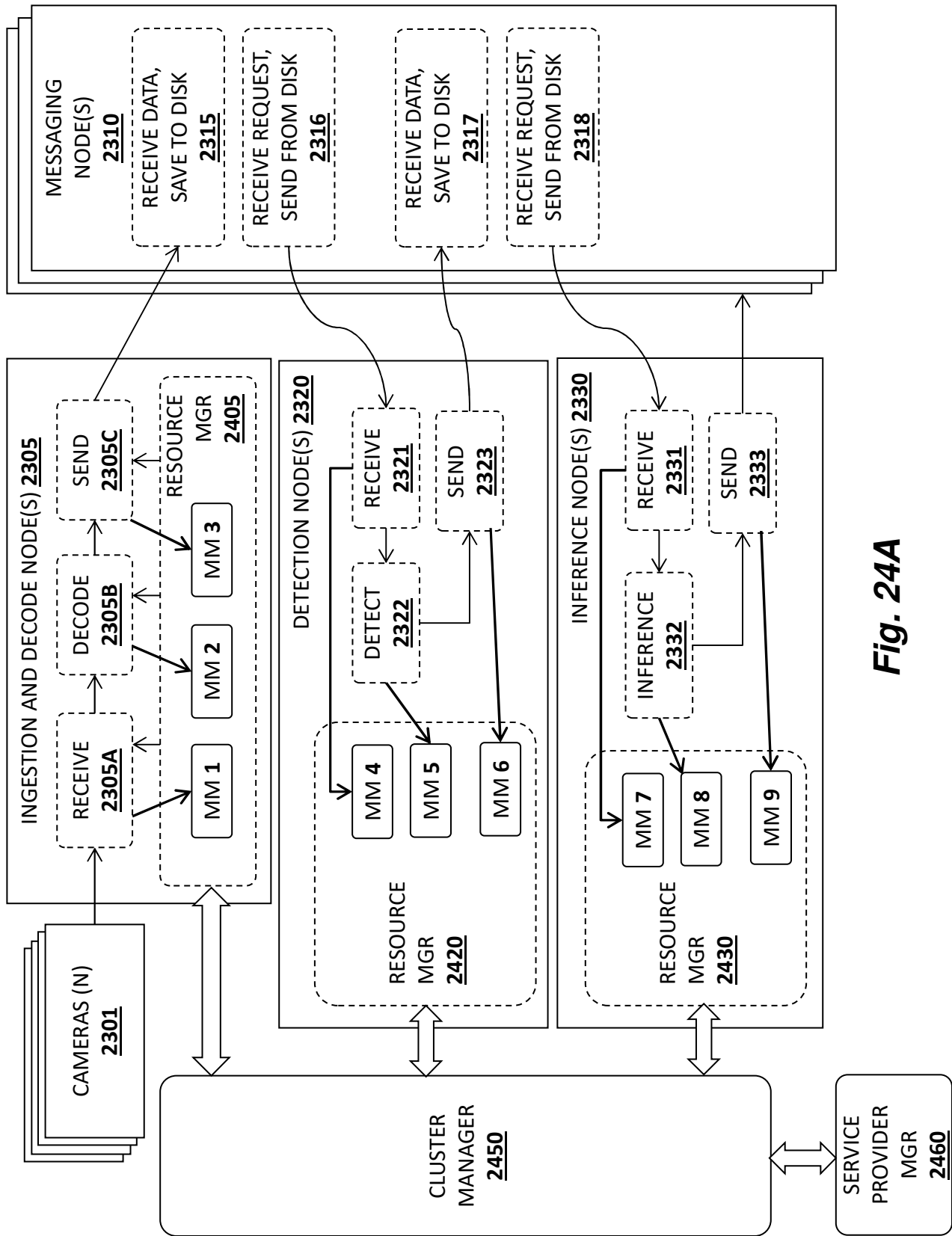


Fig. 24A

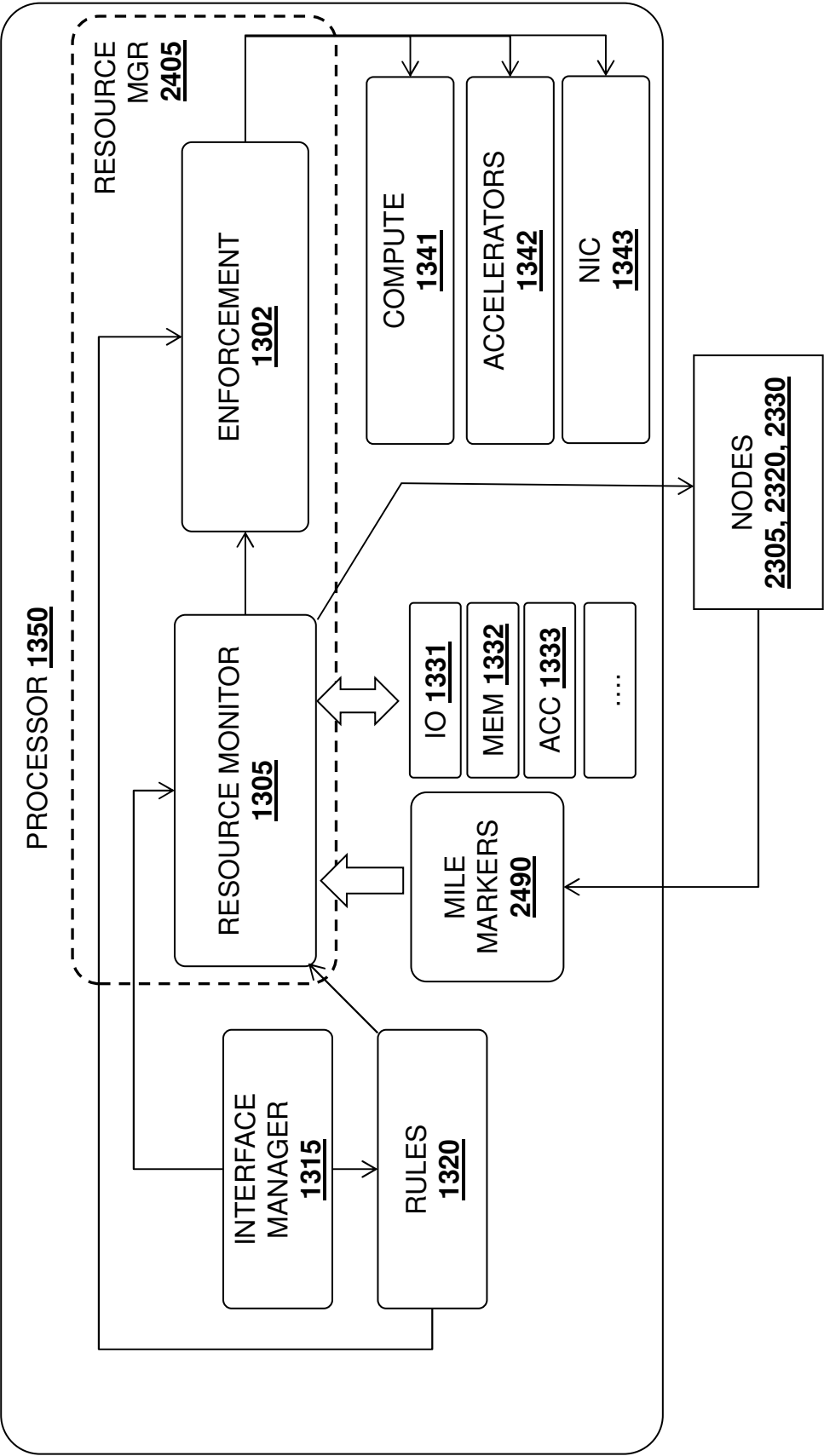


Fig. 24B

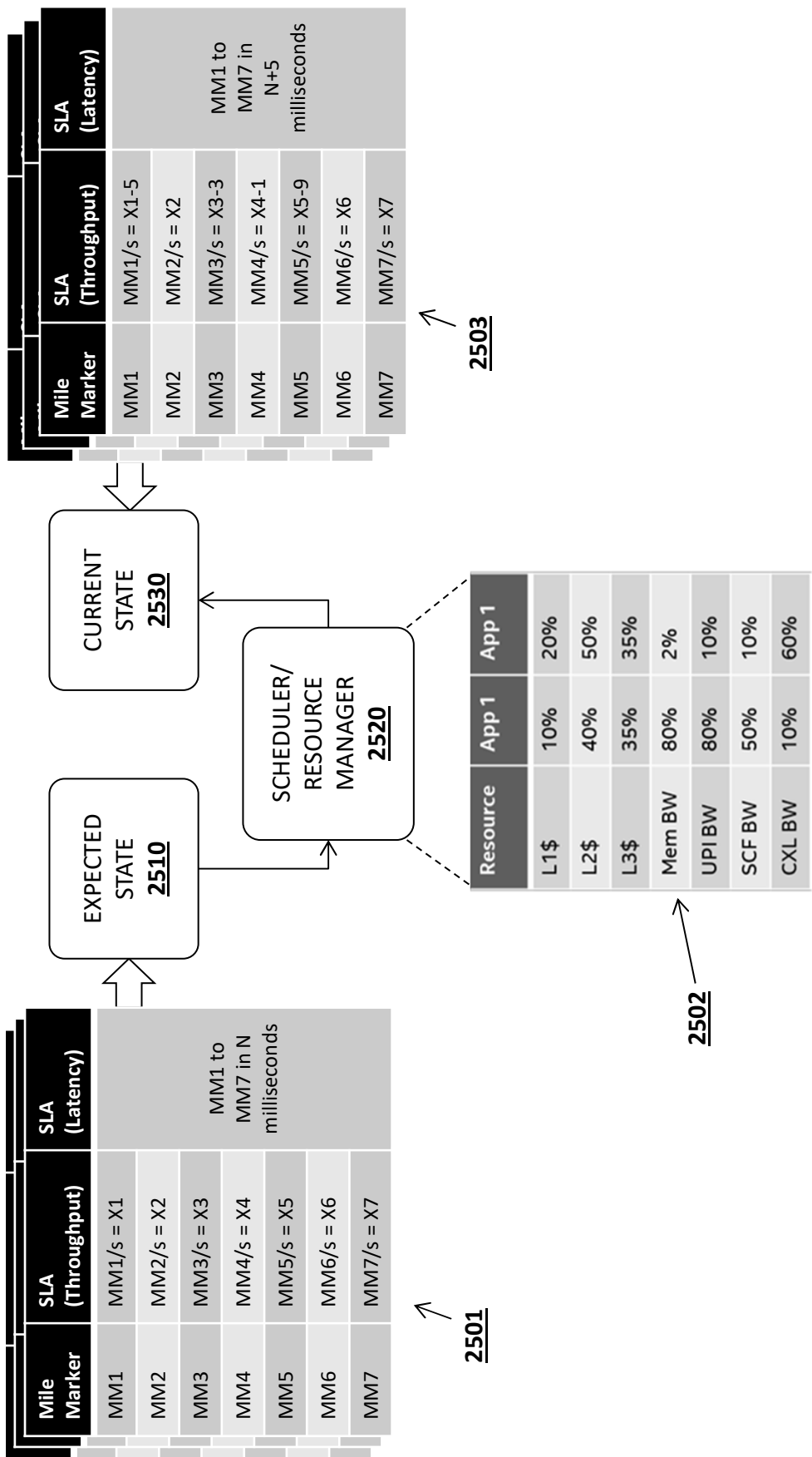
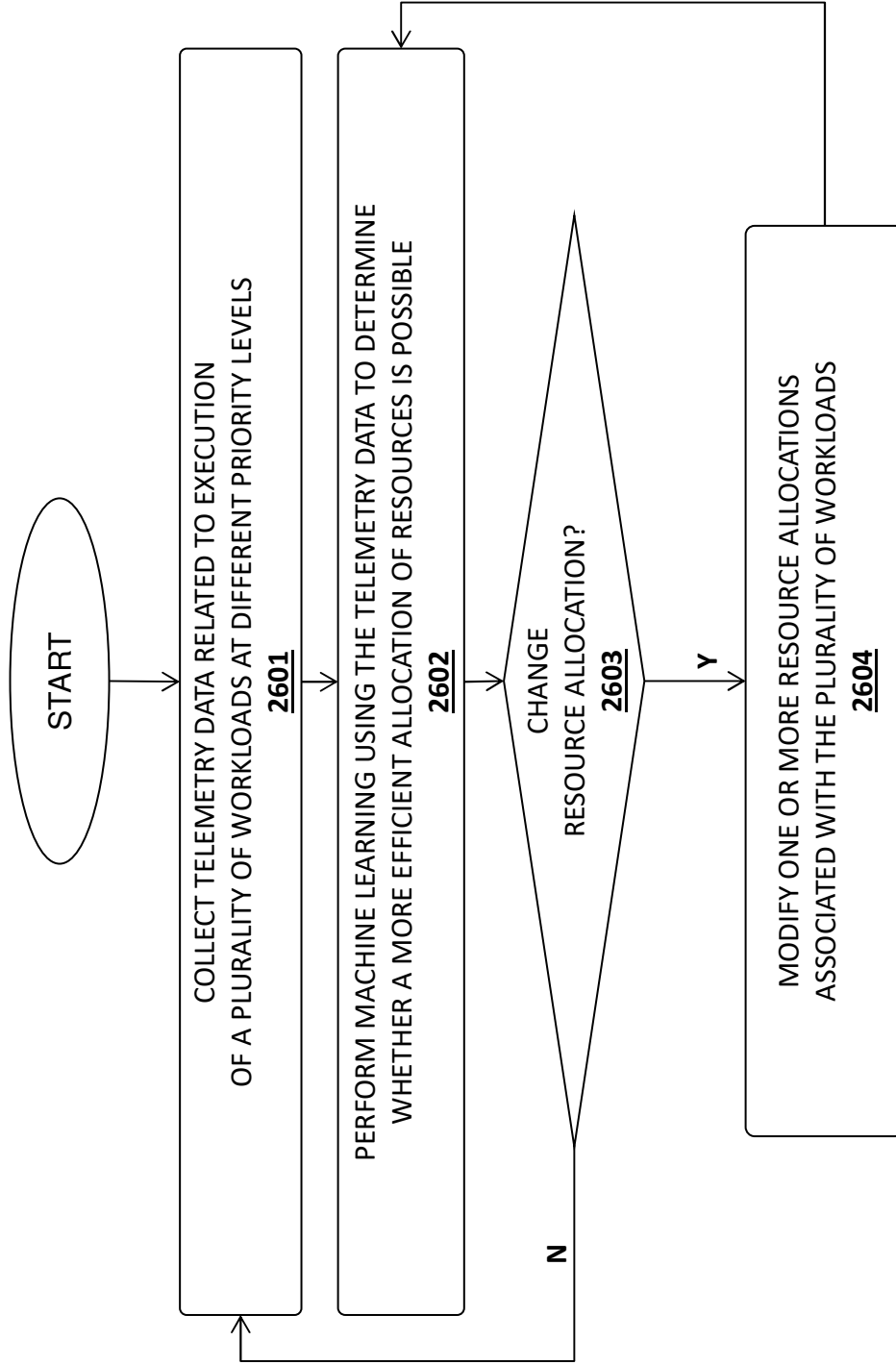


Fig. 25

**Fig. 26**

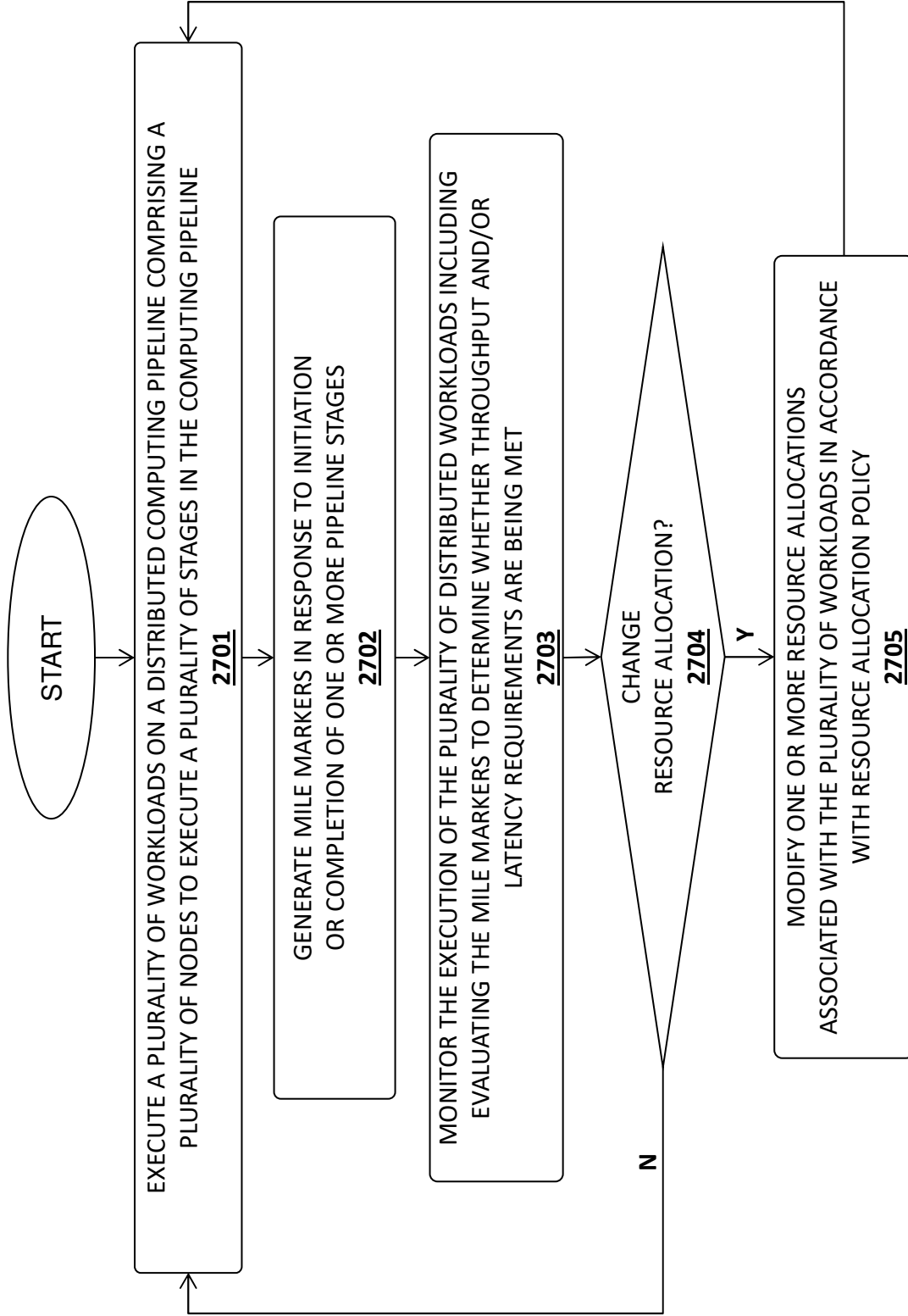


Fig. 27