

FIG. 1A

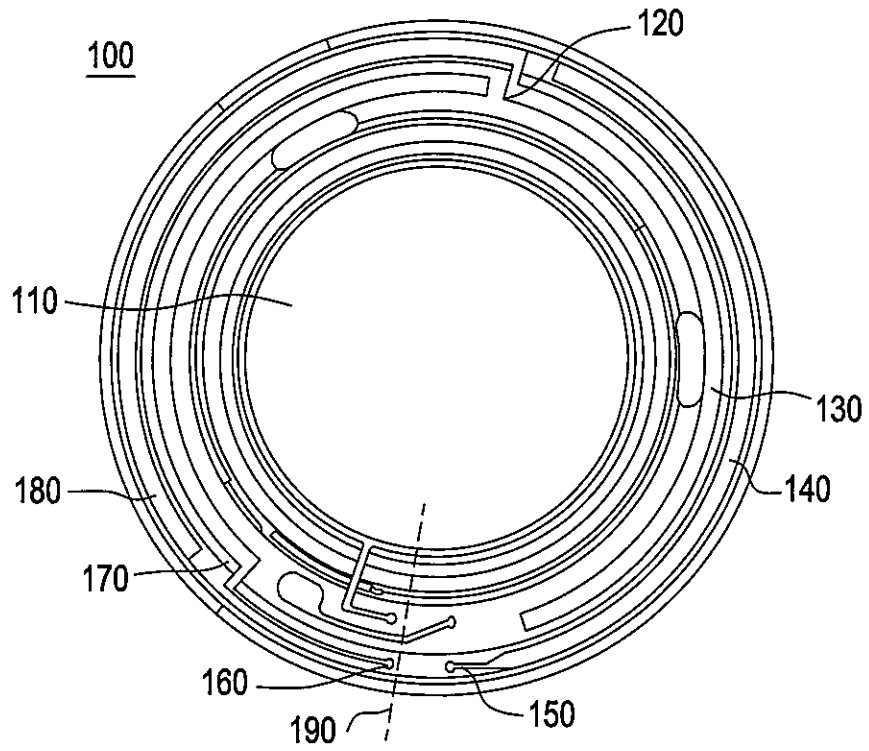


FIG. 1B

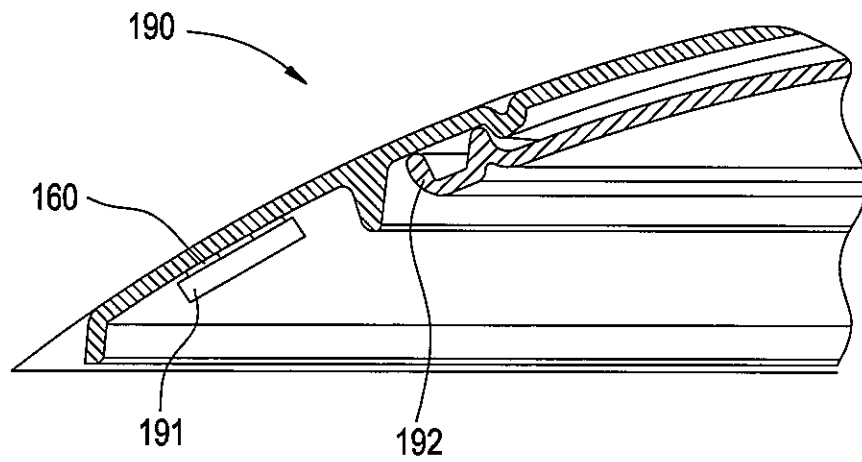
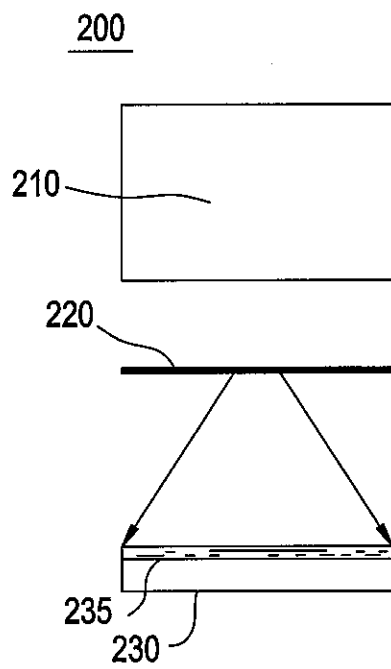
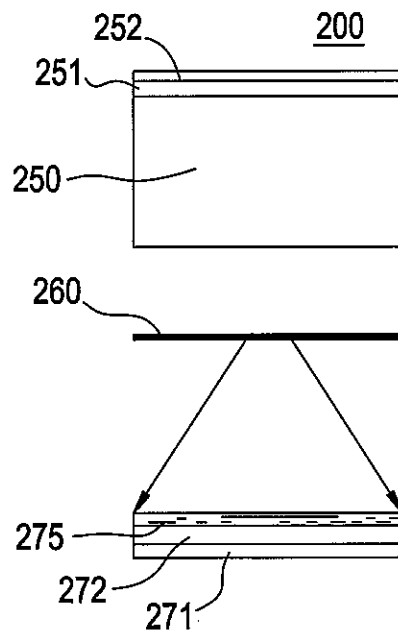


FIG. 2A



Thinning of
Semiconductor

FIG. 2B



Thinning of Semiconductor
with Semiconductor on
Oxide Substrates

FIG. 3A

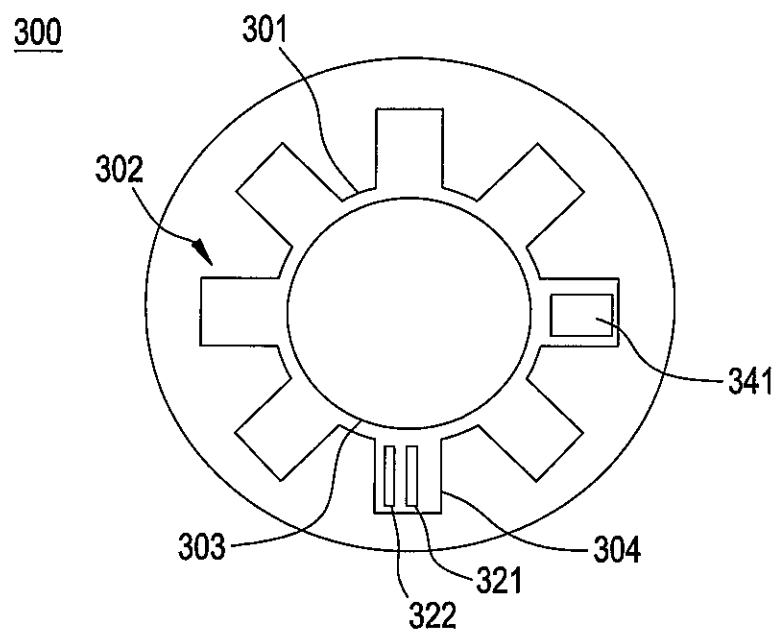


FIG. 3B

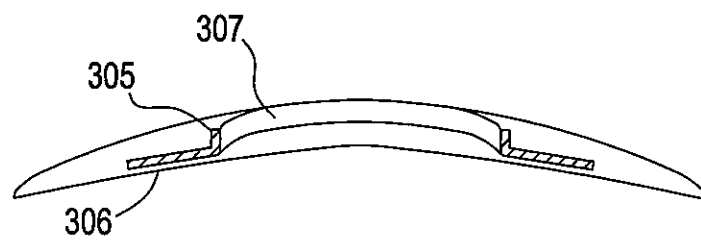


FIG. 4A

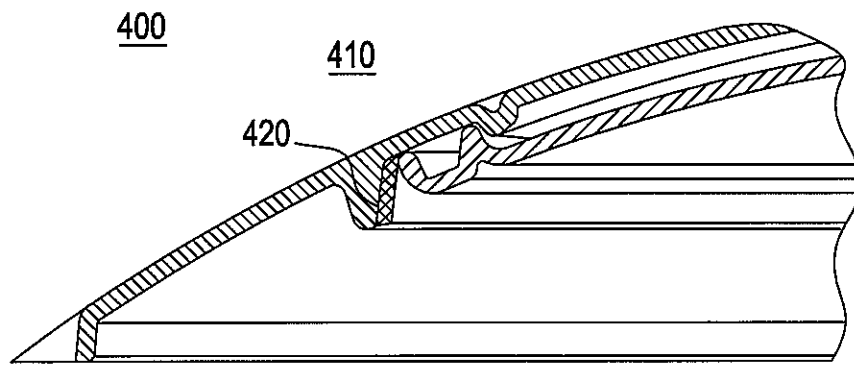


FIG. 4B

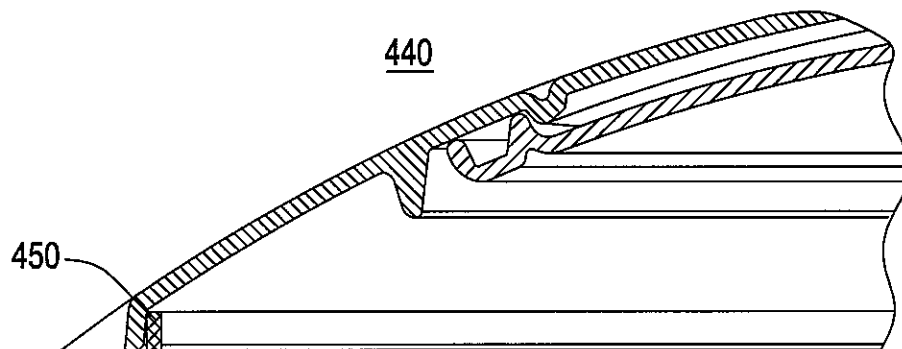


FIG. 5A

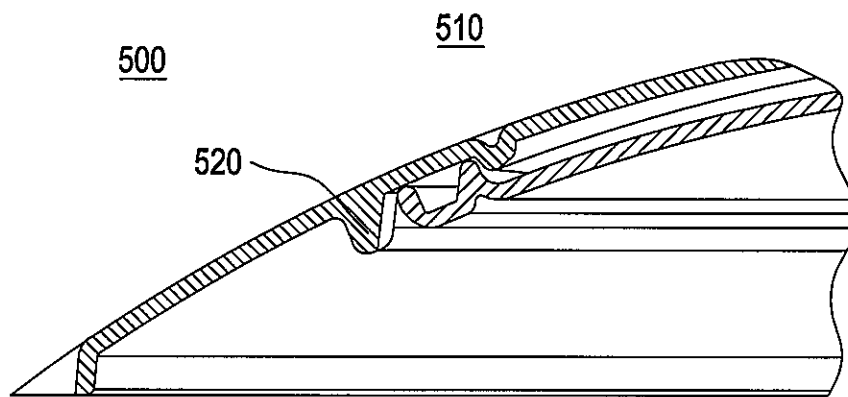


FIG. 5B

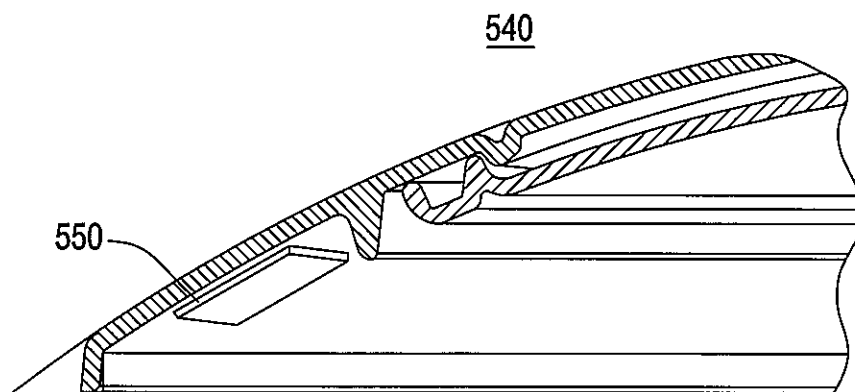


FIG. 6

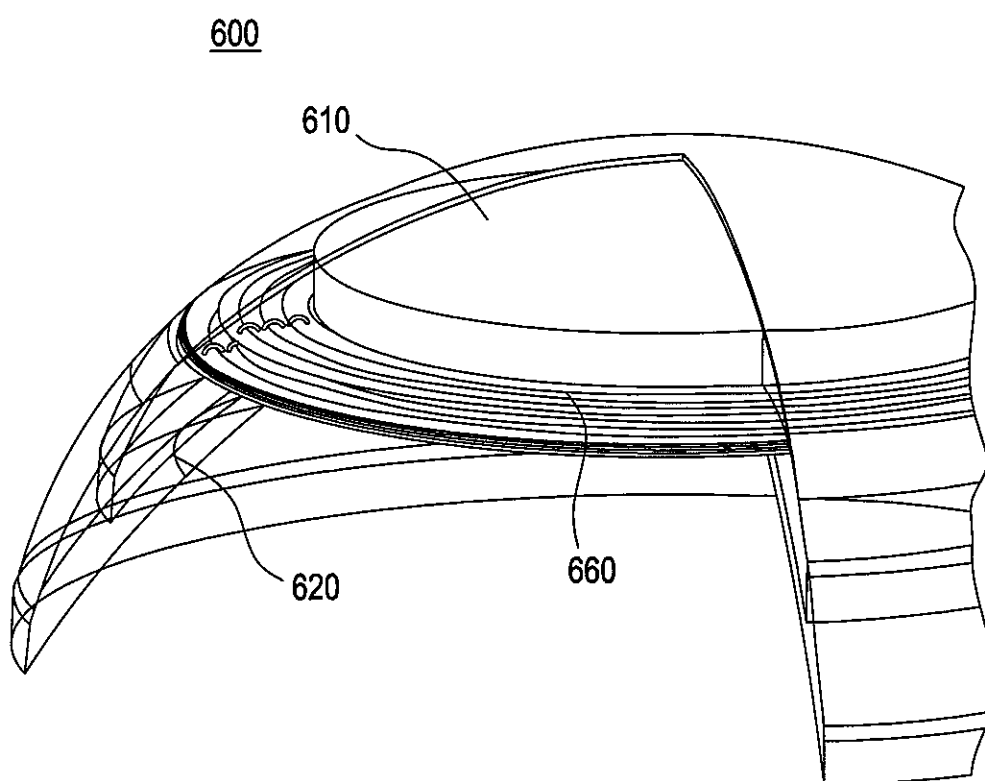


FIG. 7

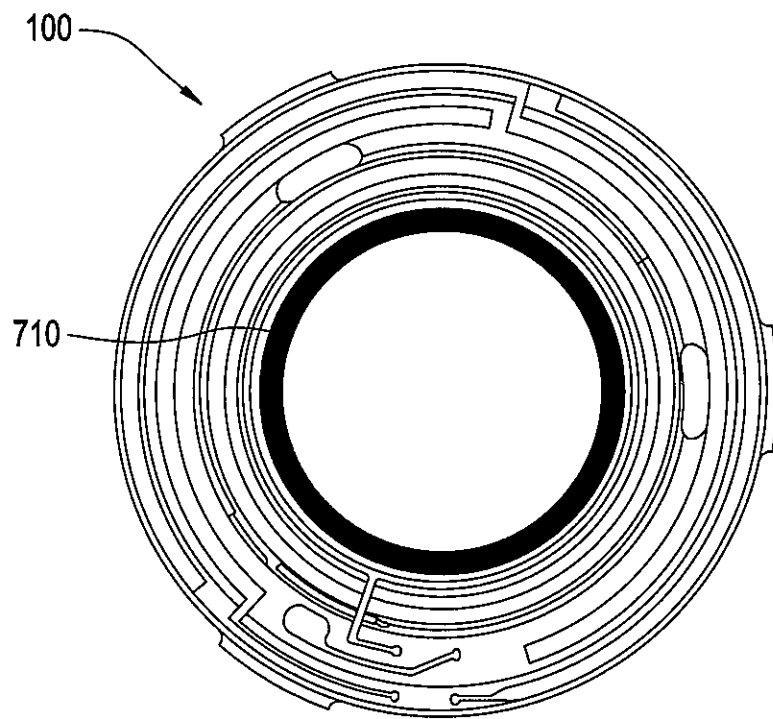


FIG. 8A

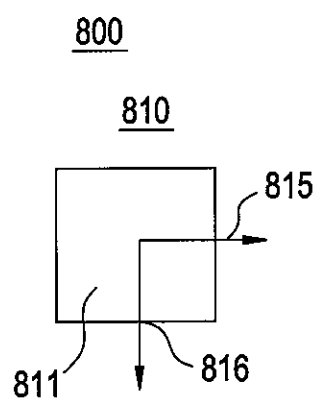


FIG. 8B

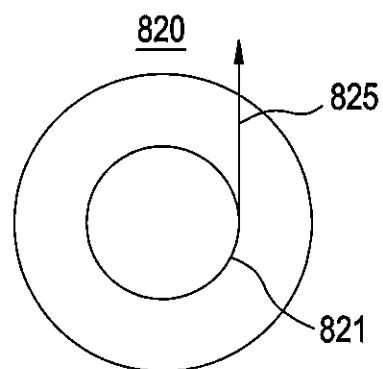


FIG. 8C

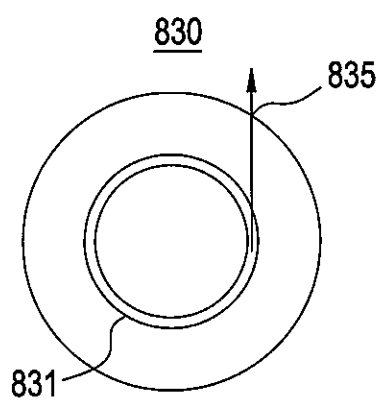


FIG. 8D

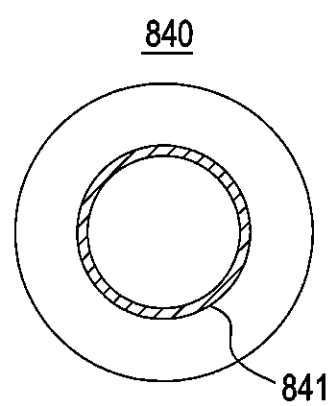


FIG. 9A

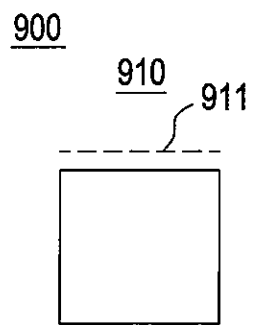


FIG. 9B

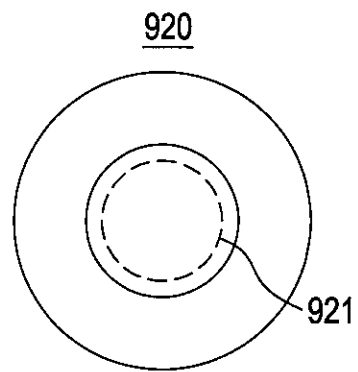


FIG. 9C

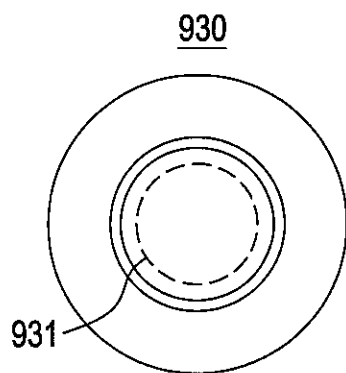


FIG. 9D

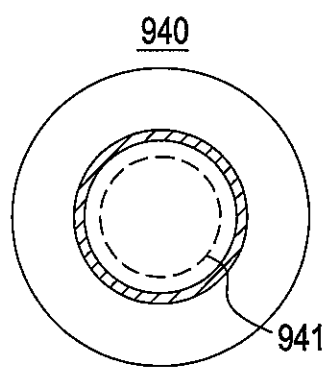


FIG. 10A FIG. 10B FIG. 10C

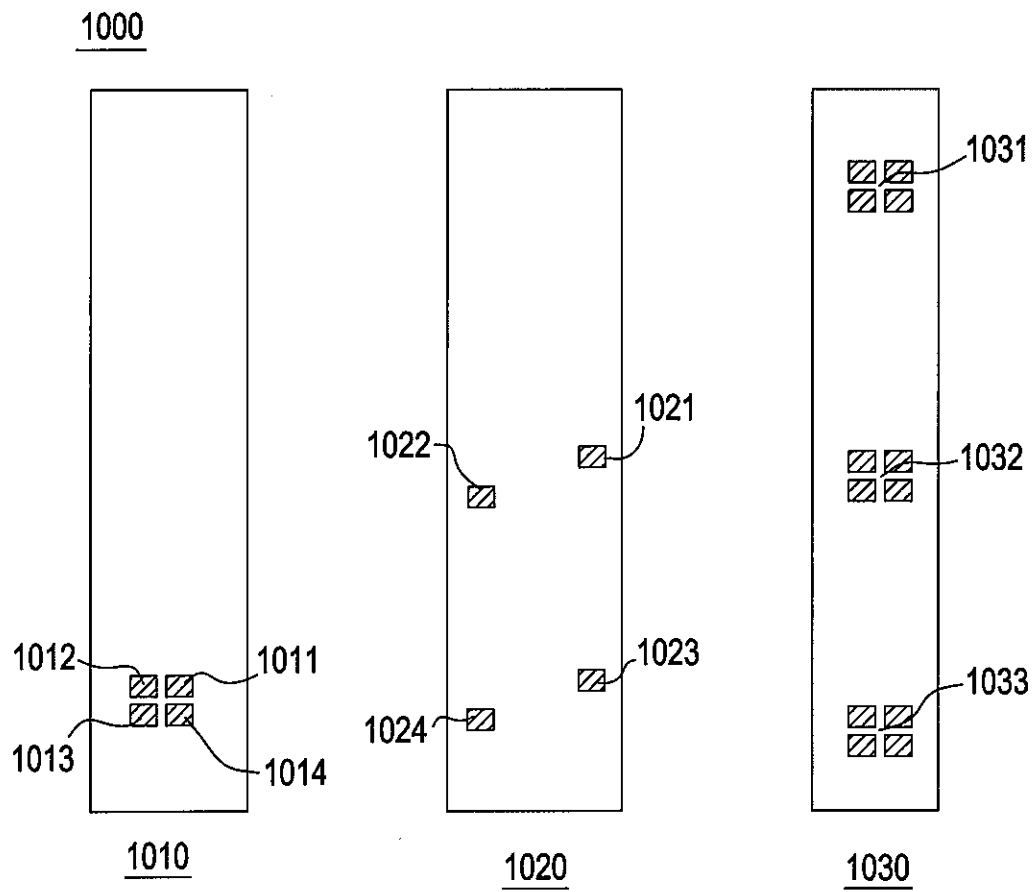


FIG. 11A FIG. 11B FIG. 11C

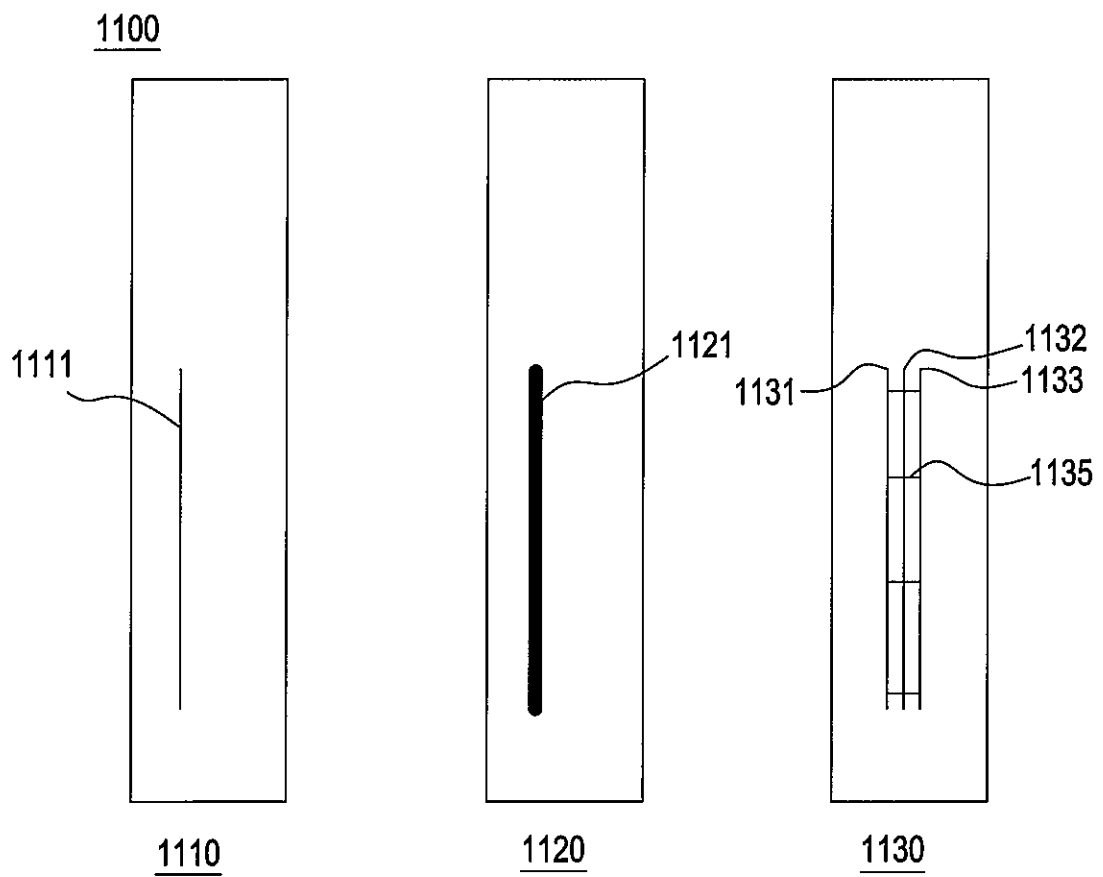
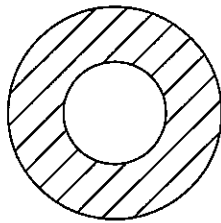


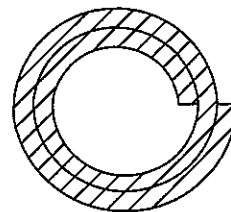
FIG. 12A

1200



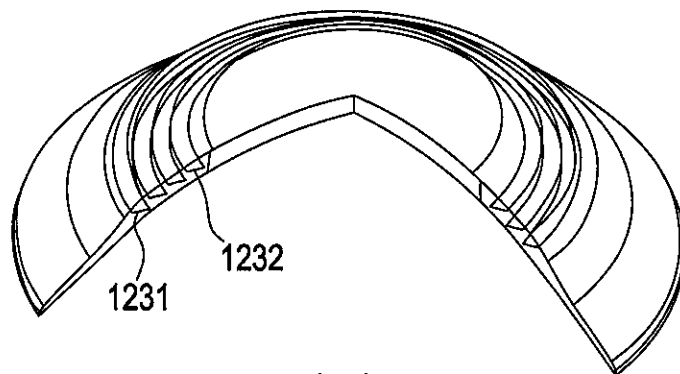
1210

FIG. 12B



1220

FIG. 12C



1230