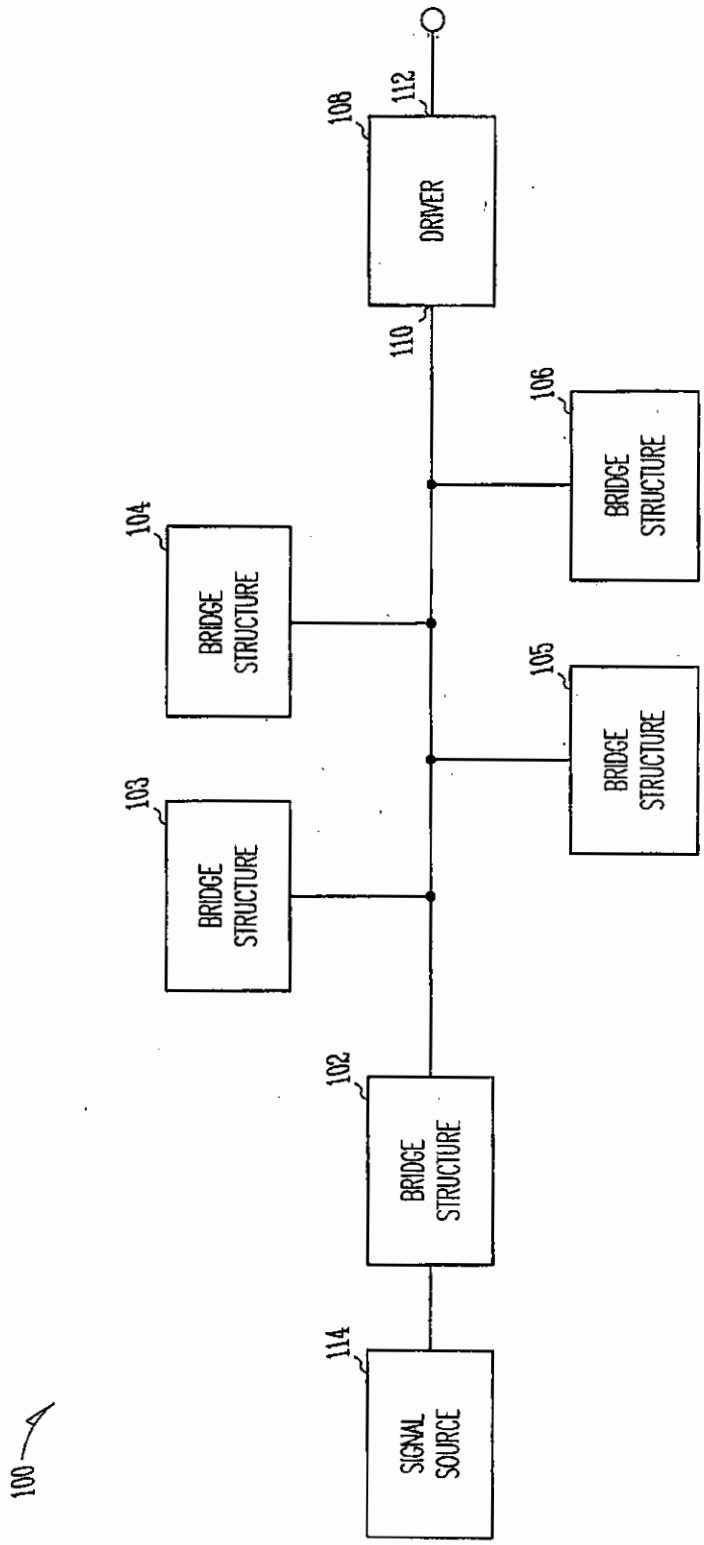

100
SIGNAL SOURCE
114
BRIDGE STRUCTURE
102
BRIDGE STRUCTURE
103
BRIDGE STRUCTURE
104
BRIDGE STRUCTURE
105
BRIDGE STRUCTURE
106
110
DRIVER
108
112

Fig. 1A

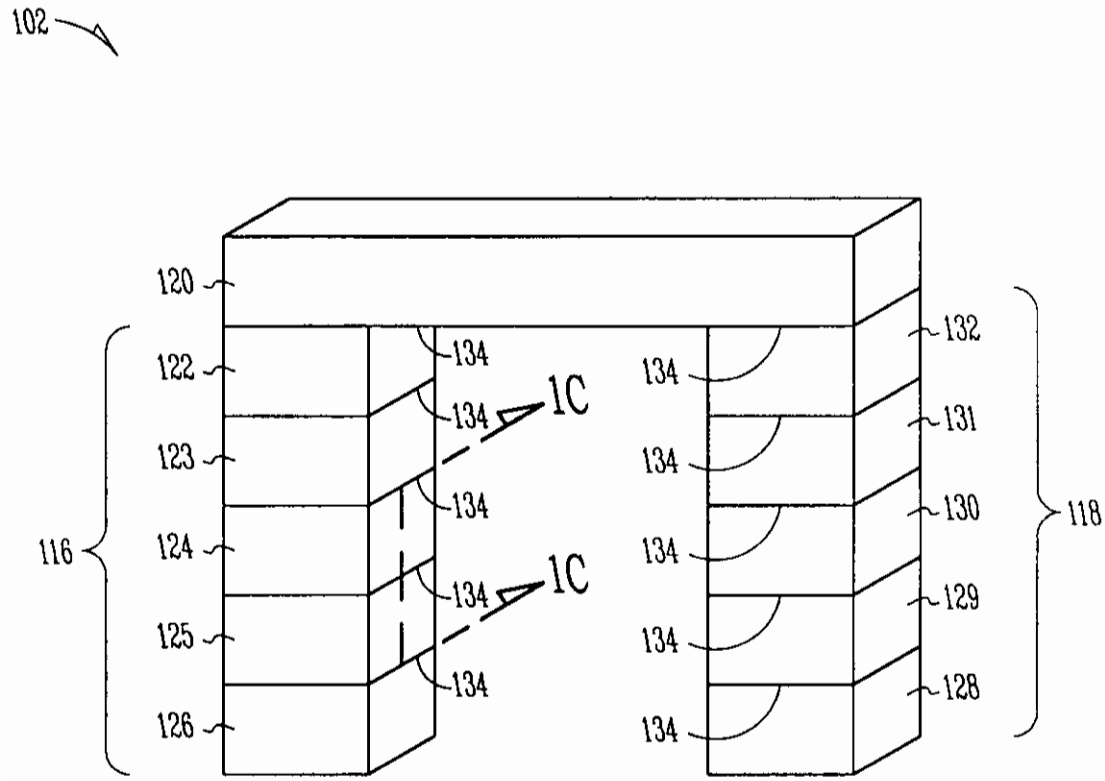

102
120
122
123
124
125
126
116
134
1C
132
131
130
129
128
118

Fig. 1B

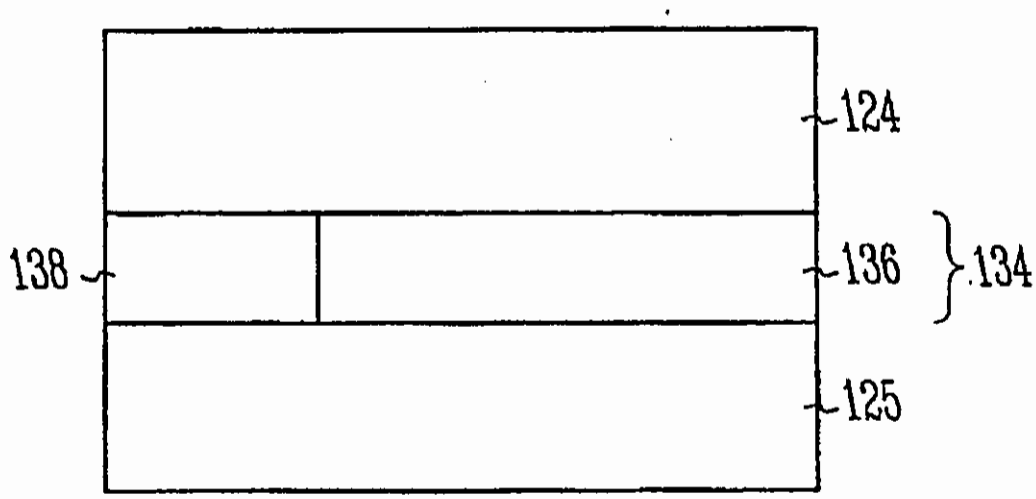

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138
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134
125

Fig. 1C

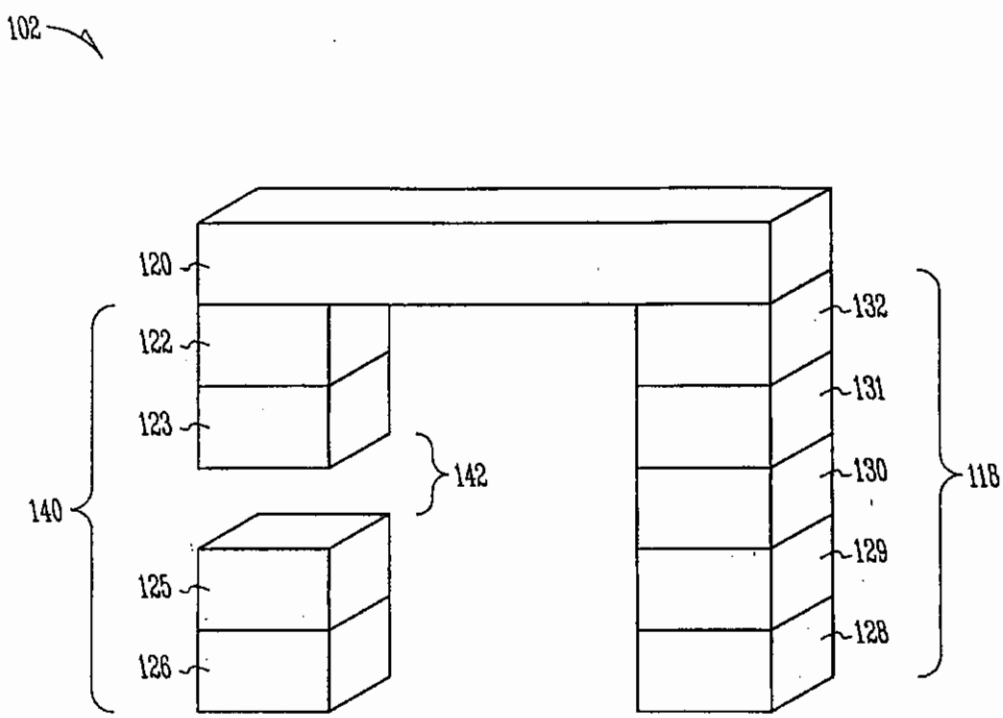


Fig. 1D

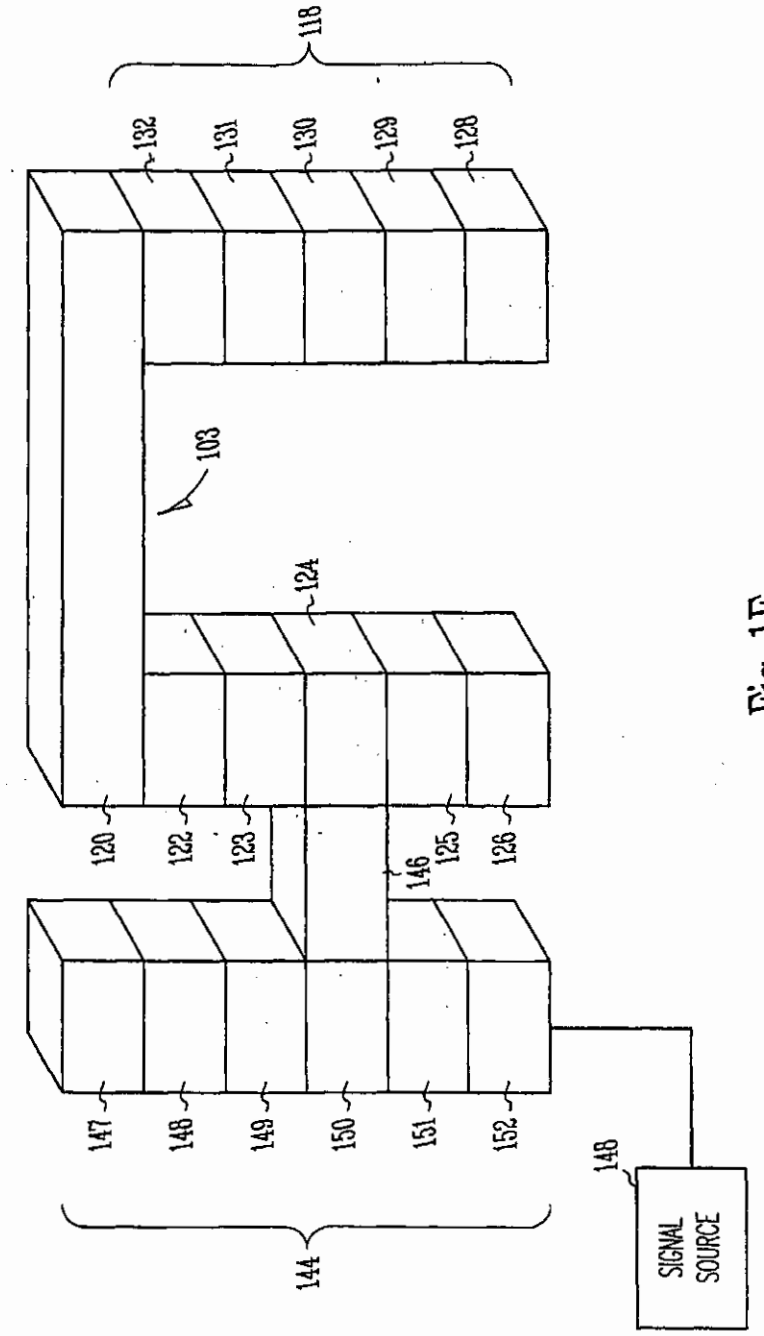

118
132
131
130
129
128
103
124
120
122
123
146
125
126
147
148
149
150
151
152
144
148
SIGNAL SOURCE

Fig. 1E

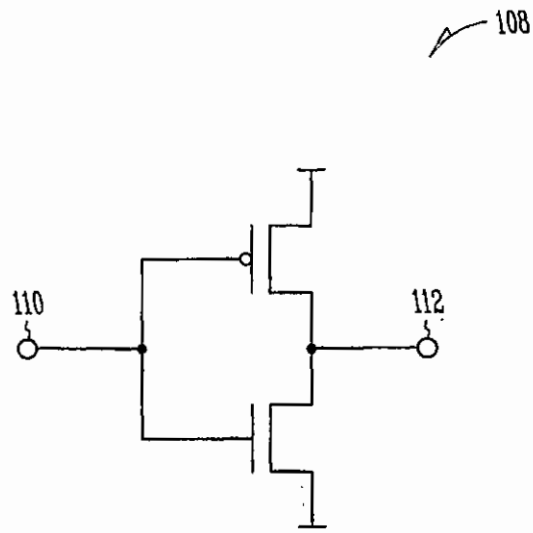

108
110
112

Fig. 1F

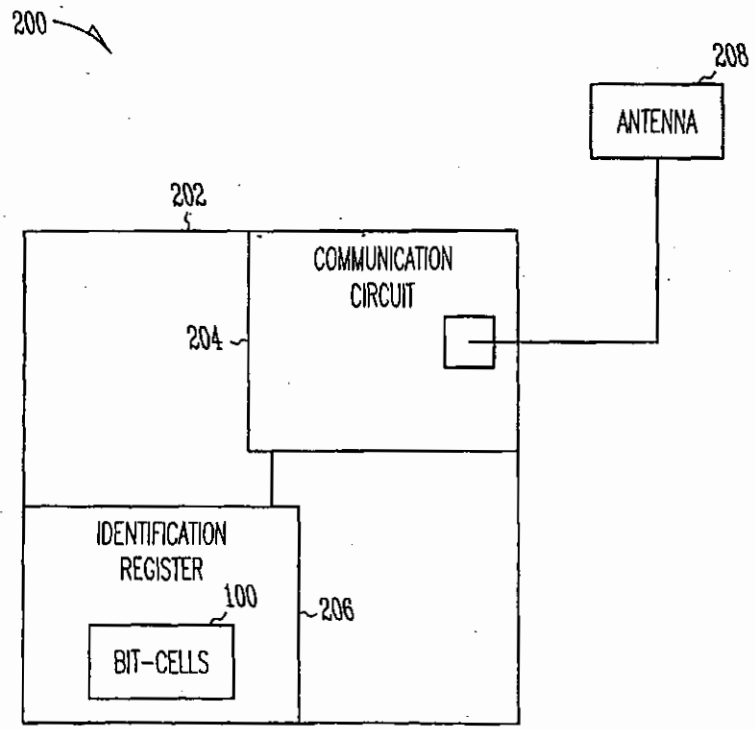

200
208
ANTENNA
202
COMMUNICATION CIRCUIT
204
IDENTIFICATION REGISTER
100
206
BIT-CELLS

Fig. 2

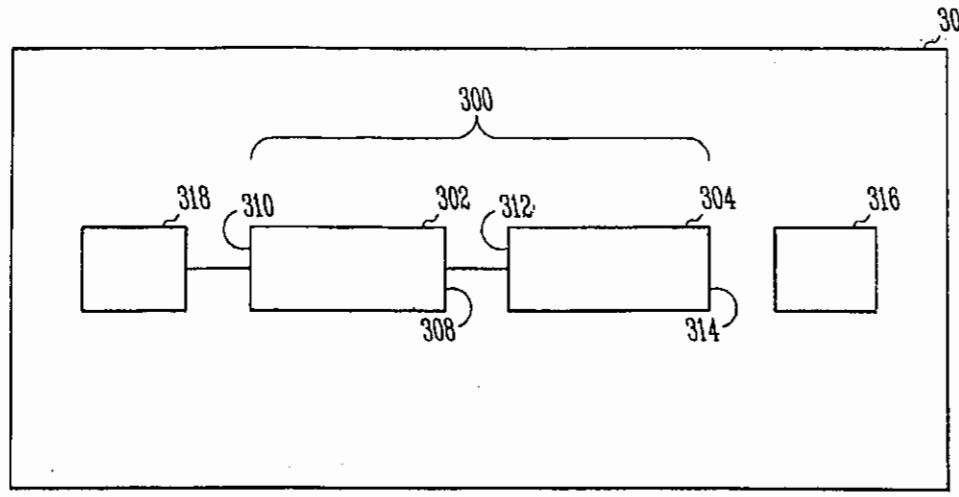

306
300
318
310
302
312
304
316
308
314

Fig. 3

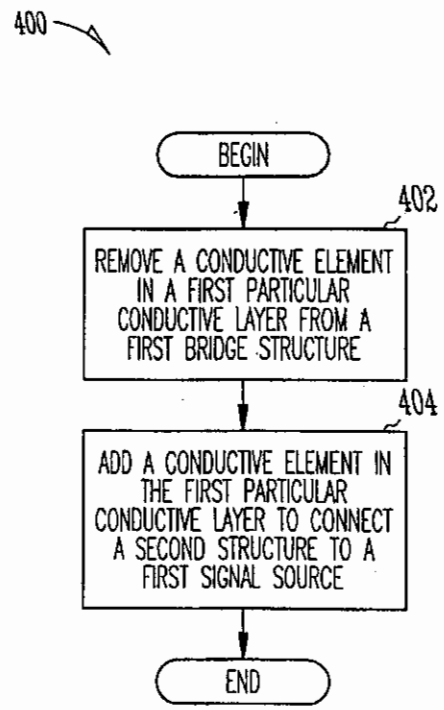

400
BEGIN
402
REMOVE A CONDUCTIVE ELEMENT IN A FIRST PARTICULAR CONDUCTIVE LAYER FROM A FIRST BRIDGE STRUCTURE
404
ADD A CONDUCTIVE ELEMENT IN THE FIRST PARTICULAR CONDUCTIVE LAYER TO CONNECT A SECOND STRUCTURE TO A FIRST SIGNAL SOURCE
END

Fig. 4

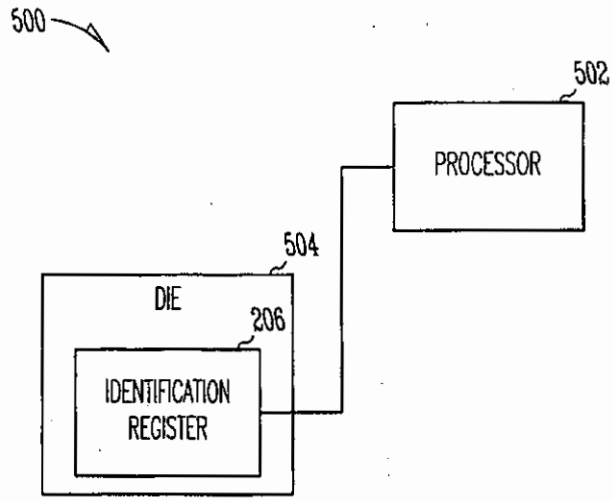

500
502
PROCESSOR
504
DIE
206
IDENTIFICATION REGISTER

Fig. 5

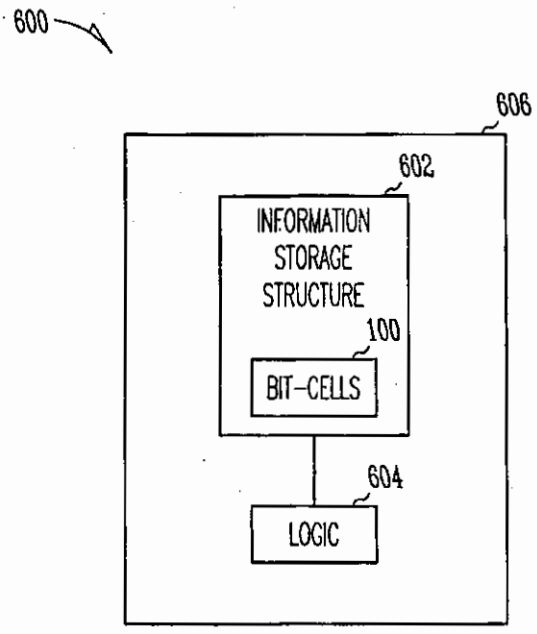

600
606
602
INFORMATION STORAGE STRUCTURE
100
BIT-CELLS
604
LOGIC

Fig. 6