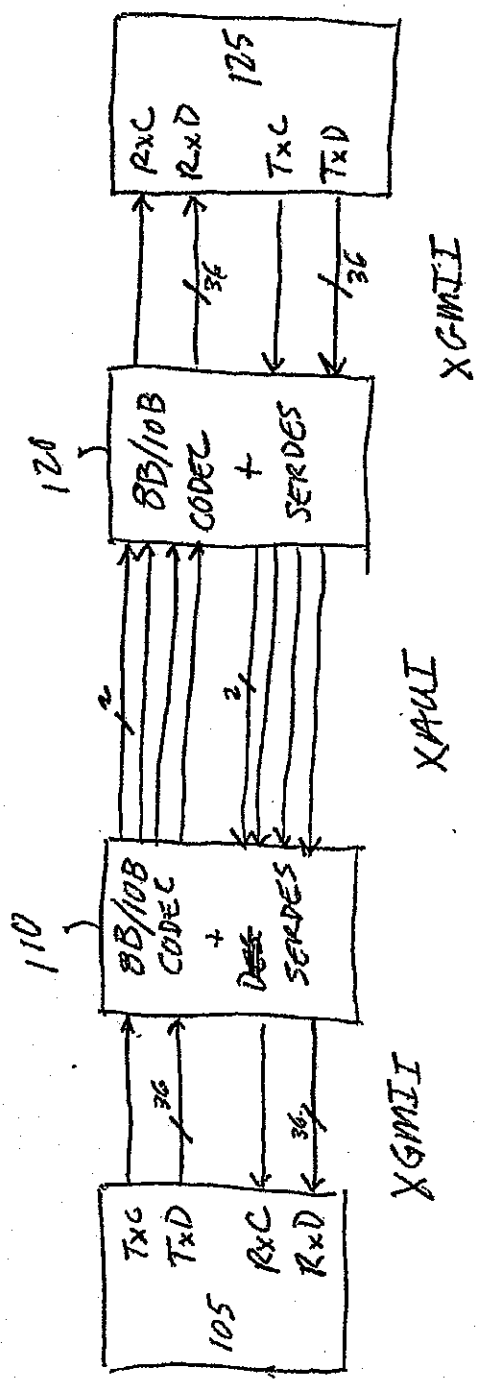


FIG 1 - PRIOR ART

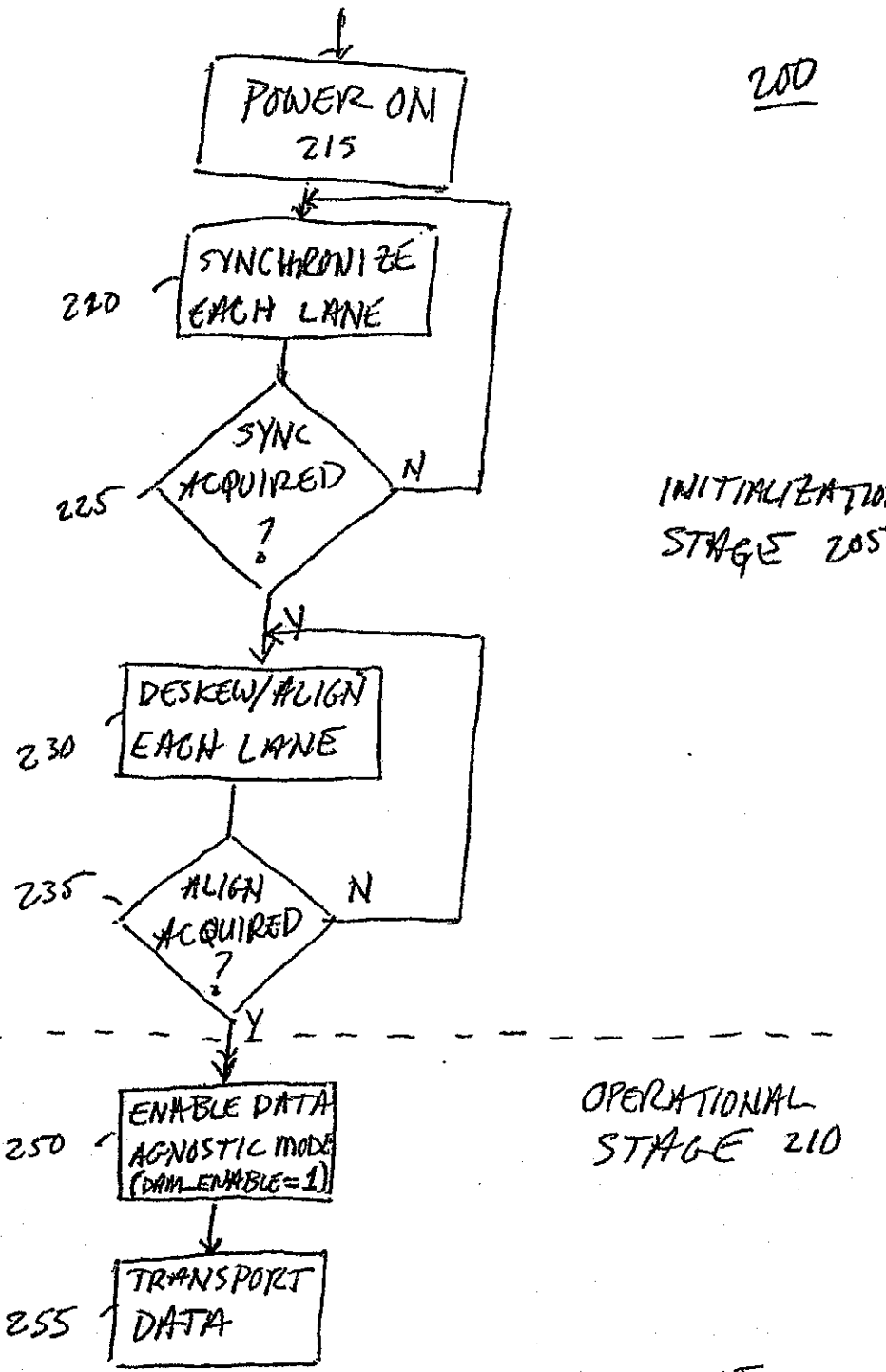

200
POWER ON
215
SYNCHRONIZE
EACH LANE
220
SYNC
ACQUIRED
?
225
N
Y
INITIALIZATION
STAGE 205
DESKEW/ALIGN
EACH LANE
230
ALIGN
ACQUIRED
?
235
N
Y
ENABLE DATA
AGNOSTIC MODE
(DAM_ENABLE=1)
250
TRANSPORT
DATA
255
OPERATIONAL
STAGE 210

FIG. 2

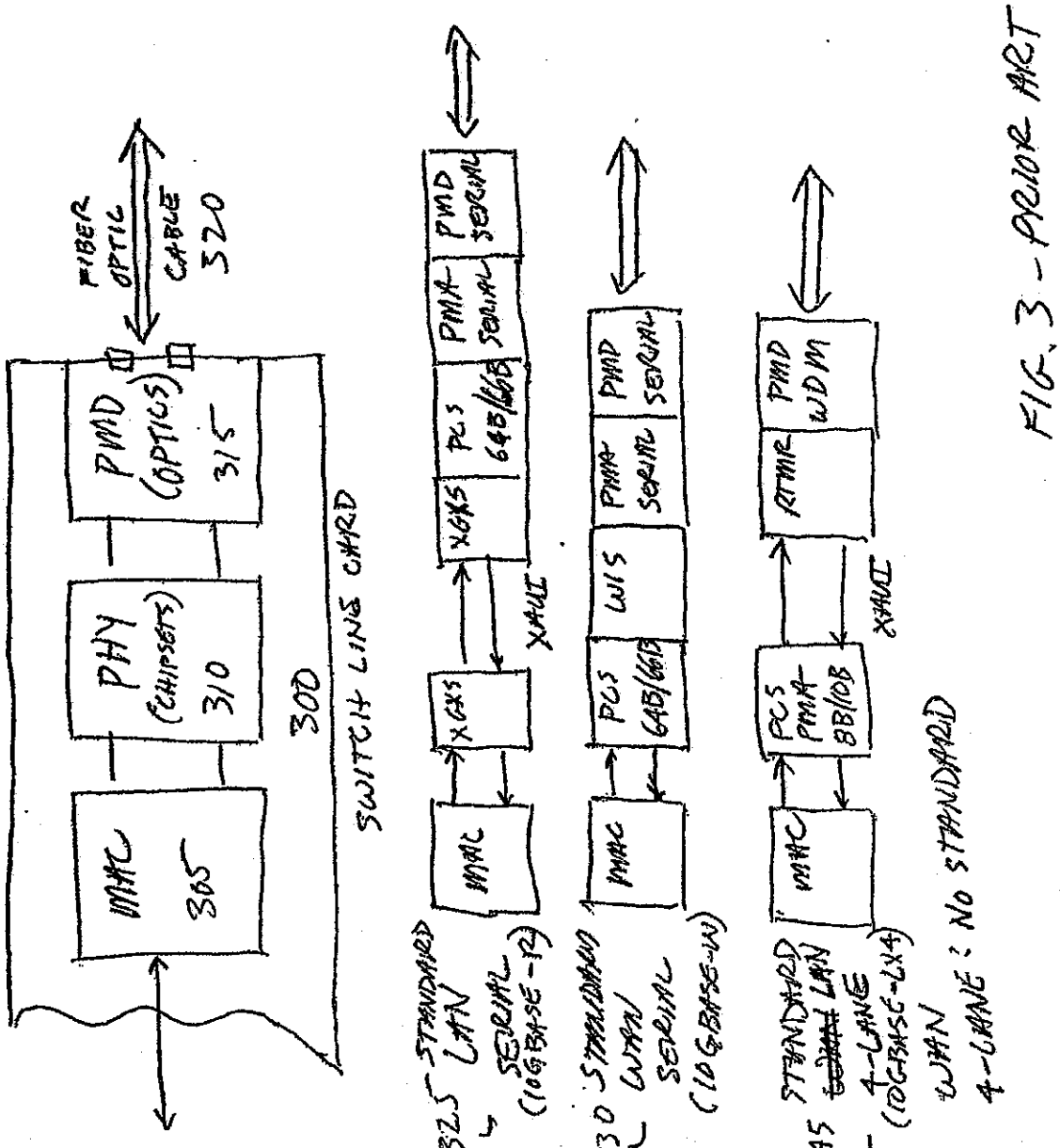


FIG. 3 - PRIOR ART

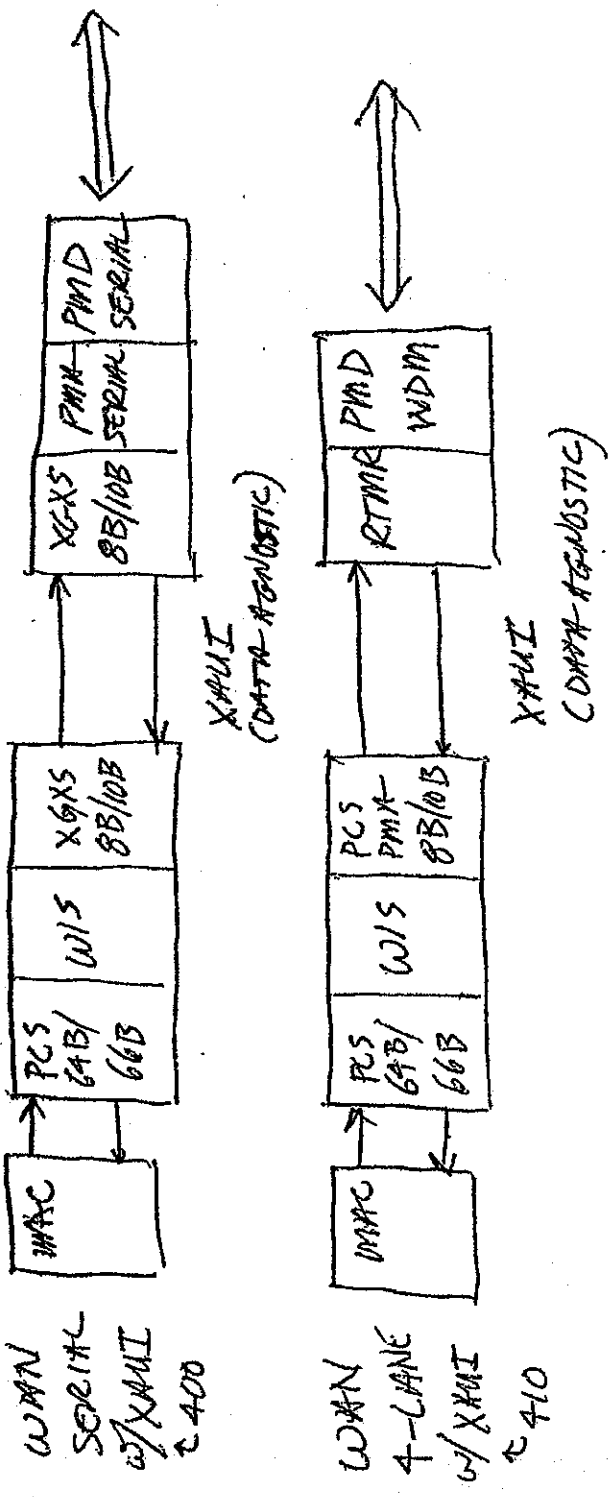

WAN SERIAL w/ XAUI
400
MAC
PCS 64B/66B
WIS
XGXS 8B/10B
XAUI (CHIP-TO-CHIP)
XGXS 8B/10B
PMA SERIAL
PMD SERIAL
WAN 4-LANE w/ XAUI
410
MAC
PCS 64B/66B
WIS
PCS PMA 8B/10B
XAUI (CHIP-TO-CHIP)
PMA
PMD WDM

FIG. 4