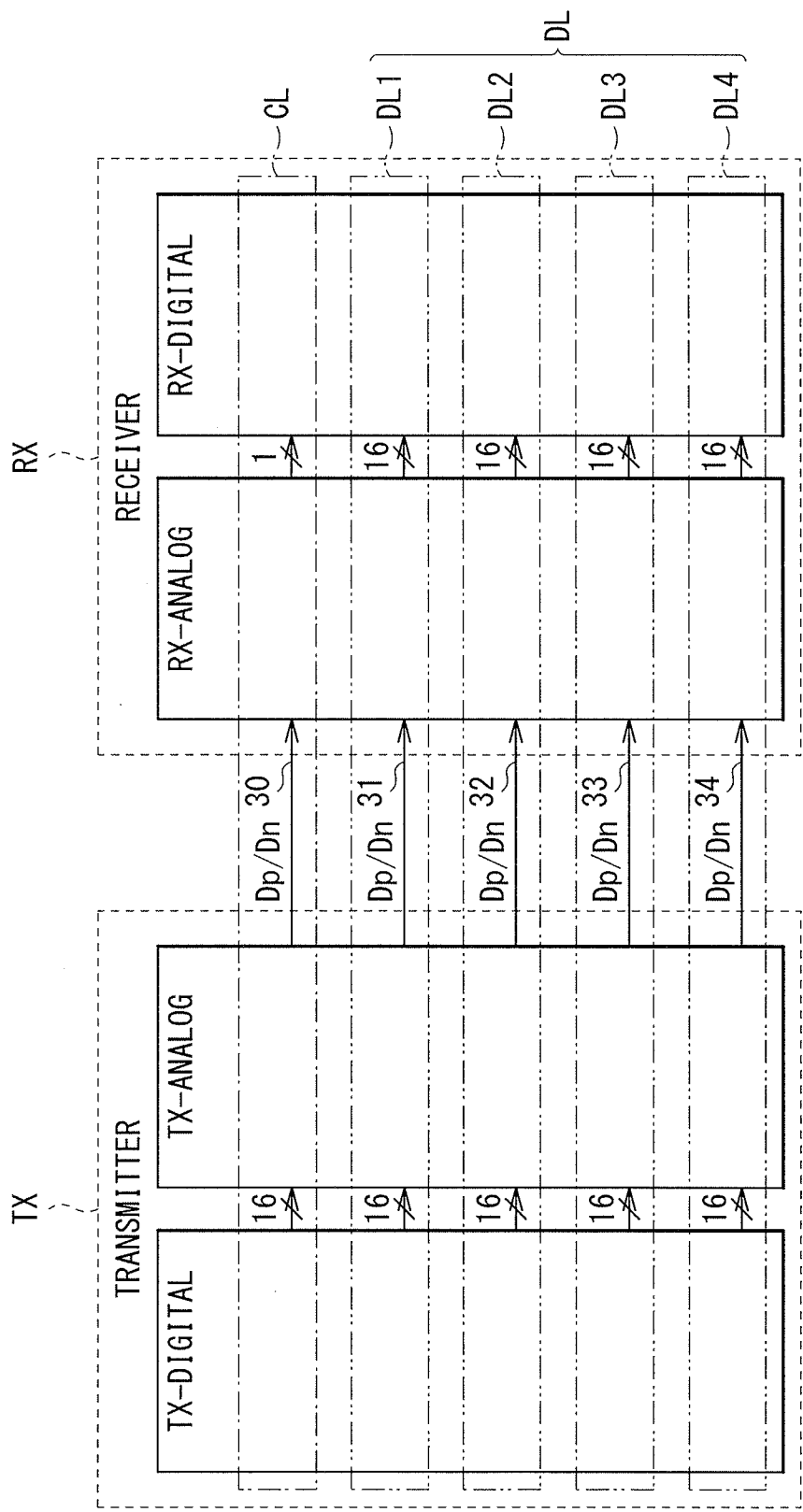
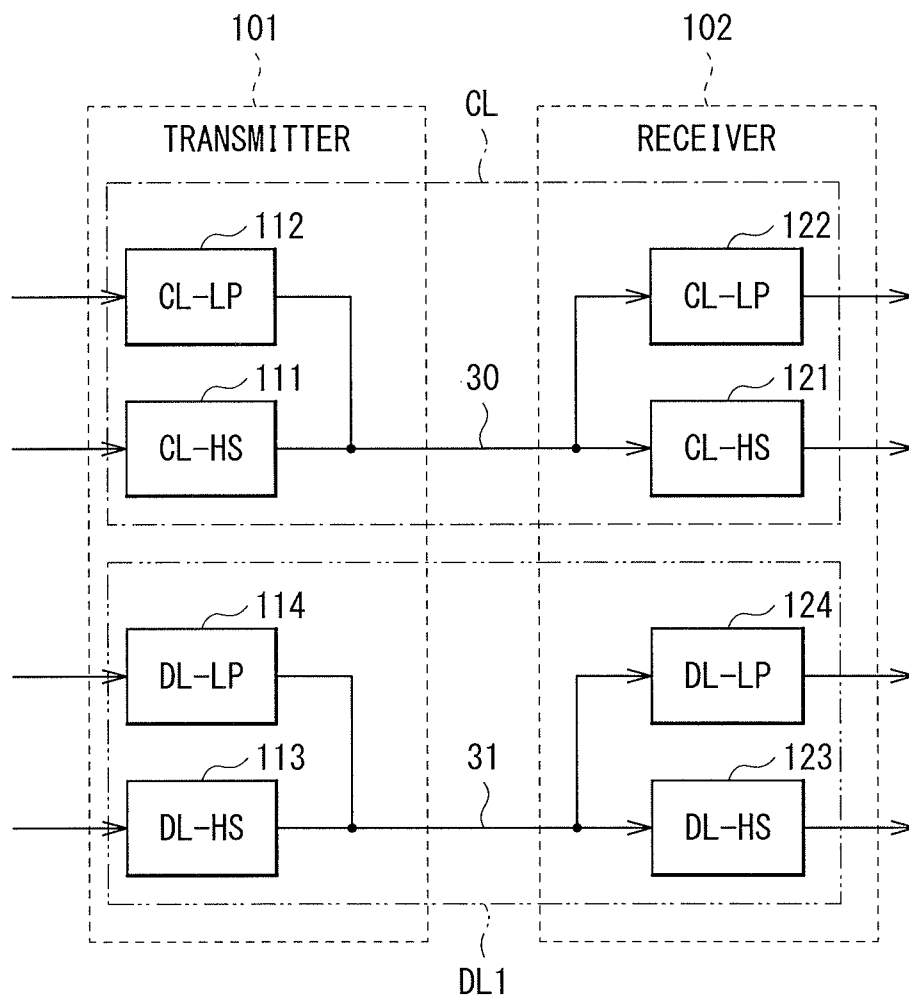


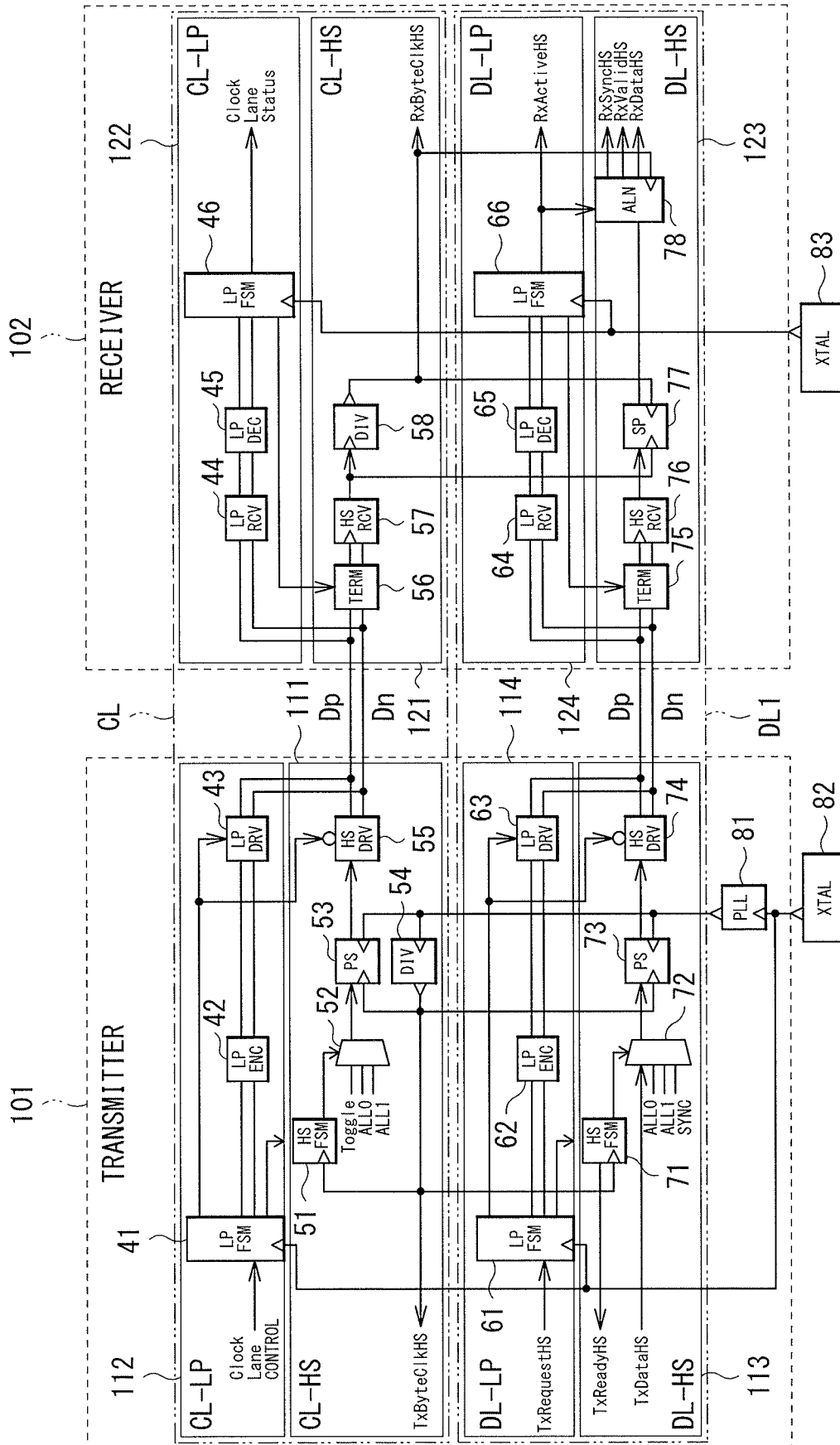
[FIG. 1]



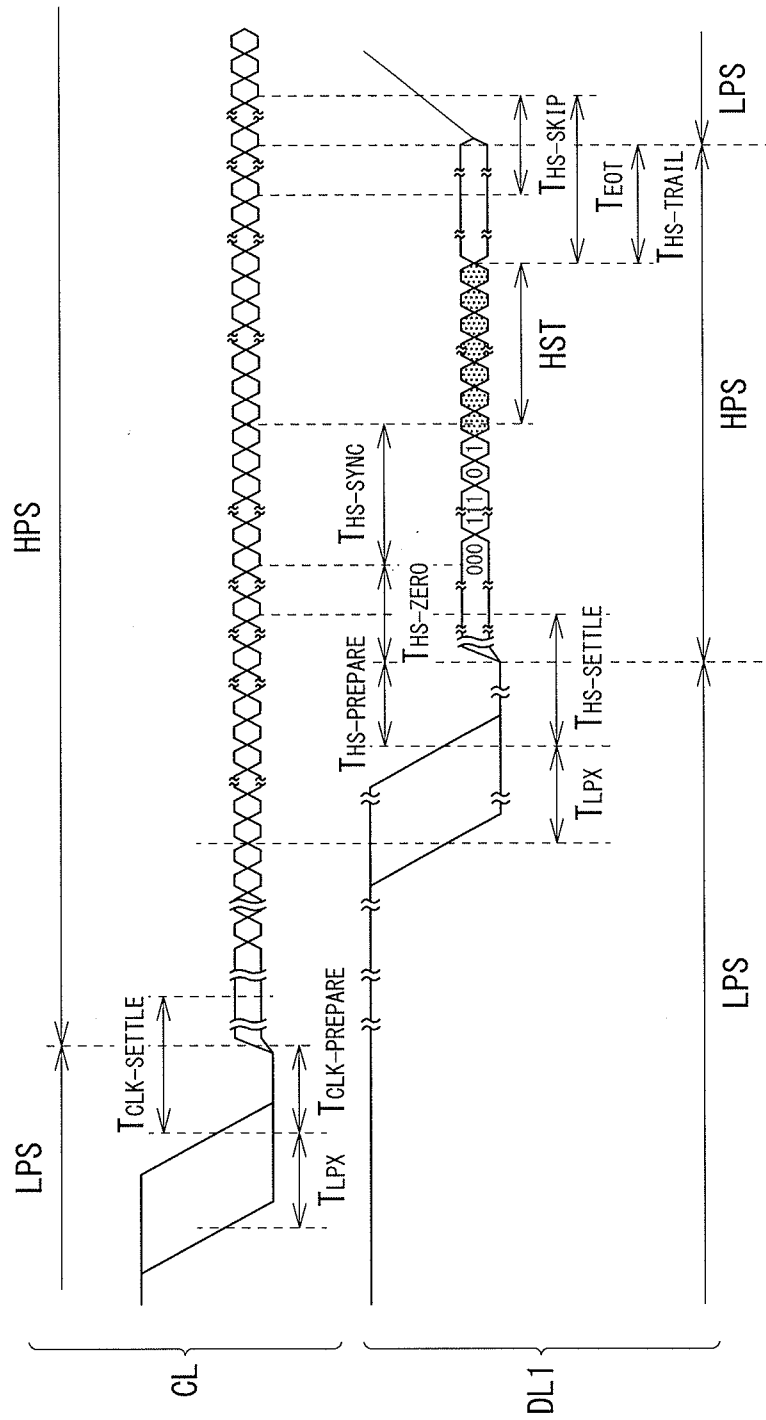
[FIG. 2]



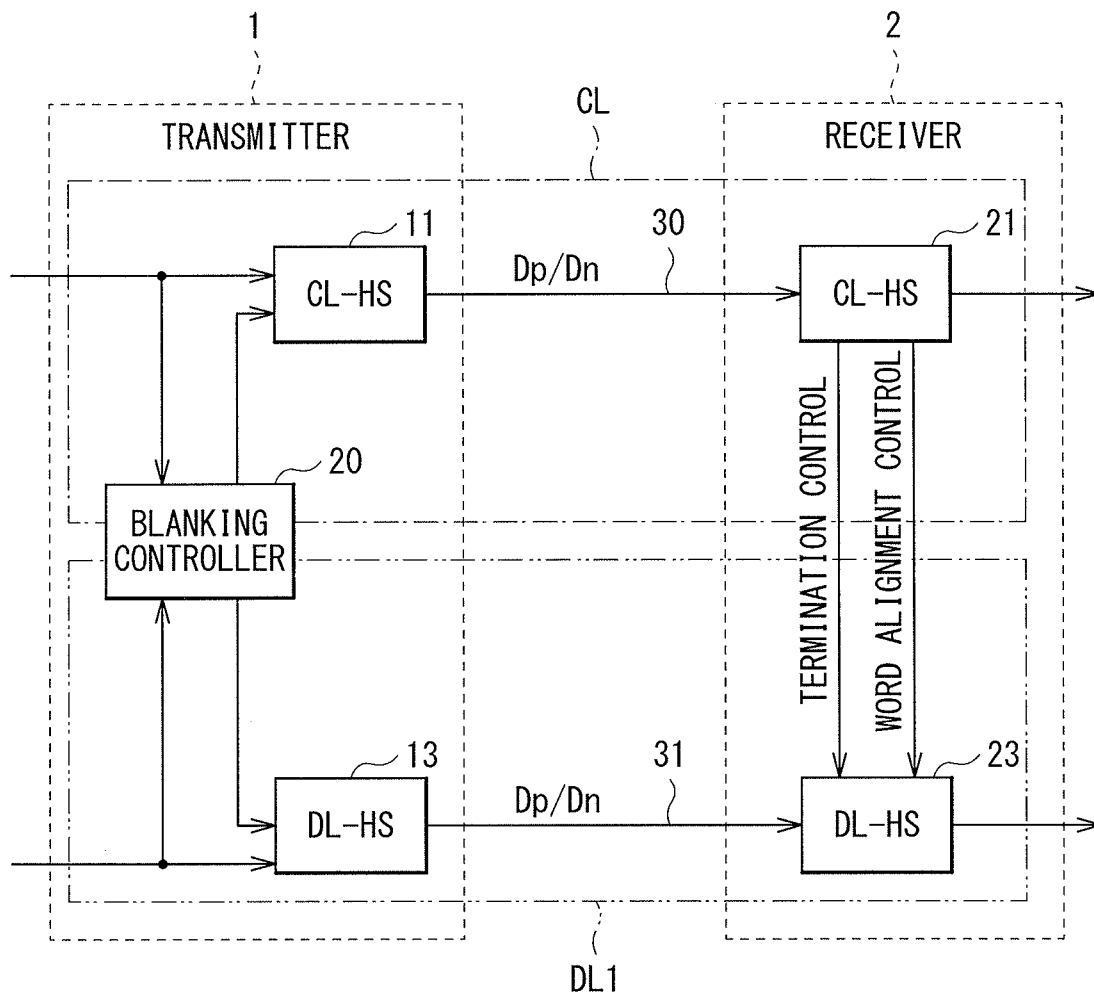
[FIG. 3]



[FIG. 4]

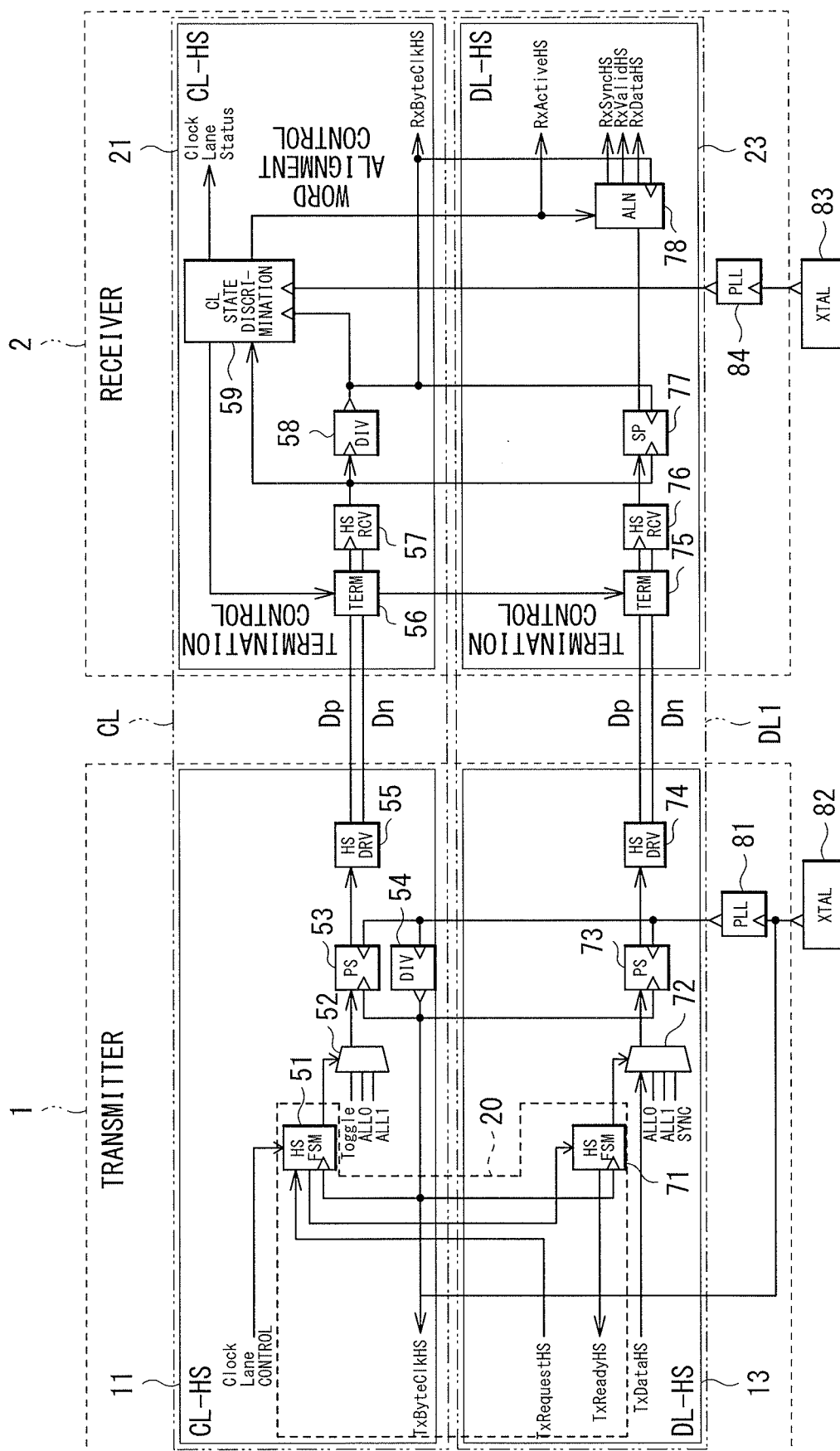


[FIG. 5]

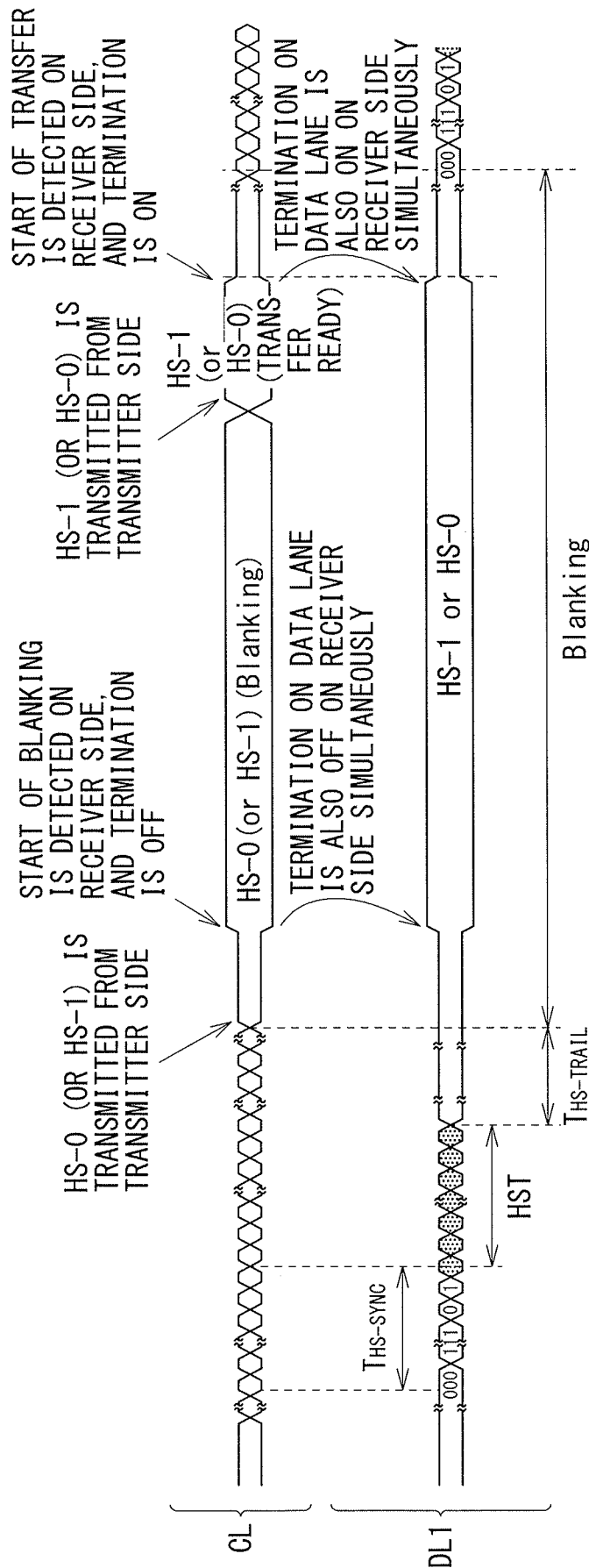


[FIG. 6]

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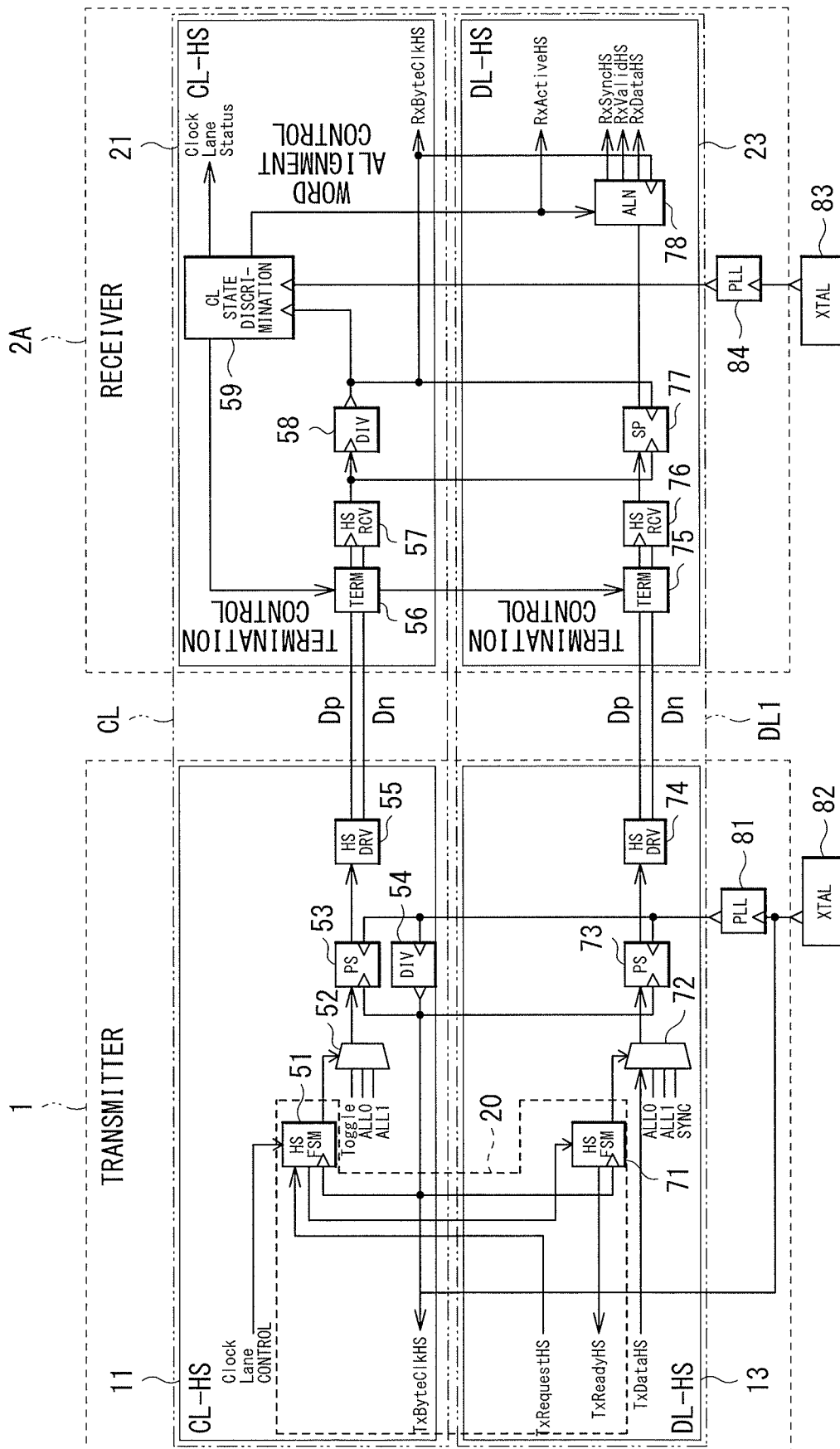
[FIG. 7]



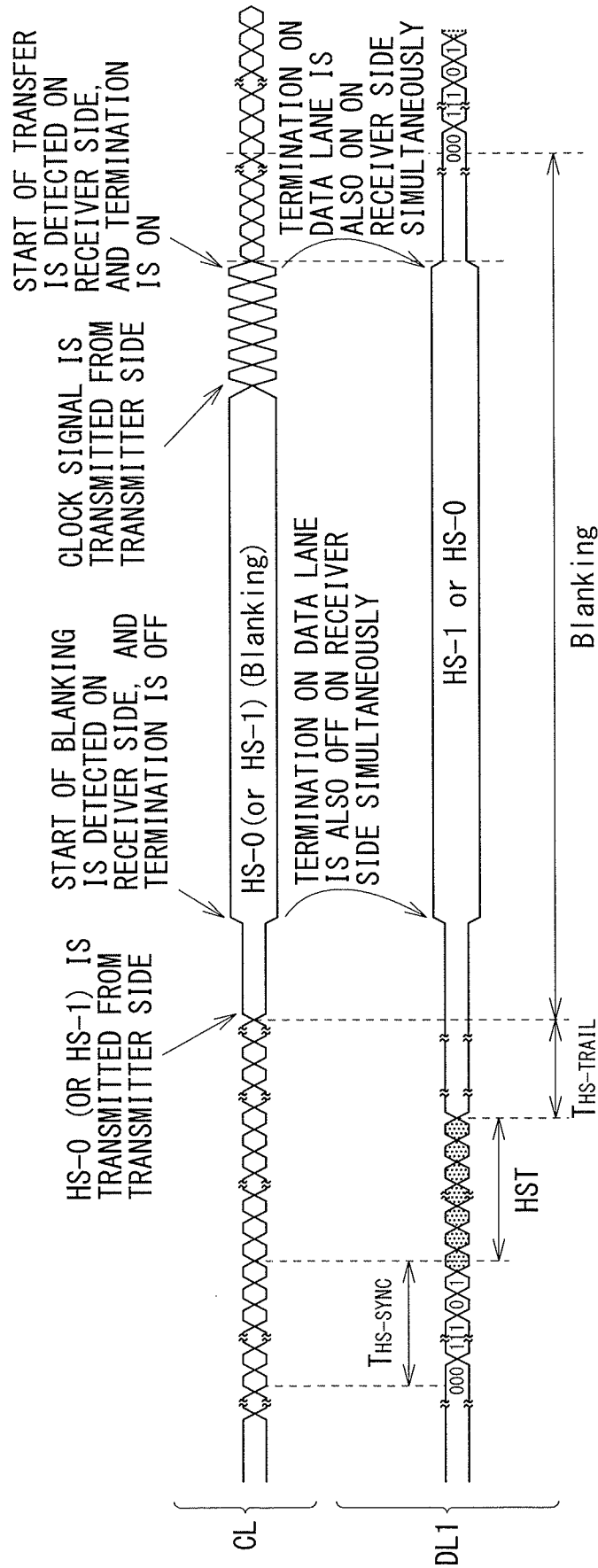
[FIG. 8]

State Code	Line Voltage Levels		High-Speed
	Dp-Line	Dn-Line	Burst Mode
HS-0	HS Low	HS High	Differential-0
HS-1	HS High	HS Low	Differential-1

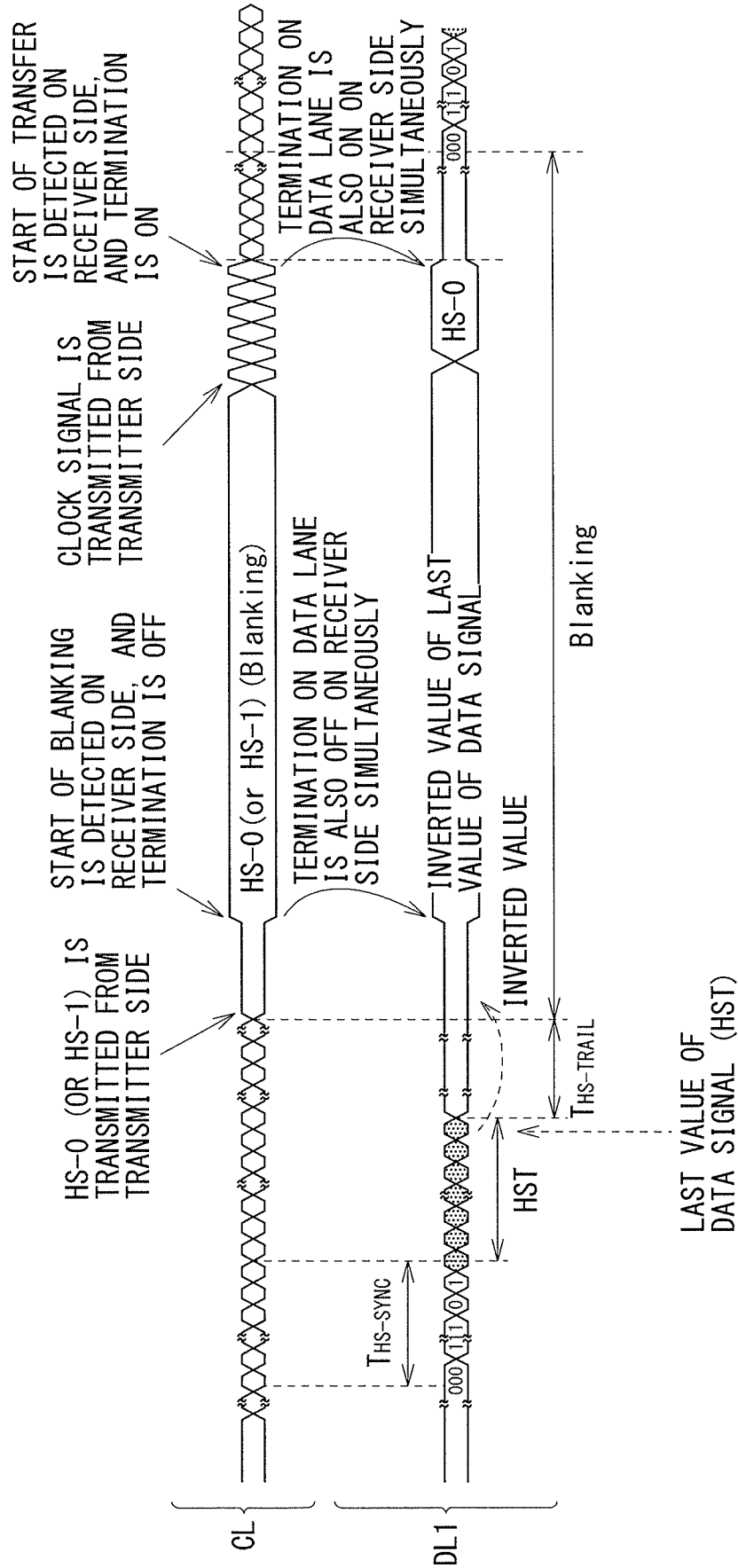
[FIG. 9]



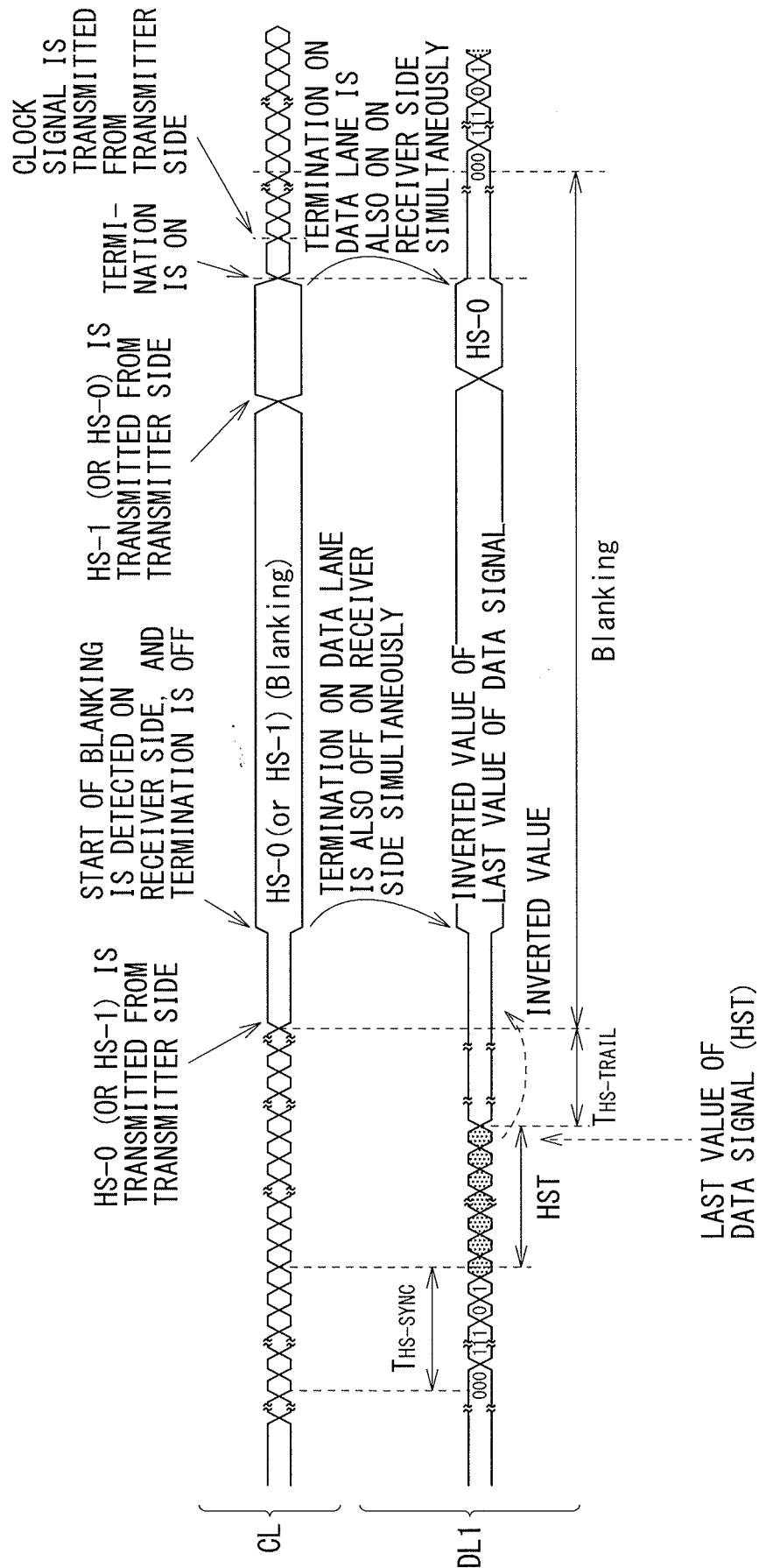
[FIG. 10]



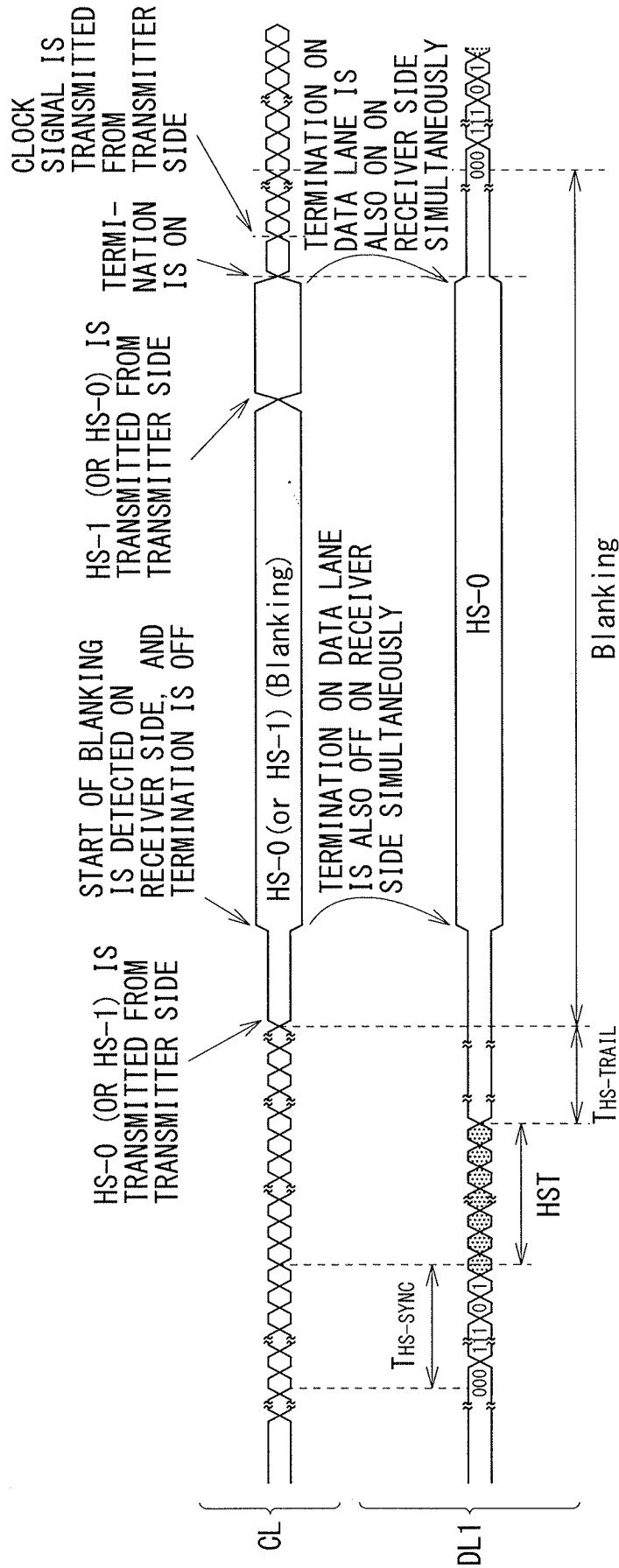
[FIG. 11]



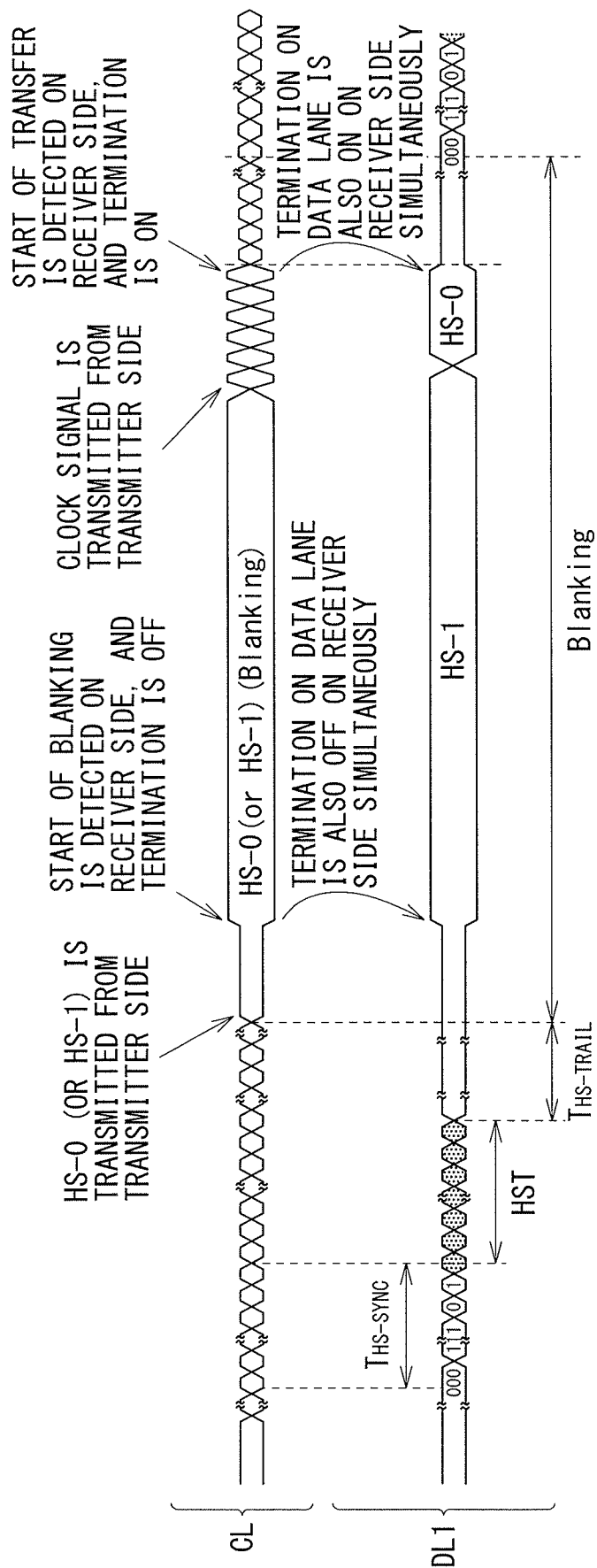
[FIG. 12]



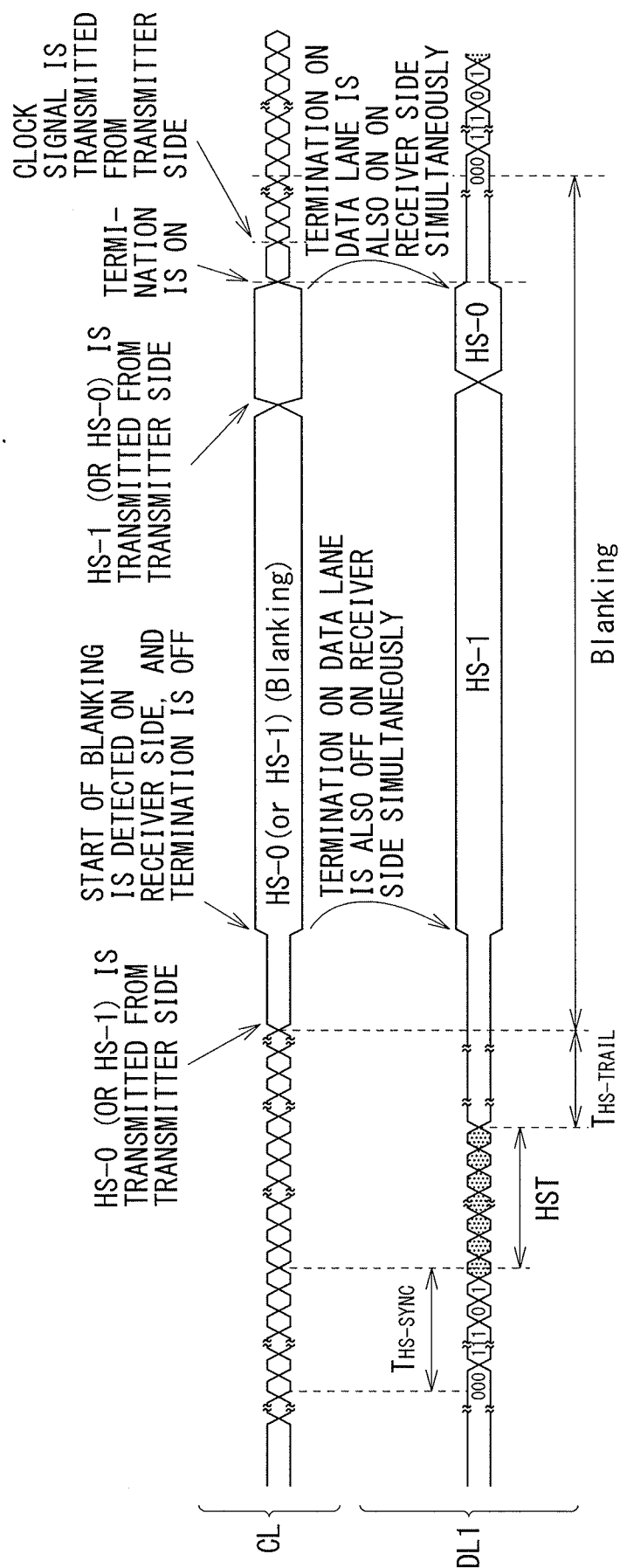
[FIG. 13]



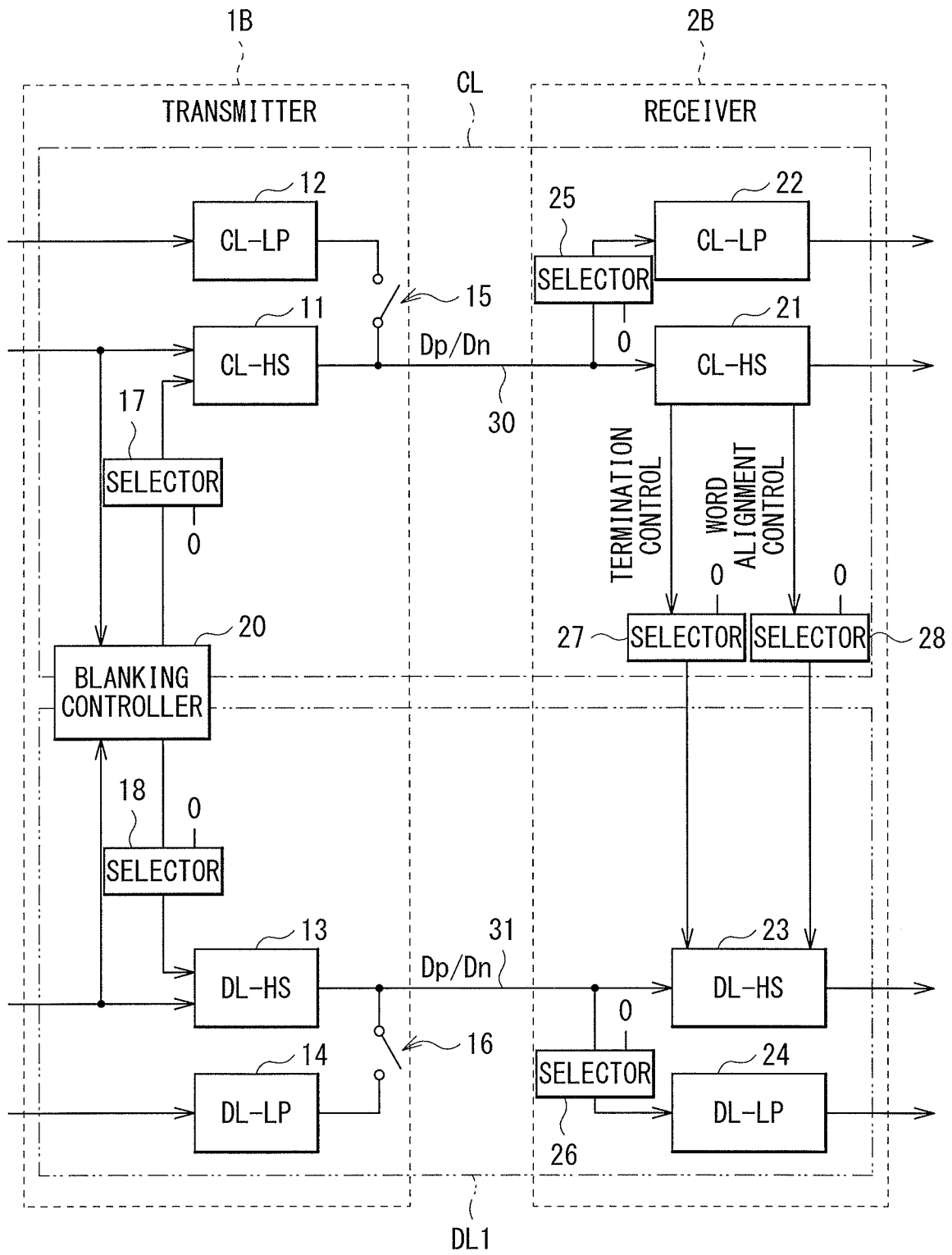
[FIG. 14]



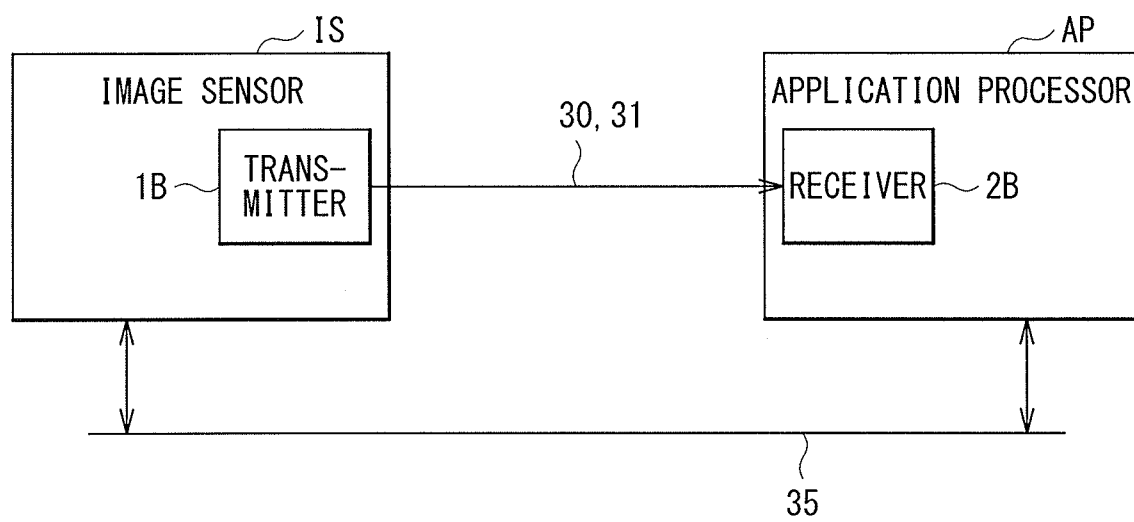
[FIG. 15]



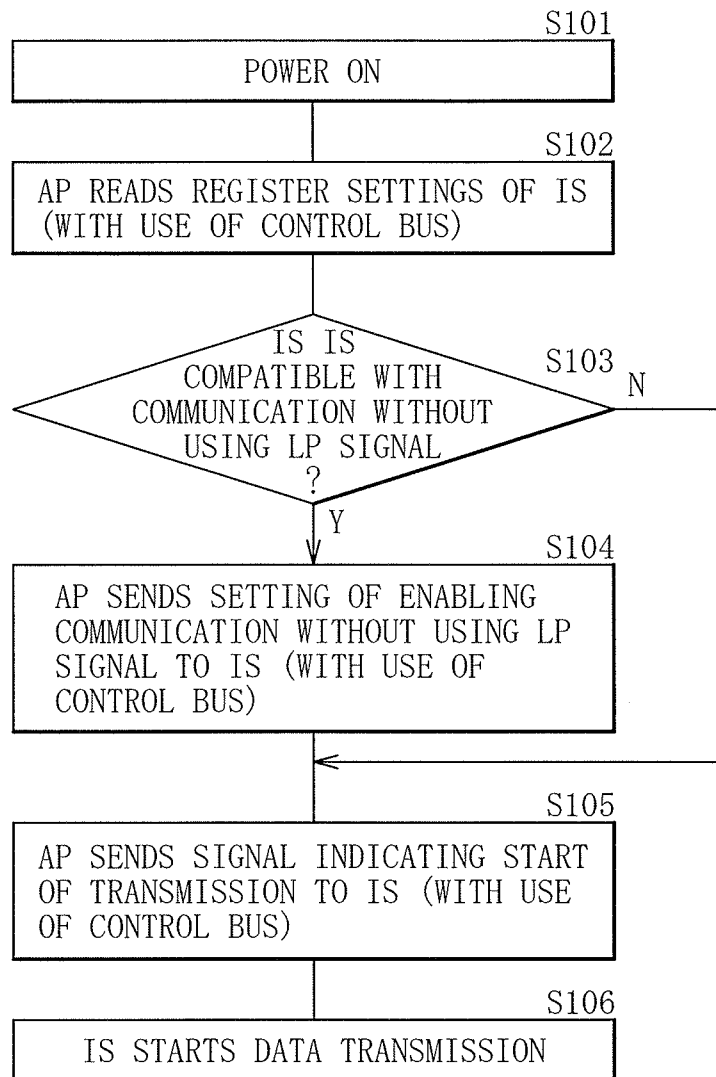
[FIG. 16]



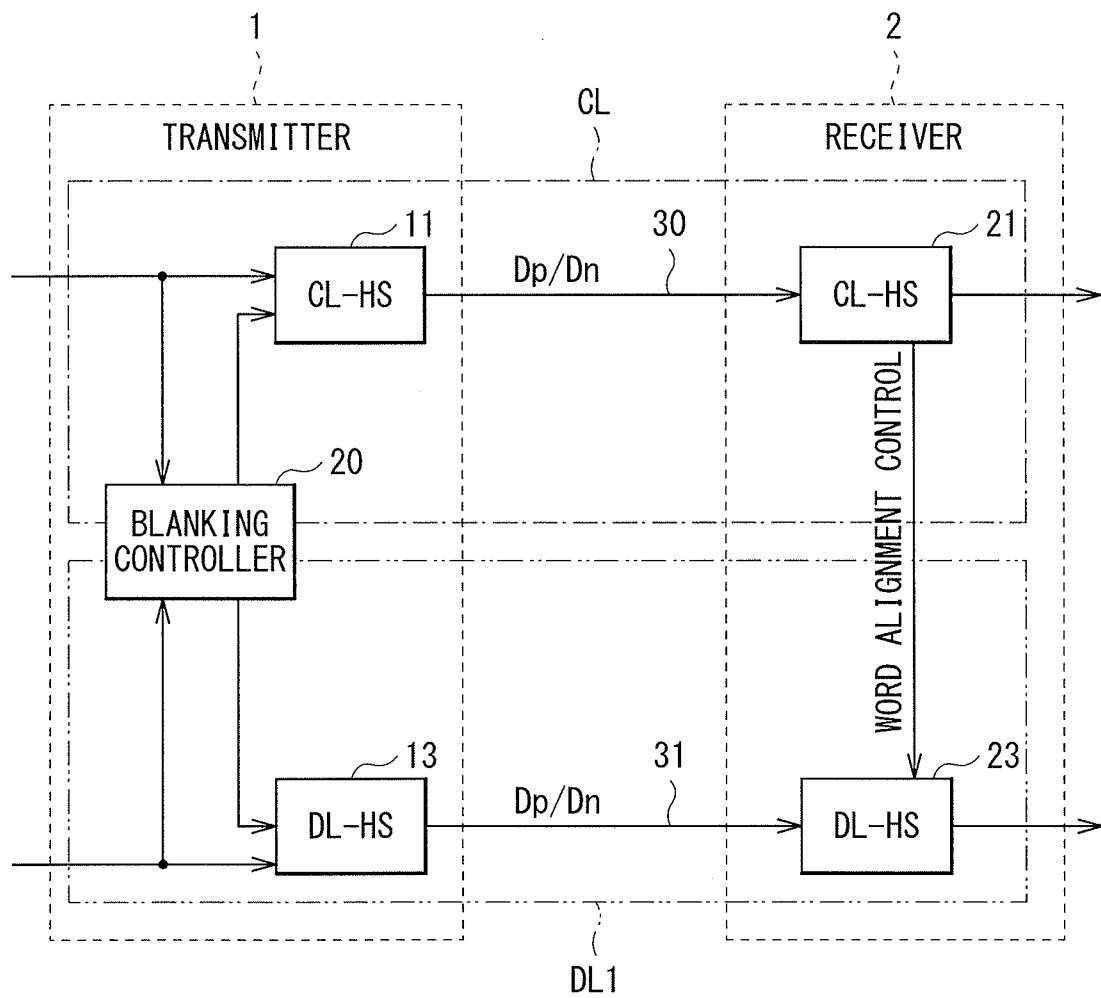
[FIG. 17]



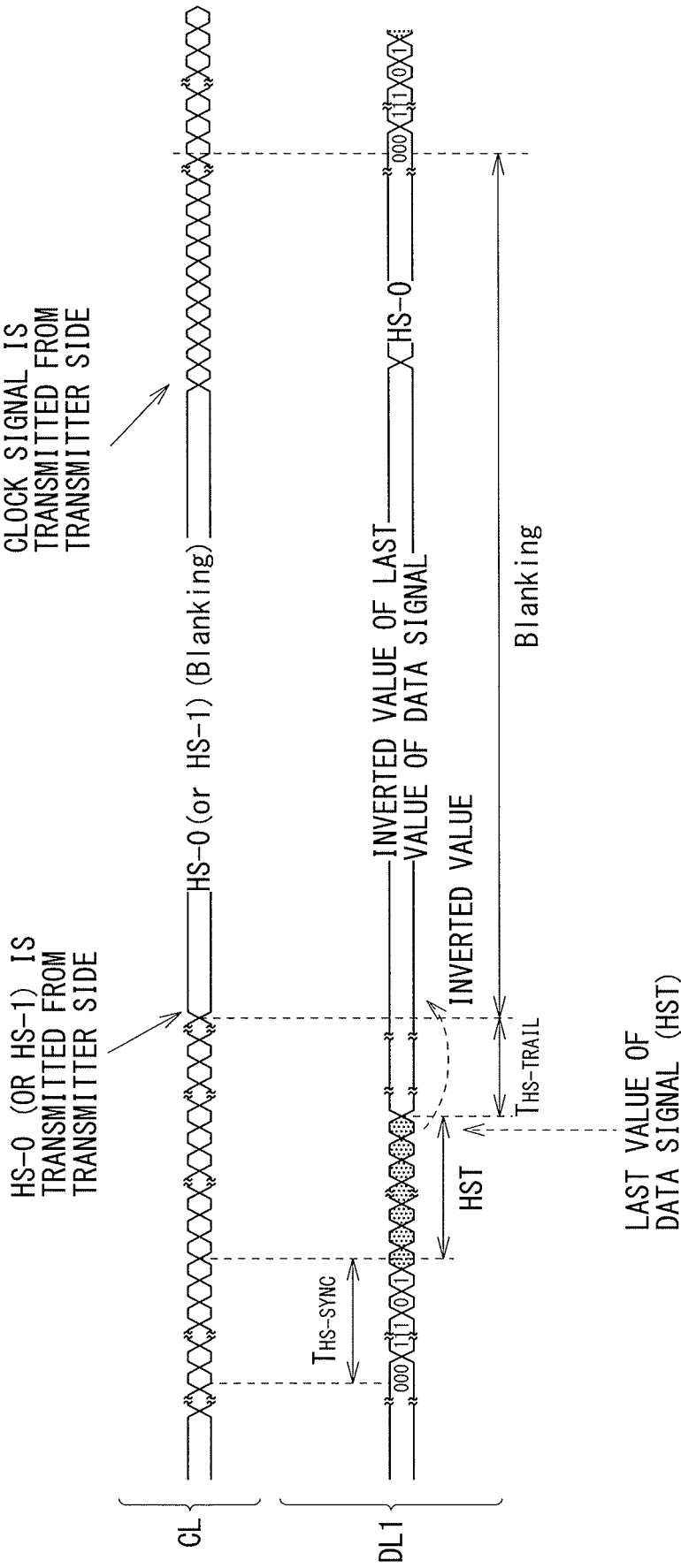
[FIG. 18]



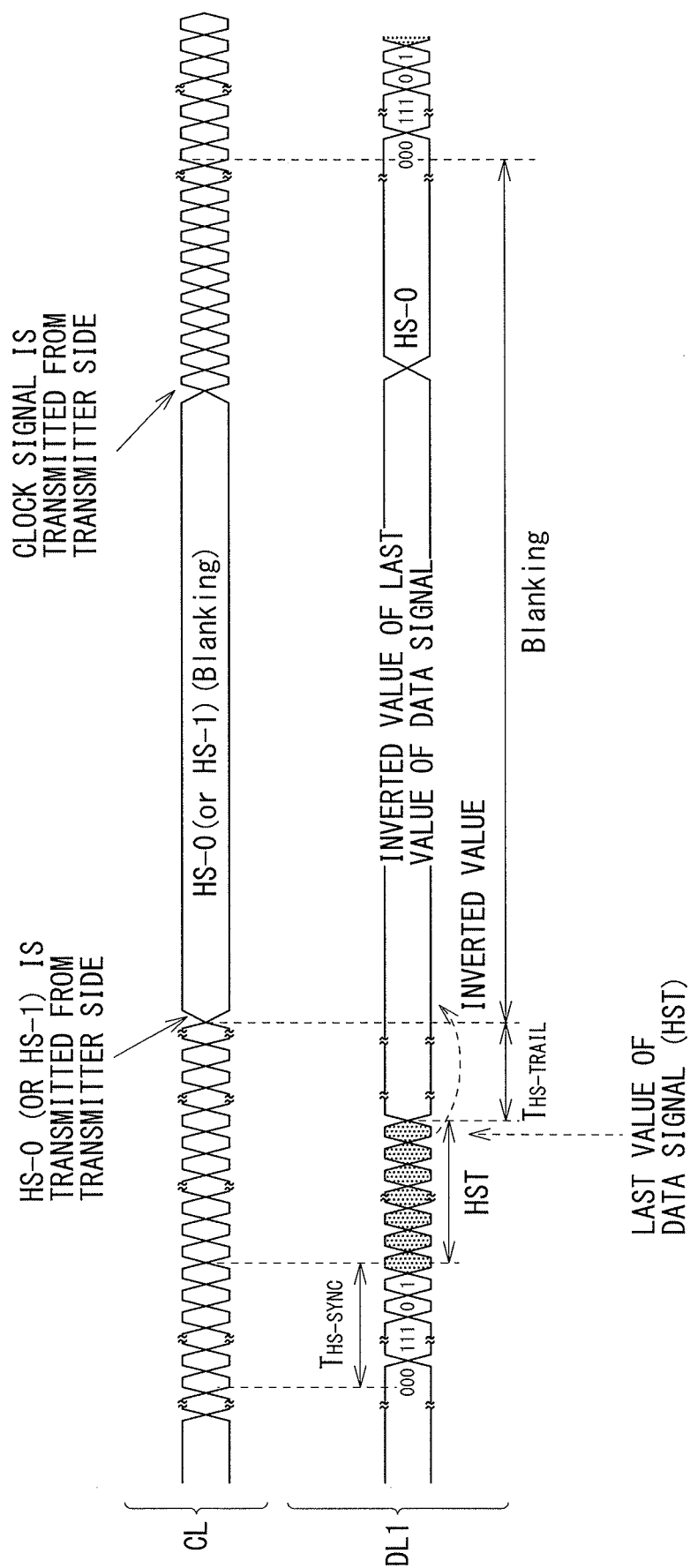
[FIG. 19]



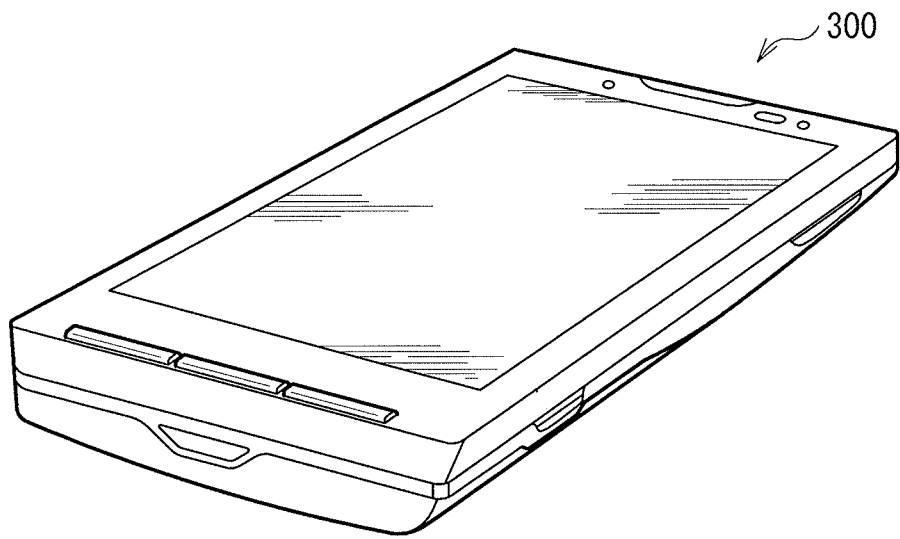
[FIG. 20]



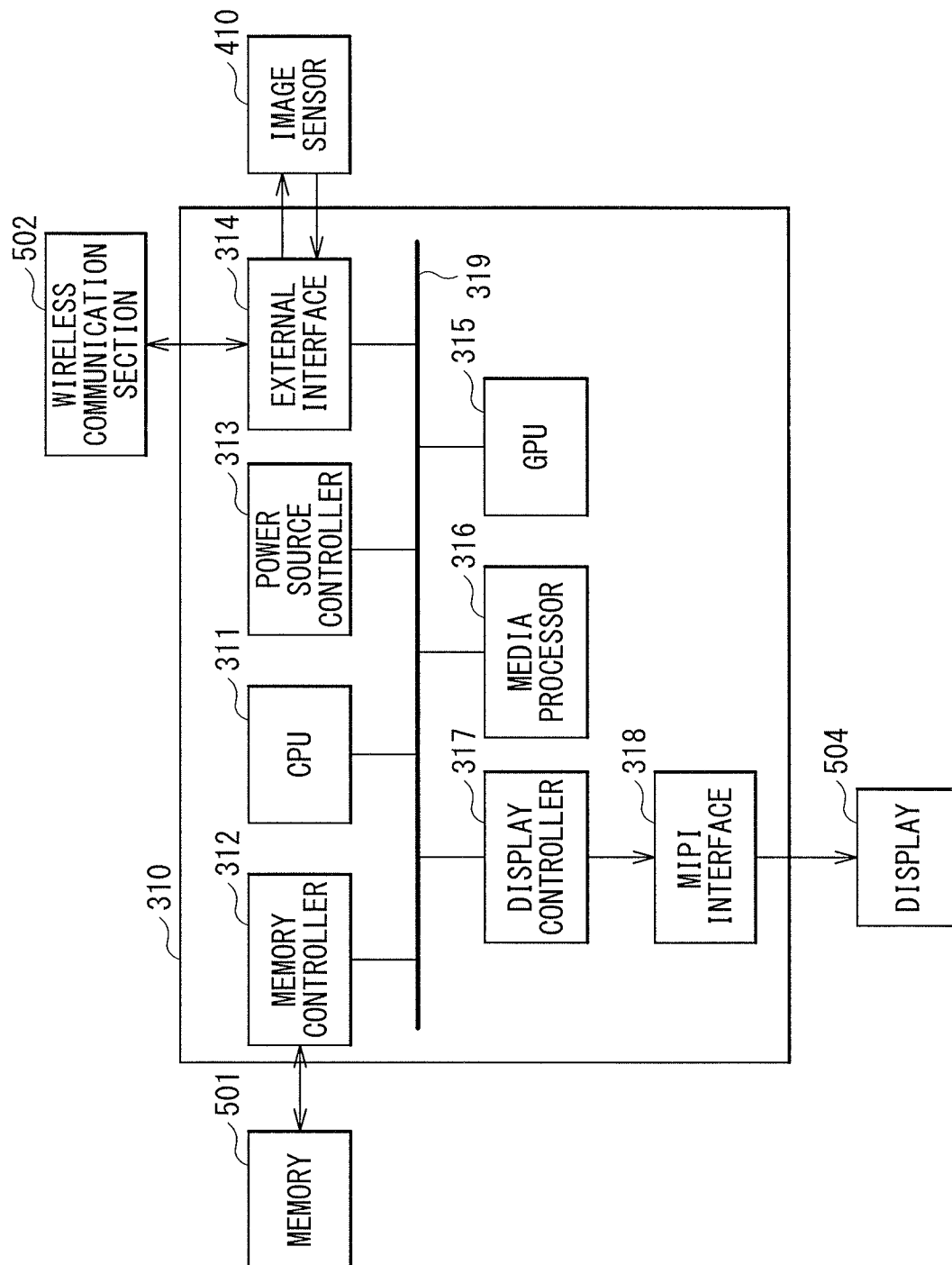
[FIG. 21]



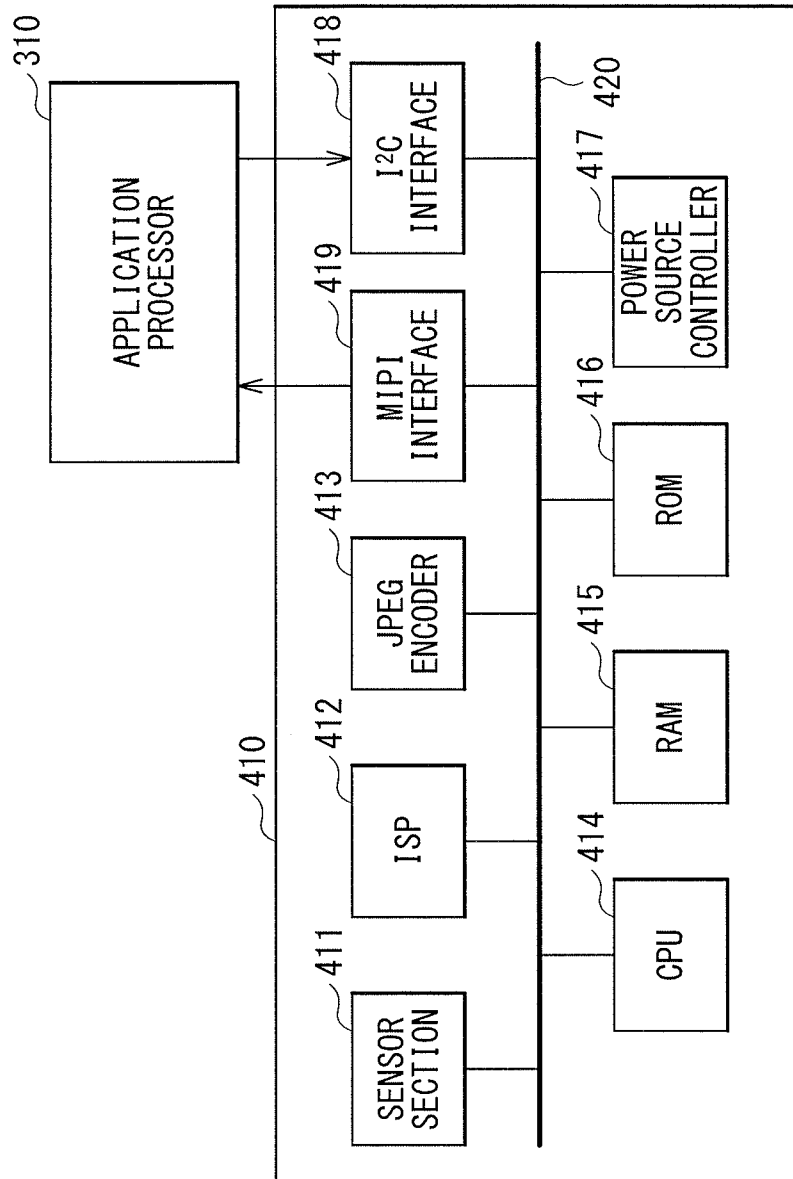
[FIG. 22]



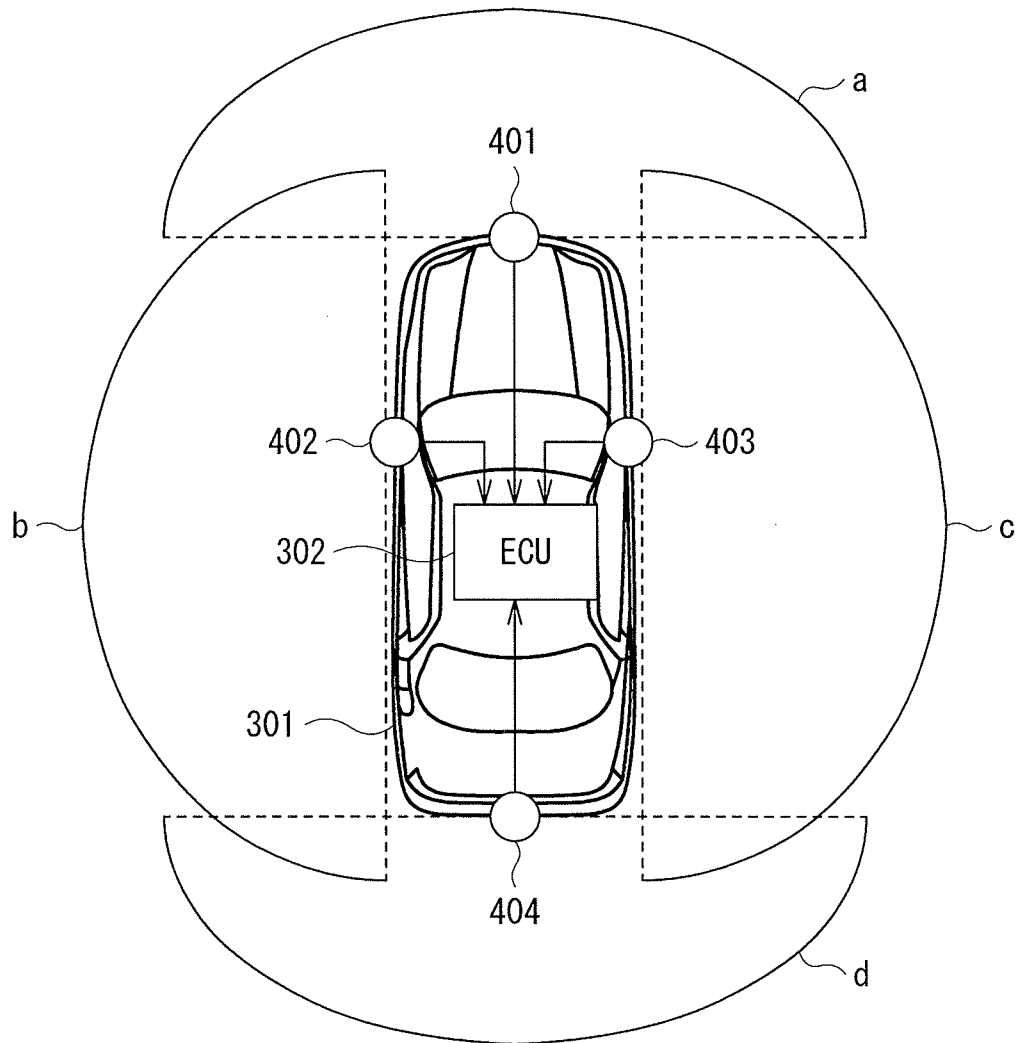
[FIG. 23]



[FIG. 24]



[FIG. 25]



[FIG. 26]

