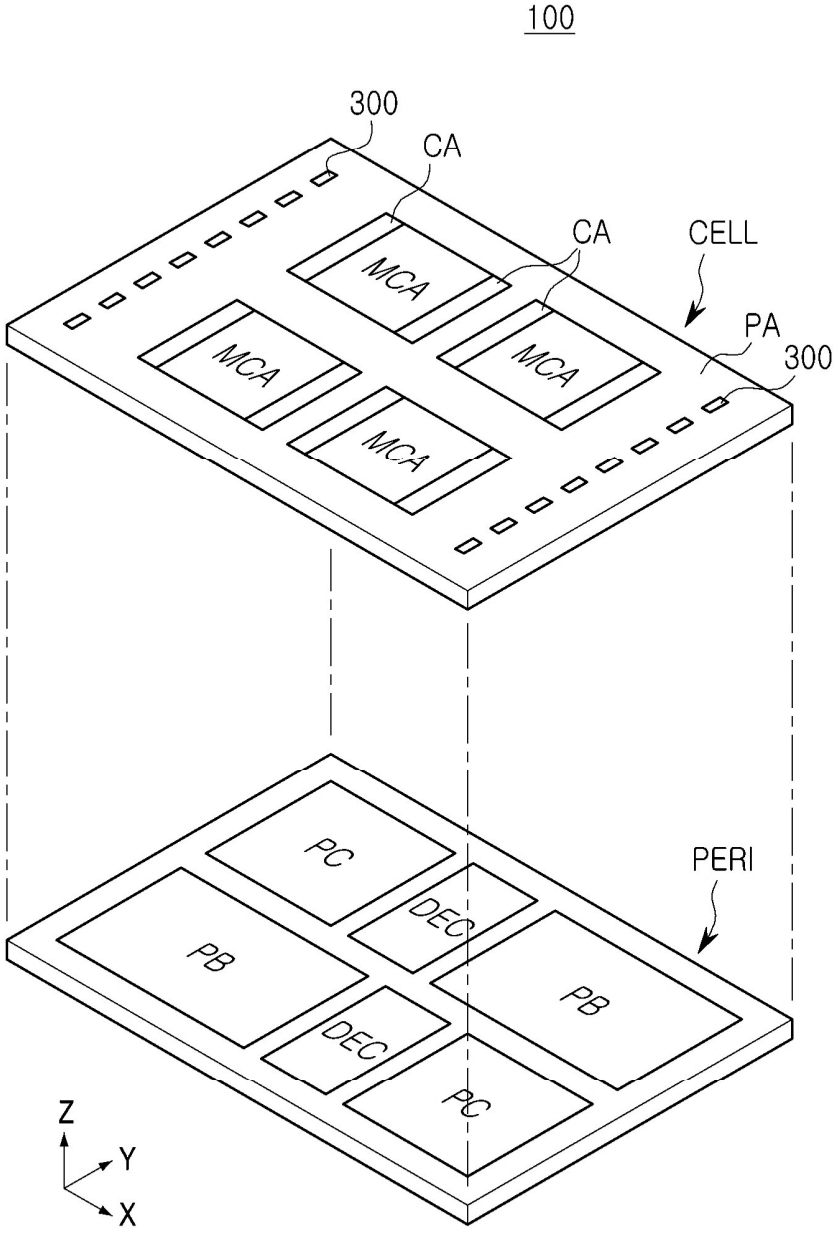

100
300
CA
CA
CELL
MCA
MCA
MCA
MCA
PA
300
PERI
PC
DEC
PB
PB
DEC
PC
Z
Y
X

FIG. 1

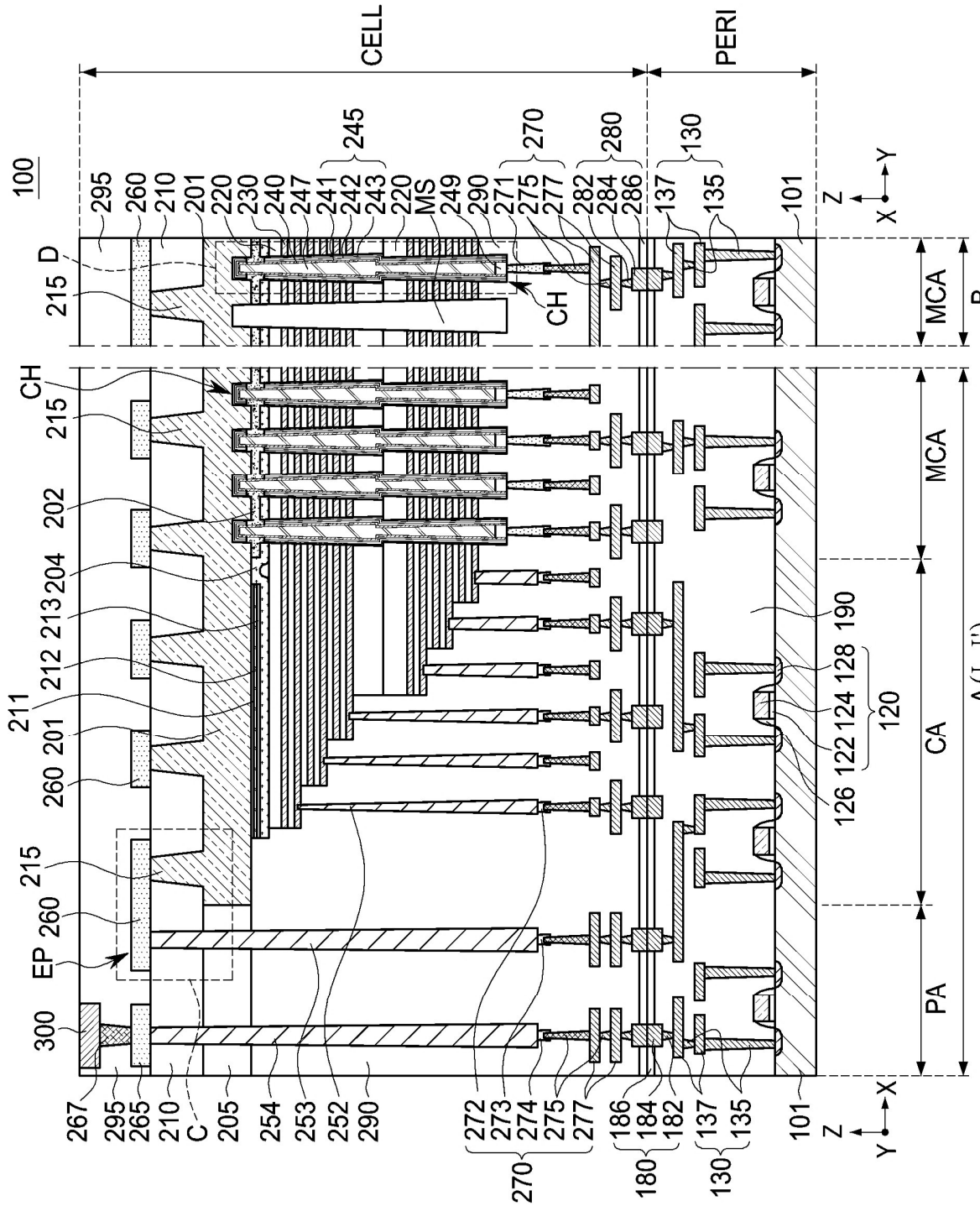

100
CELL
PERI
MCA
B
CA
PA
A(I-I')
CH
D
EP
C
120
245
270
280
130
180
211
215
202
204
213
212
201
260
300
267
295
265
210
205
254
253
252
290
272
273
274
275
277
186
184
182
137
135
101
190
128
124
122
126
MS
249
241
242
243
247
240
230
220
271
282
284
286
Z
X
Y

FIG. 2

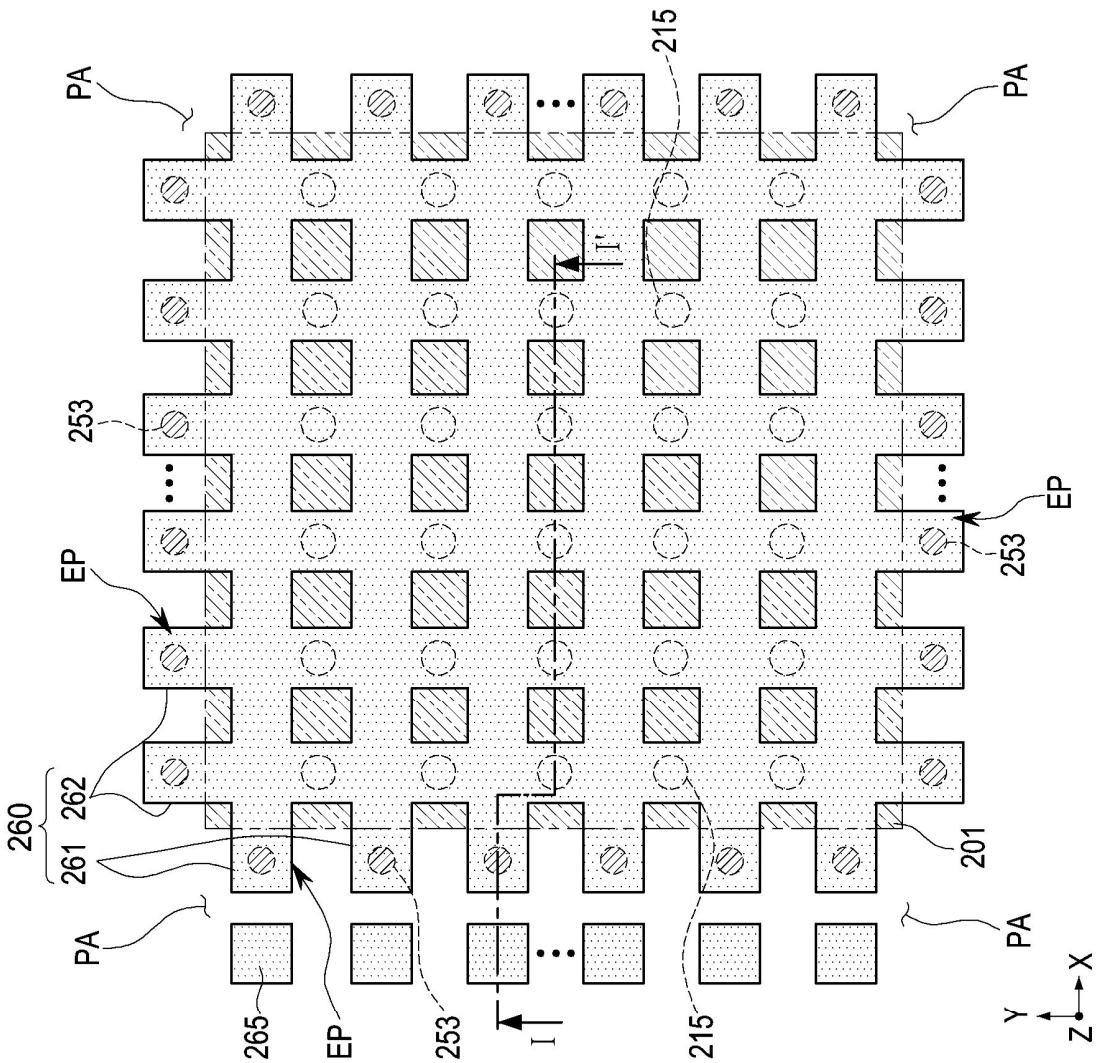


FIG. 3

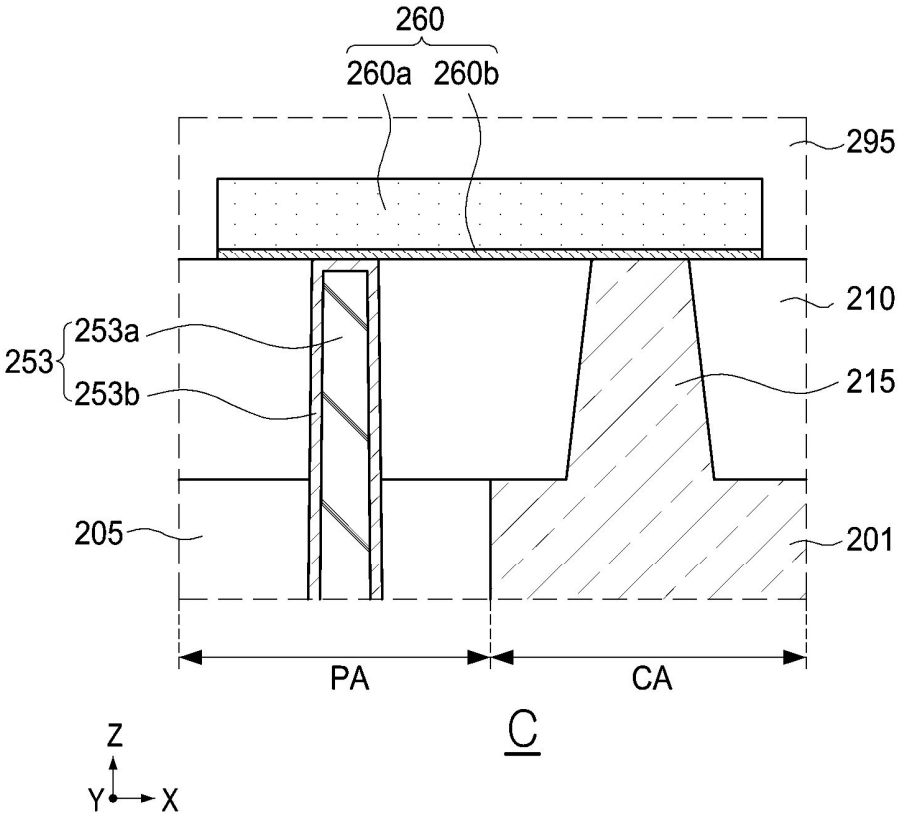


FIG. 4A

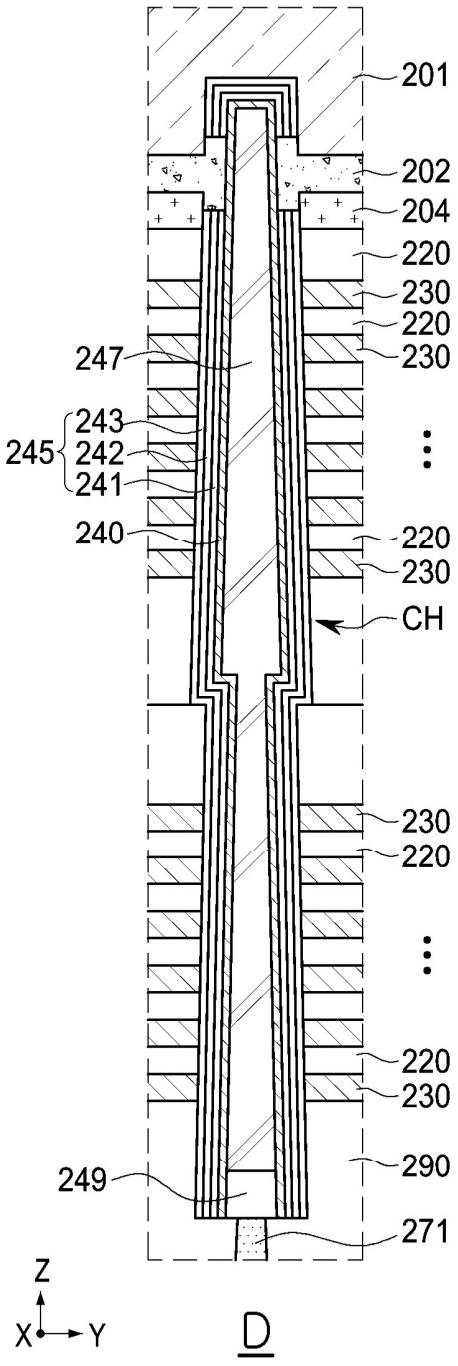

201
202
204
220
230
220
230
247
243
245
242
241
240
220
230
CH
230
220
220
230
290
249
271
Z
X
Y
D

FIG. 4B

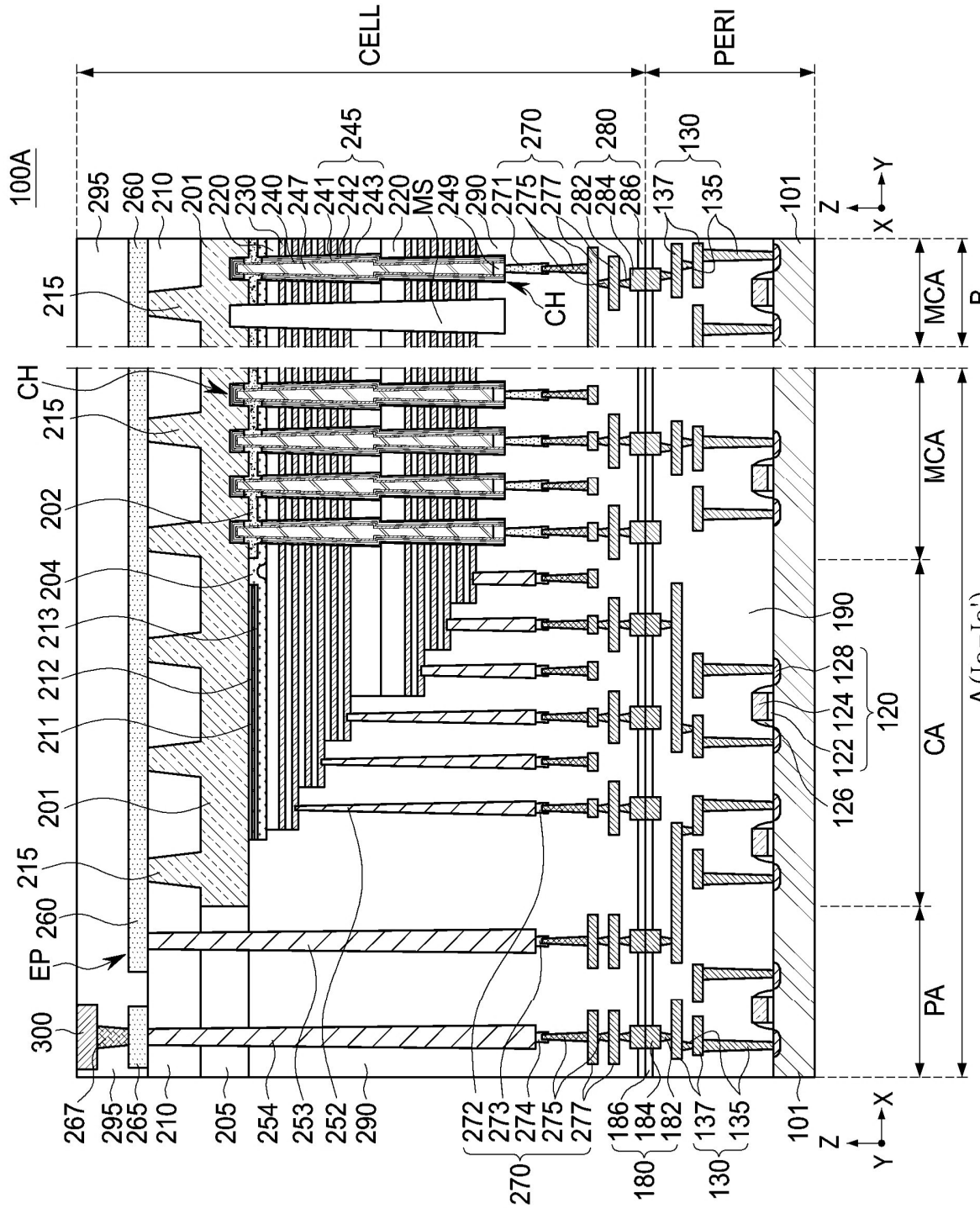


FIG. 5

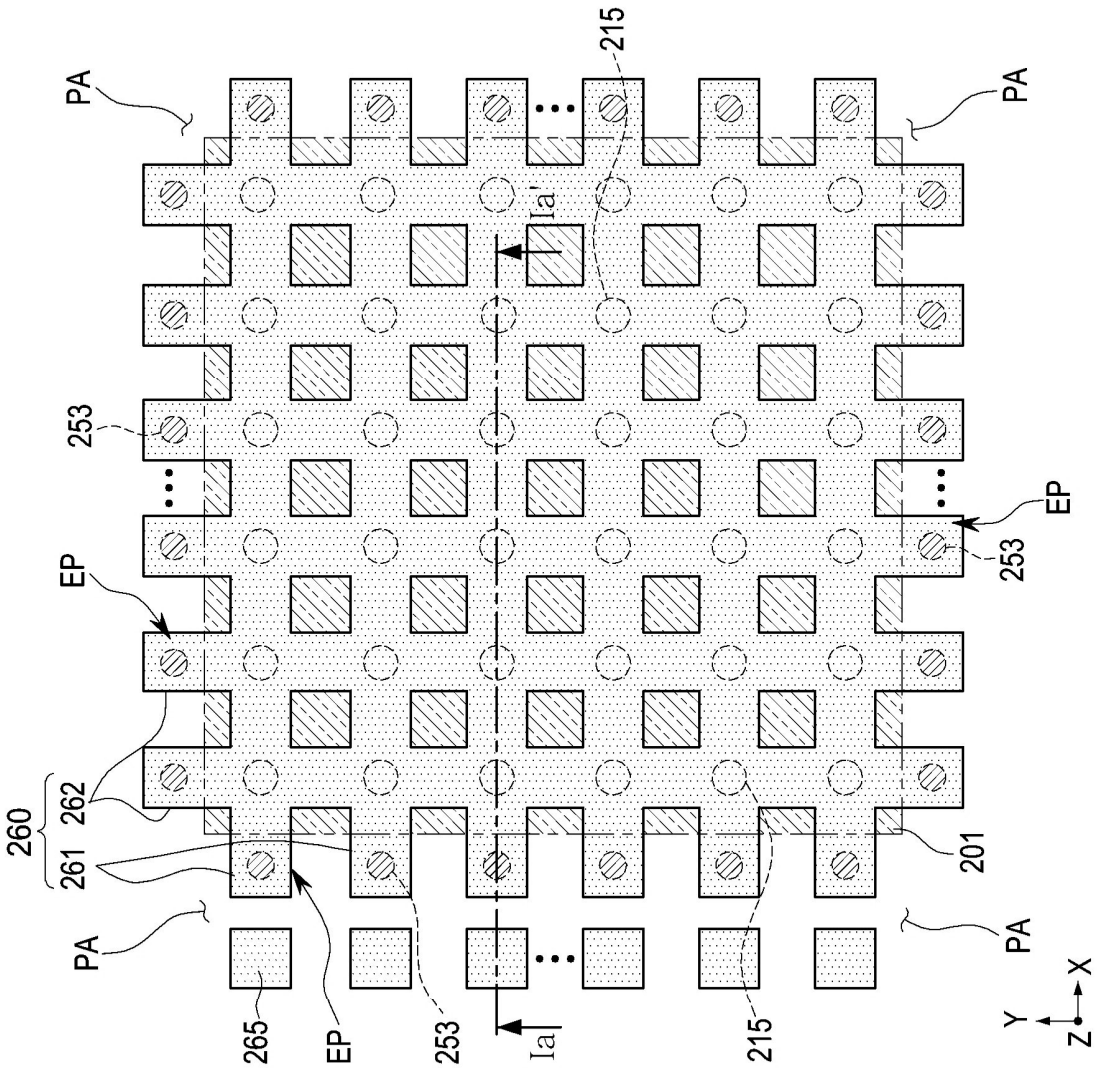

PA
215
PA
Ia'
253
EP
253
EP
260
262
261
201
PA
PA
265
EP
253
Ia
215
Y
X
Z

FIG. 6

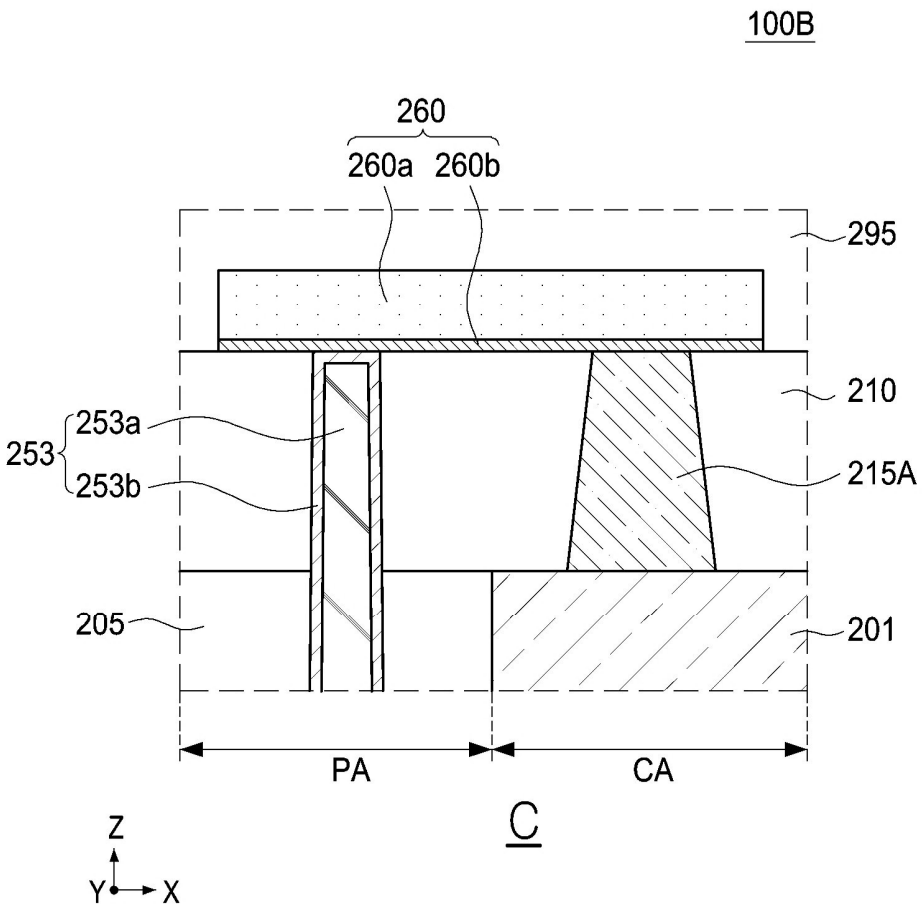

100B
260
260a
260b
295
210
253
253a
253b
215A
205
201
PA
CA
C
Z
Y
X

FIG. 7

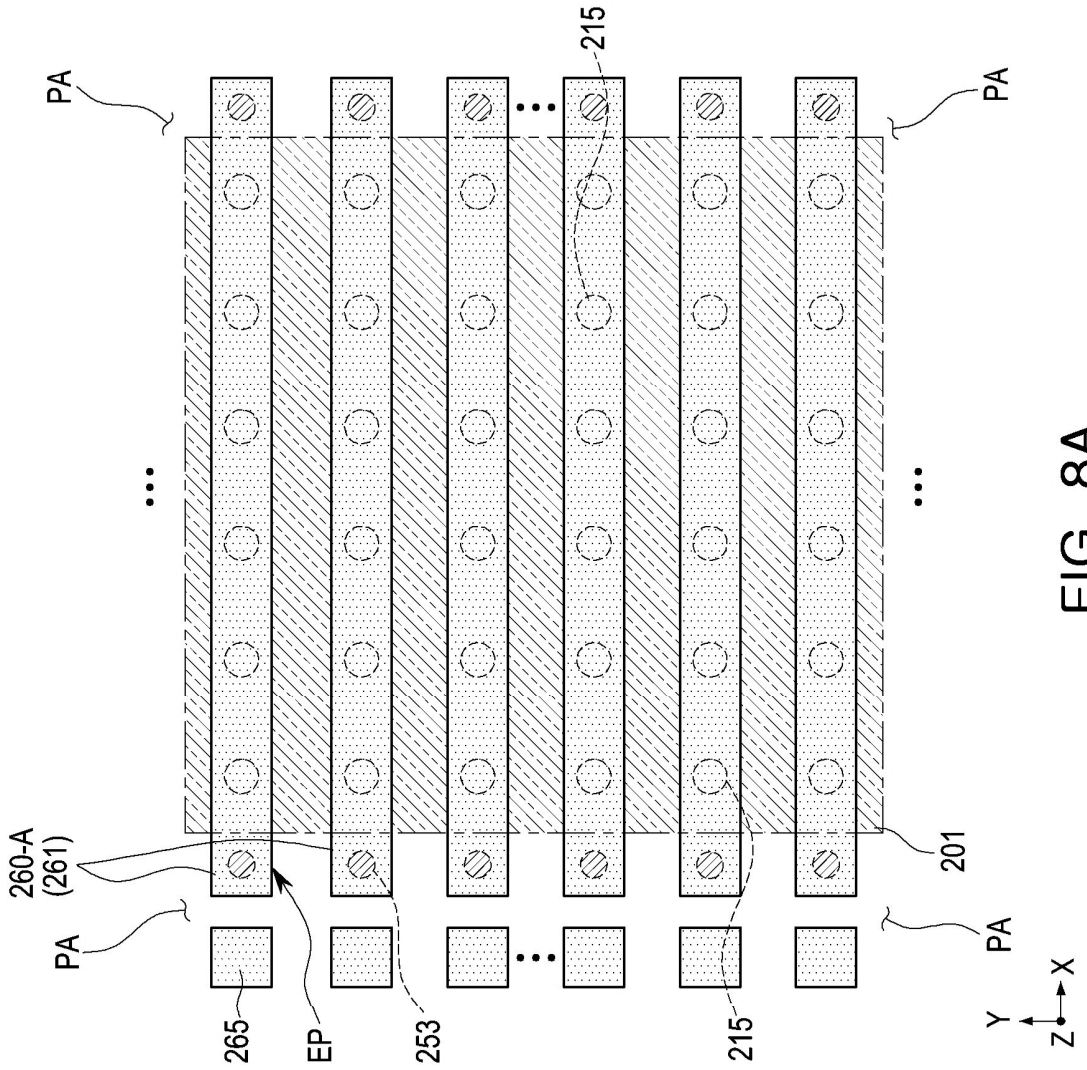


FIG. 8A

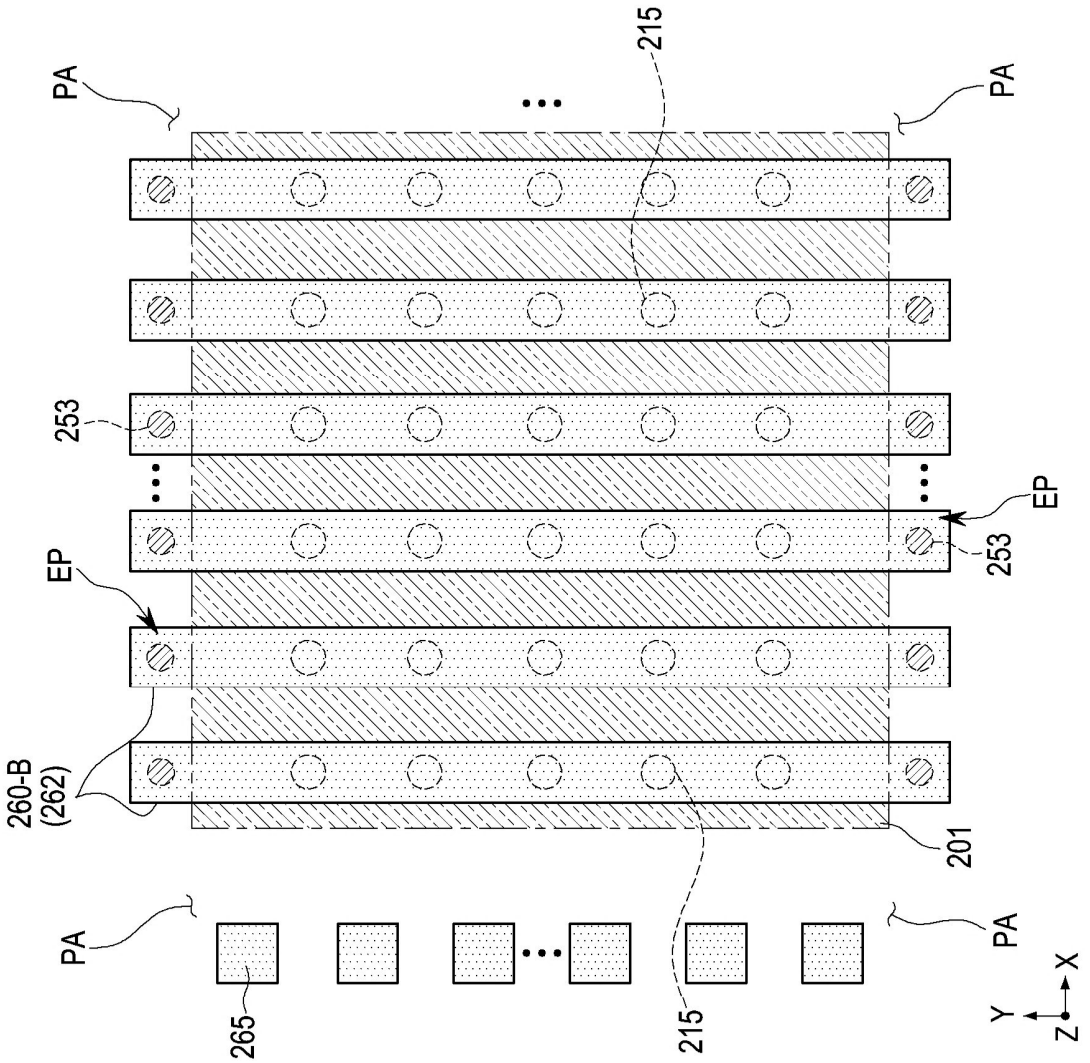


FIG. 8B

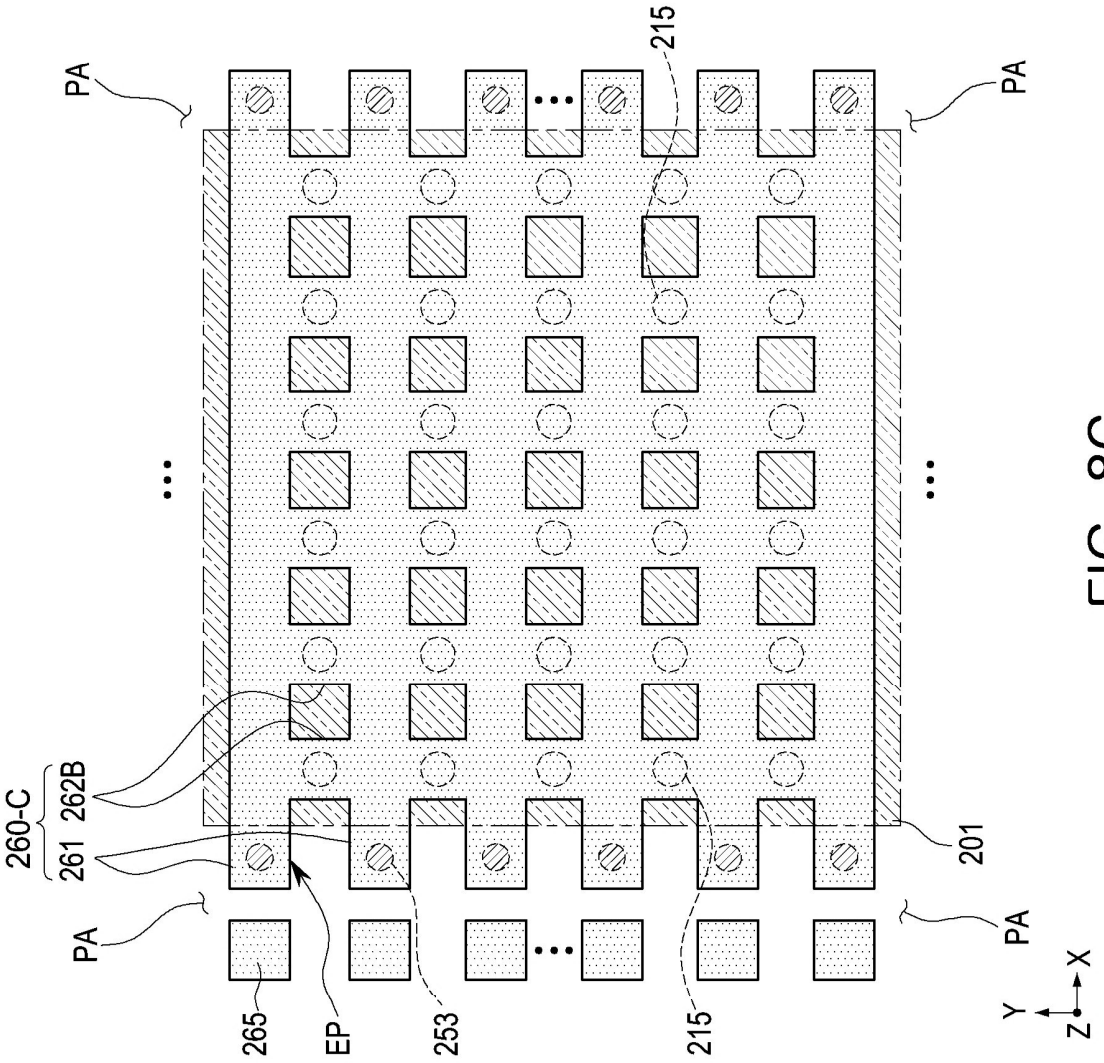


FIG. 8C

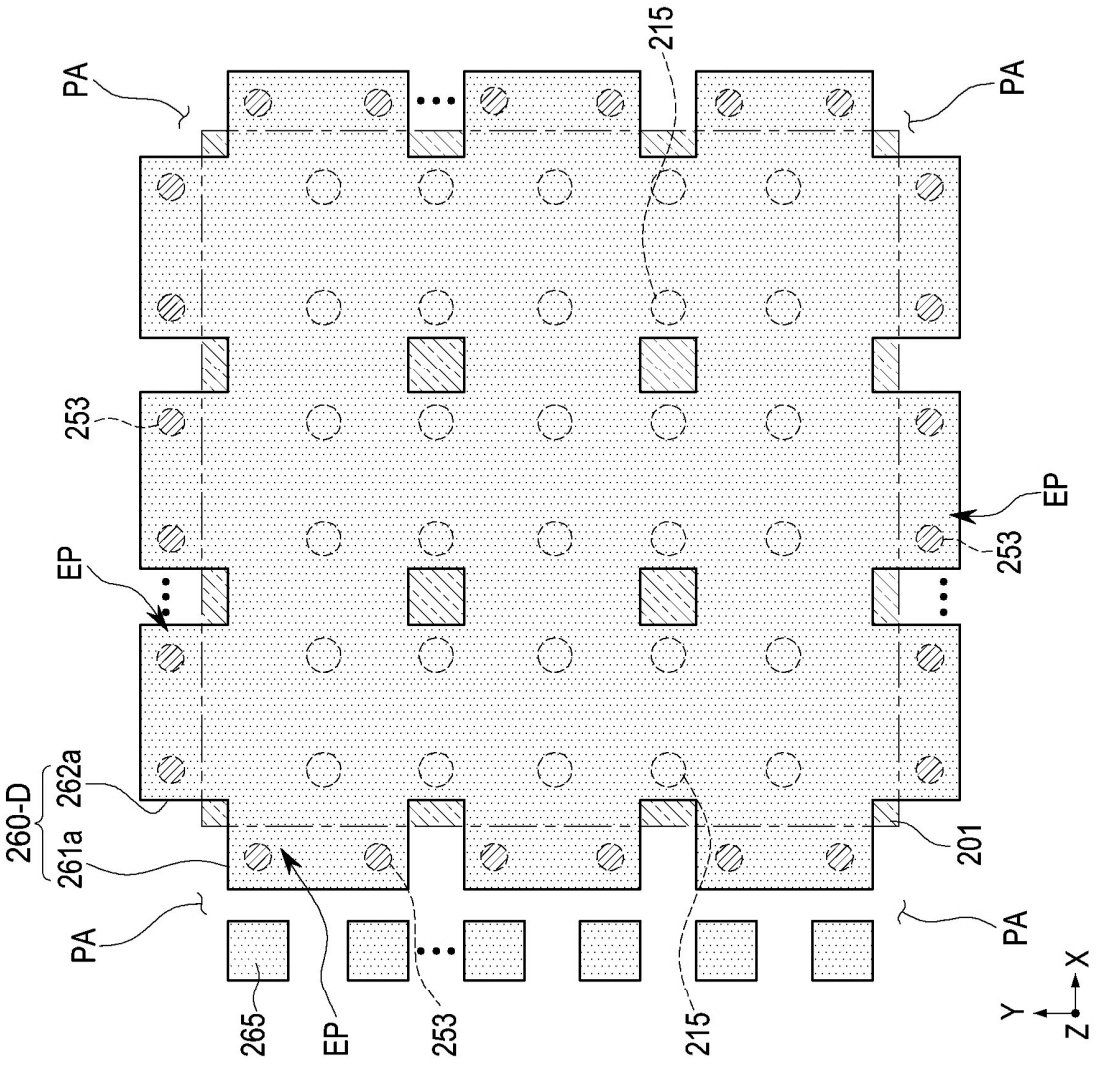

PA
215
PA
253
EP
253
EP
260-D
262a
261a
201
PA
PA
265
EP
253
215
Y
X
Z

FIG. 8D

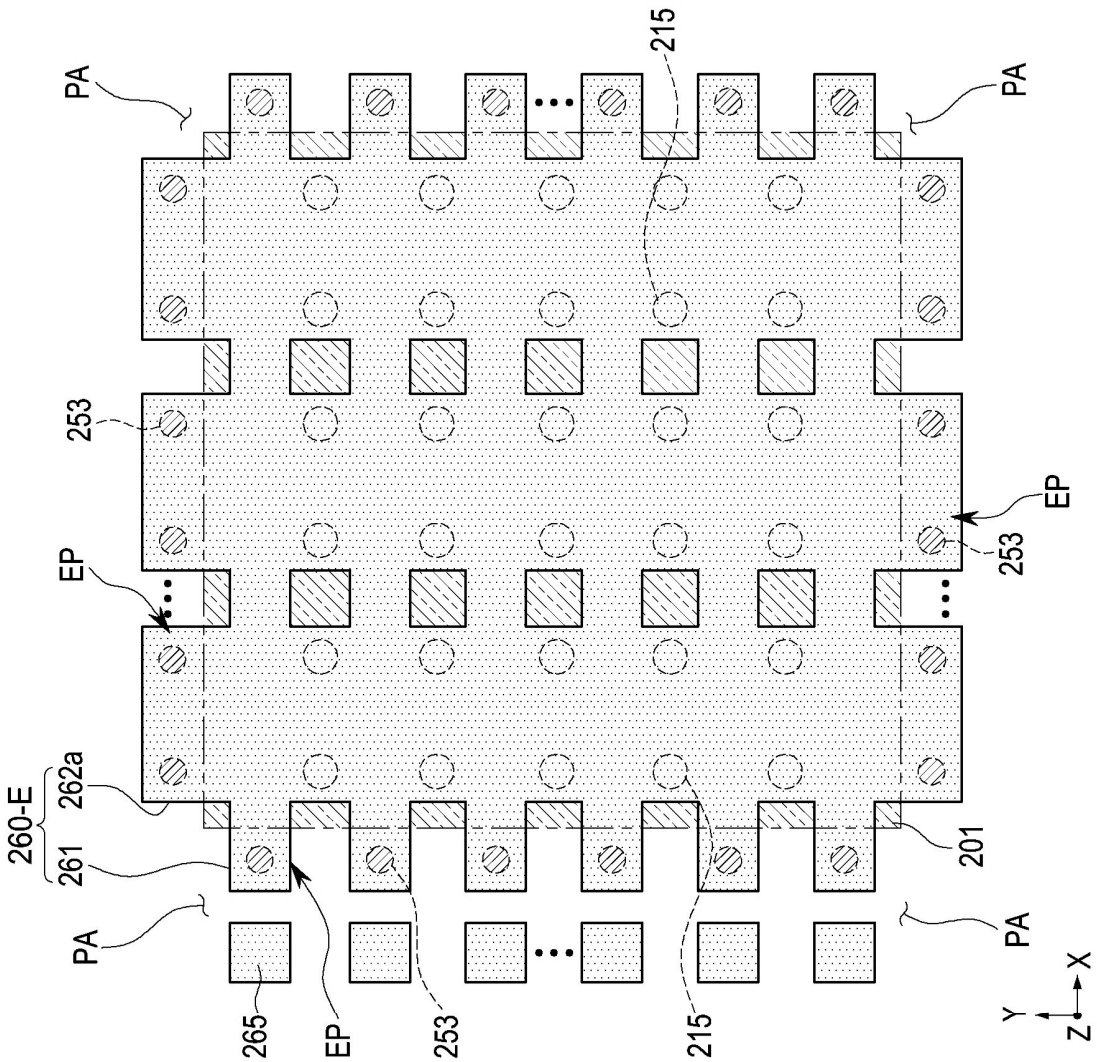


FIG. 8E

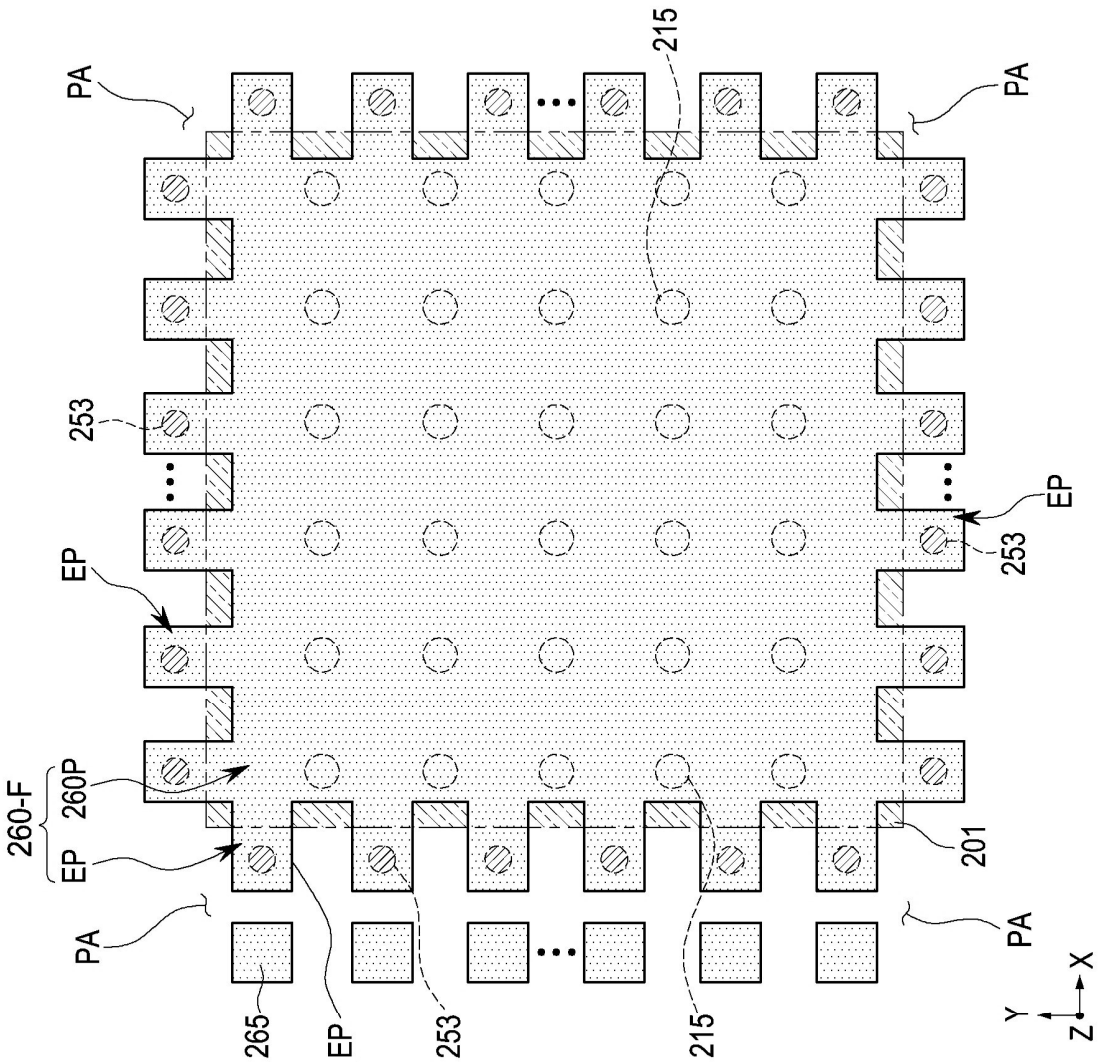


FIG. 8F

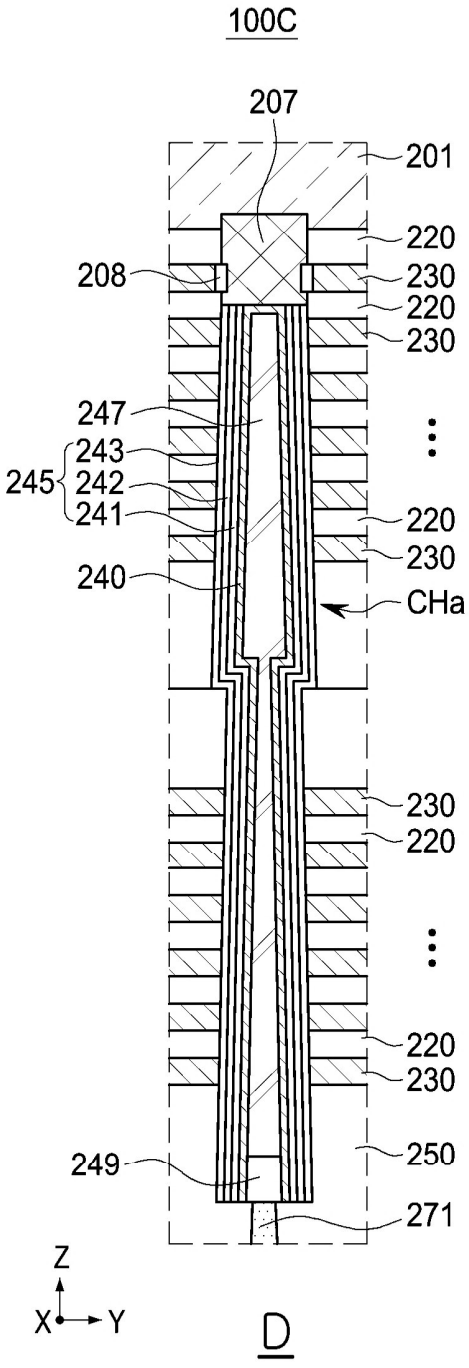

100C
207
201
220
208
230
220
230
247
243
245
242
241
220
230
240
CHa
230
220
220
230
249
250
271
Z
X
Y
D

FIG. 9

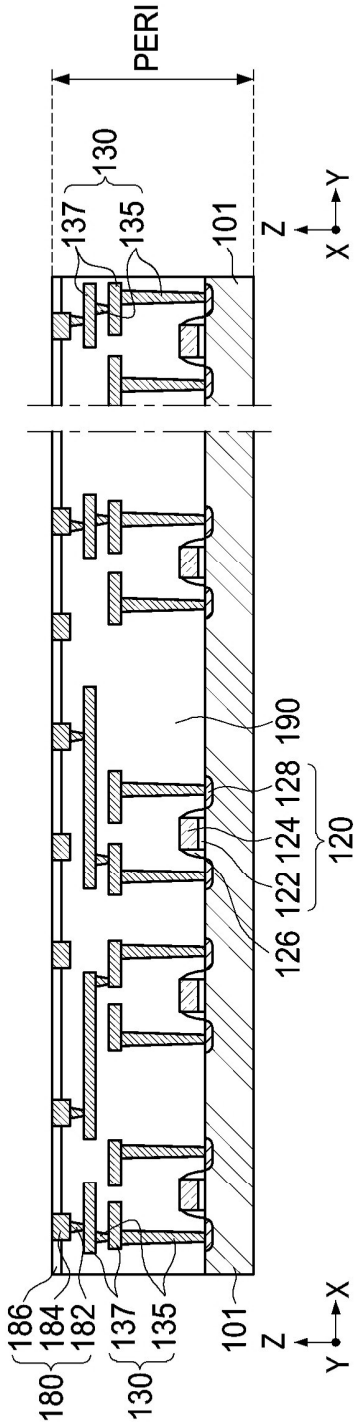

PERI
130
137
135
101
Z
Y
X
190
128
124
122
120
126
186
184
182
180
137
135
130
101
Z
X
Y

FIG. 10

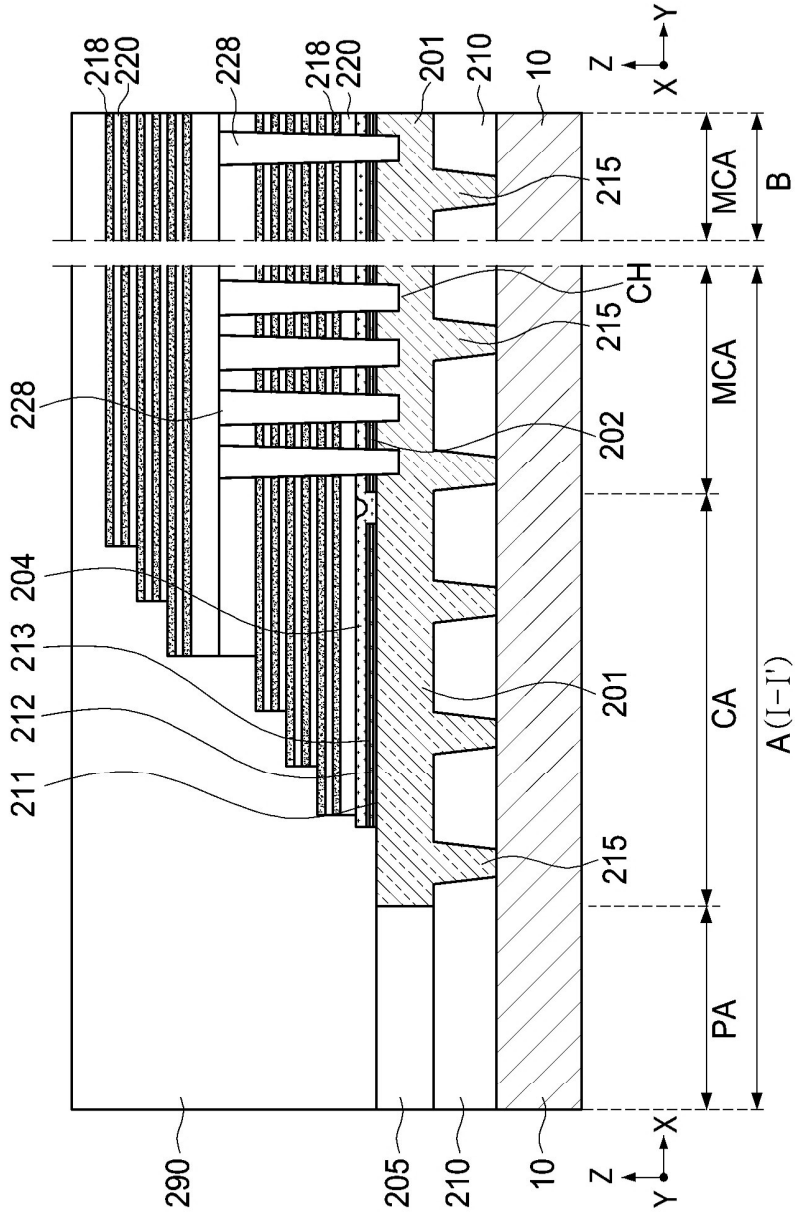

218
220
228
218
220
201
210
10
Z
Y
X
215
MCA
B
CH
215
228
MCA
202
204
213
212
211
201
CA
A(I-I')
215
PA
290
205
210
10
Z
X
Y

FIG. 11

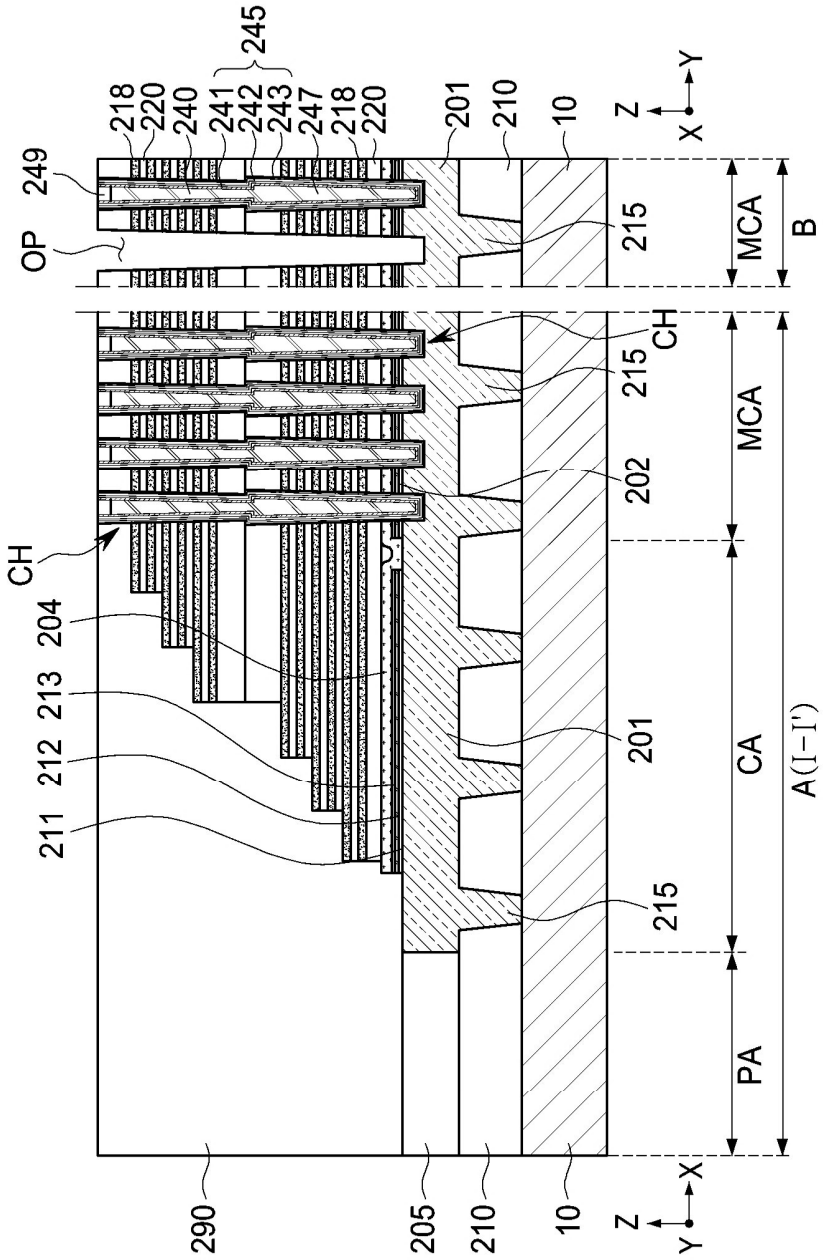

218
220
240
241
242
243
247
218
220
245
201
210
10
Z
Y
X
249
OP
215
MCA
B
CH
215
202
MCA
CH
204
213
212
211
201
CA
215
PA
A(I-I')
290
205
210
10
Z
X
Y

FIG. 12

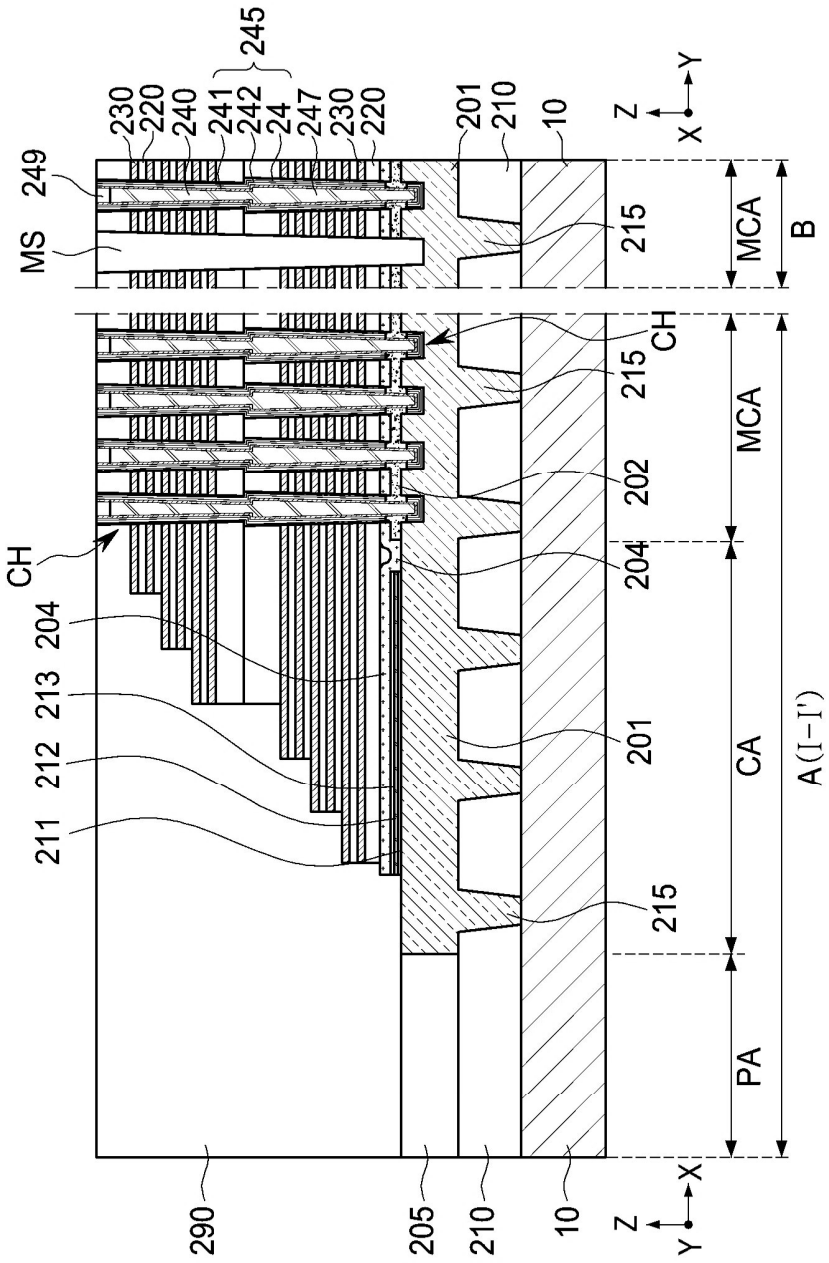

245
230
220
240
241
242
24
247
230
220
201
210
10
Z
Y
X
249
MS
215
MCA
B
CH
215
MCA
202
204
CH
204
213
212
211
201
CA
A(I-I')
215
PA
290
205
210
10
Z
X
Y

FIG. 13

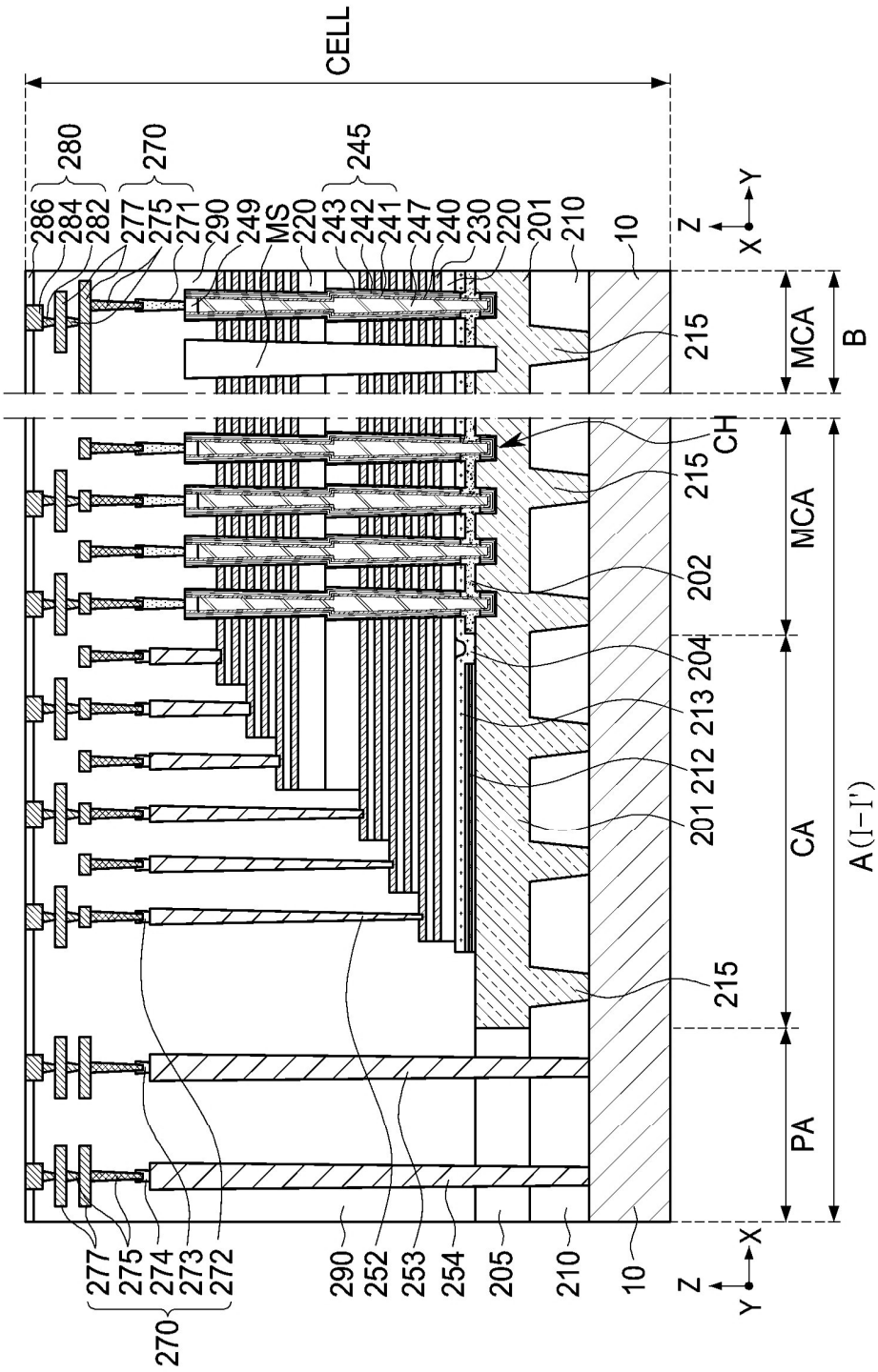

CELL
280
270
245
286
284
282
277
275
271
290
249
MS
220
243
242
241
247
240
230
220
201
210
10
Z
Y
X
215
MCA
B
CH
215
MCA
202
204
213
212
201
CA
215
PA
A(I-I')
277
275
274
273
272
270
290
252
253
254
205
210
10
Z
X
Y

FIG. 14

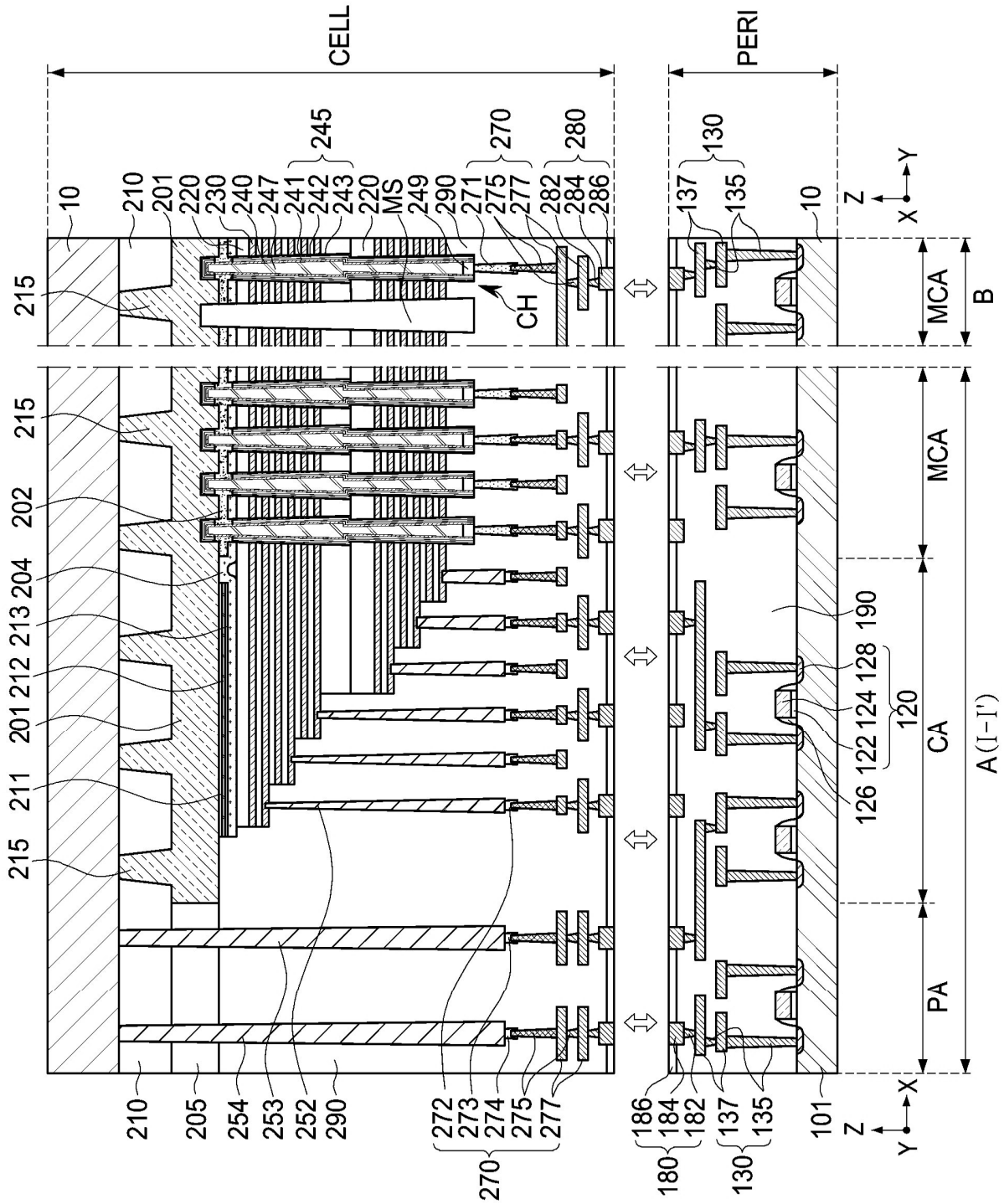

CELL
PERI
10
210
201
220
230
240
247
241
242
243
245
220
MS
249
290
271
275
277
270
282
284
286
280
CH
215
137
135
130
MCA
B
202
204
213
212
211
190
128
124
122
120
126
CA
PA
A(I-I')
205
254
253
252
272
273
274
275
277
186
184
182
180
101
Z
X
Y

FIG. 15

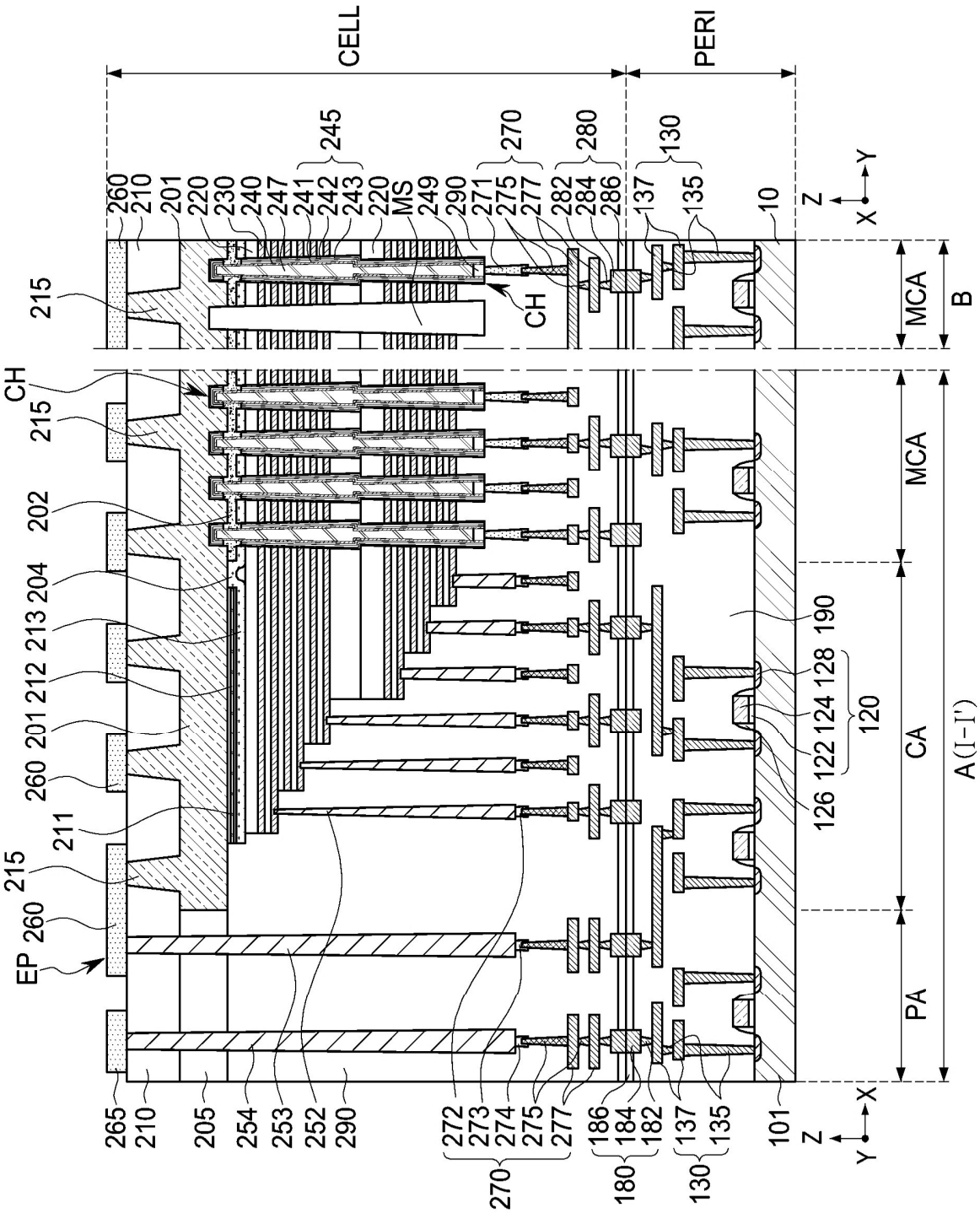

CELL
PERI
MCA
B
CH
215
260
210
201
220
230
240
247
245
241
242
243
MS
249
290
270
271
275
277
280
282
284
286
130
137
135
10
Z
X
Y
CA
PA
A(I-I')
190
128
124
122
120
126
202
204
213
212
211
EP
265
205
254
253
252
272
273
274
186
184
182
180

FIG. 16

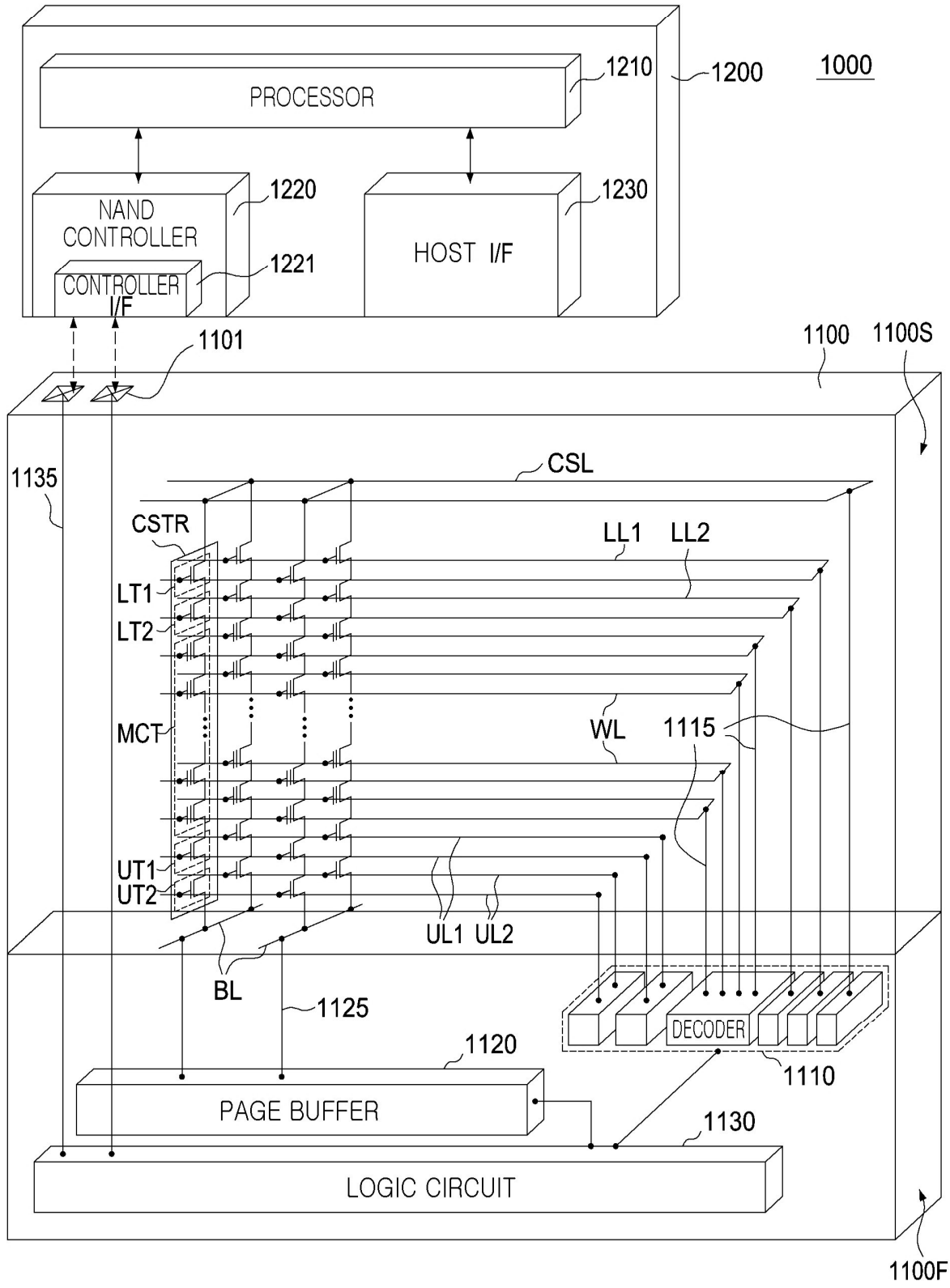

PROCESSOR
1210
1200
1000
NAND CONTROLLER
1220
CONTROLLER I/F
1221
HOST I/F
1230
1101
1100
1100S
1135
CSL
CSTR
LL1
LL2
LT1
LT2
MCT
WL
1115
UT1
UT2
UL1
UL2
BL
1125
DECODER
1120
1110
PAGE BUFFER
1130
LOGIC CIRCUIT
1100F

FIG. 17

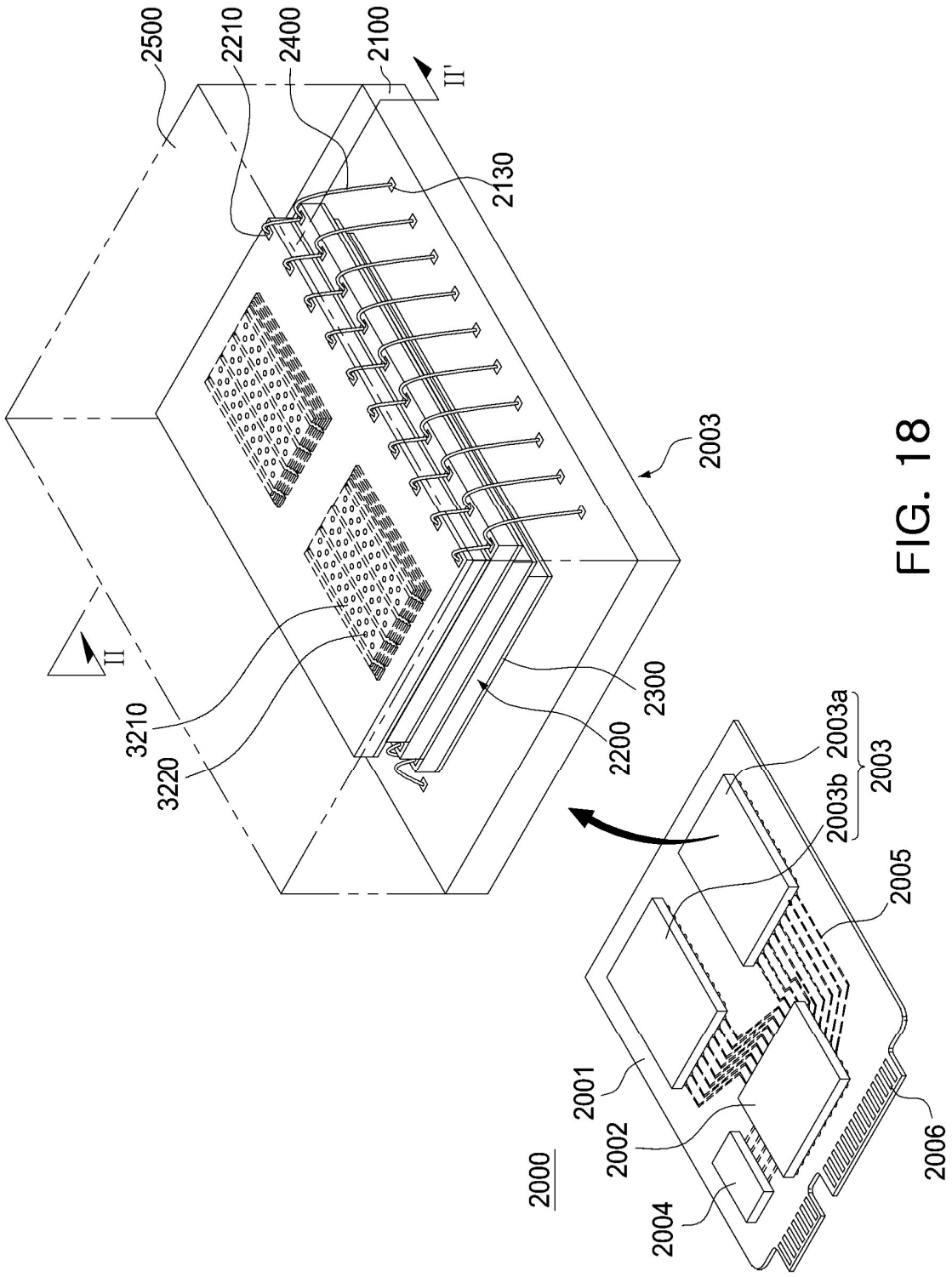

2500
2210
2400
2100
II'
2130
2003
II
3210
3220
2300
2200
2003a
2003b
2003
2005
2001
2002
2004
2006
2000

FIG. 18

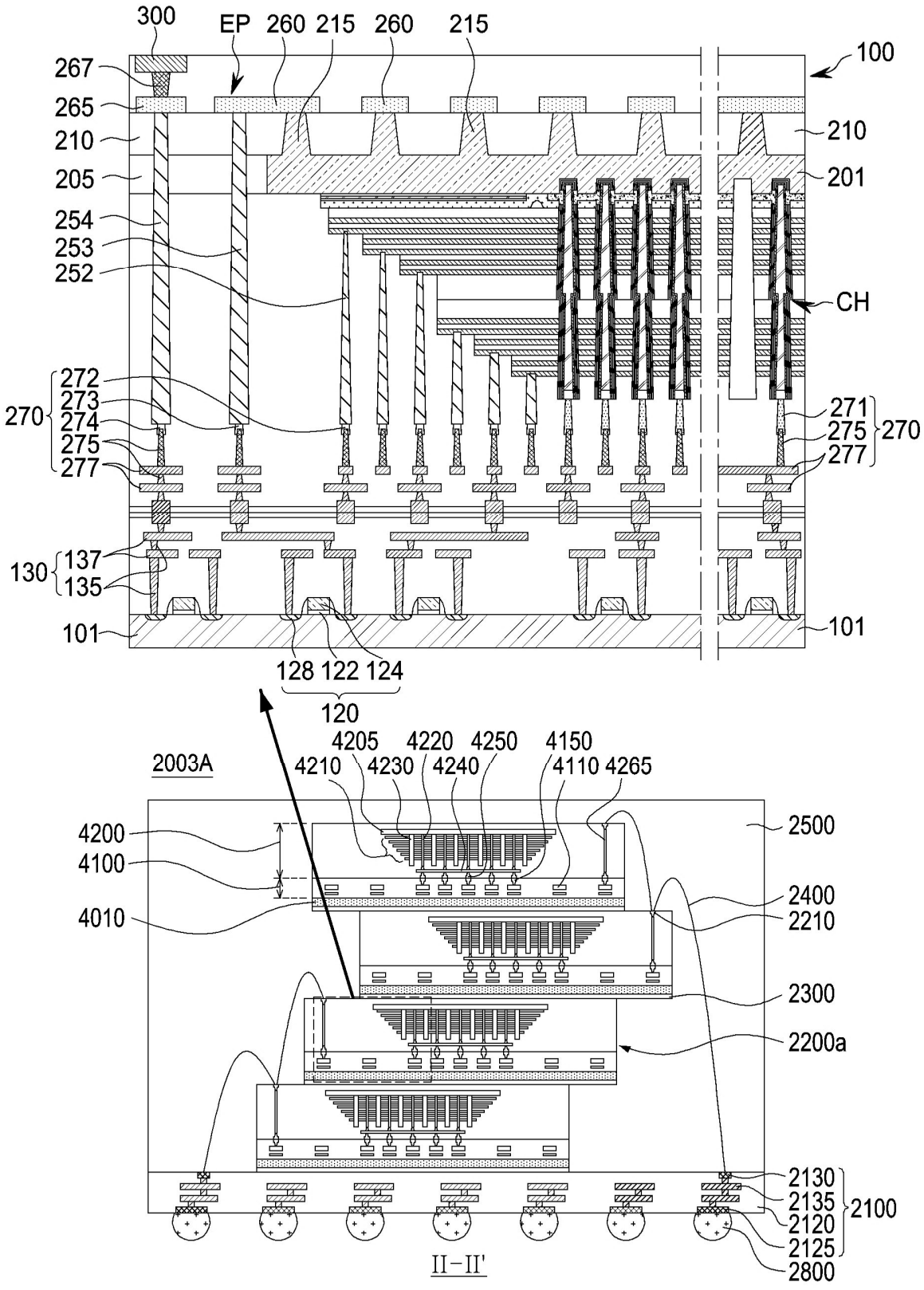

300
EP
260
215
260
215
100
267
265
210
205
254
253
252
210
201
CH
272
273
274
275
277
270
271
275
277
270
130
137
135
101
101
128 122 124
120
2003A
4205
4220
4250
4150
4210
4230
4240
4110
4265
4200
4100
4010
2500
2400
2210
2300
2200a
2130
2135
2120
2125
2100
2800
II-II'

FIG. 19