

FIG. 1

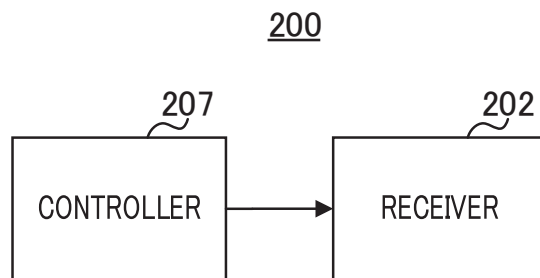


FIG. 2

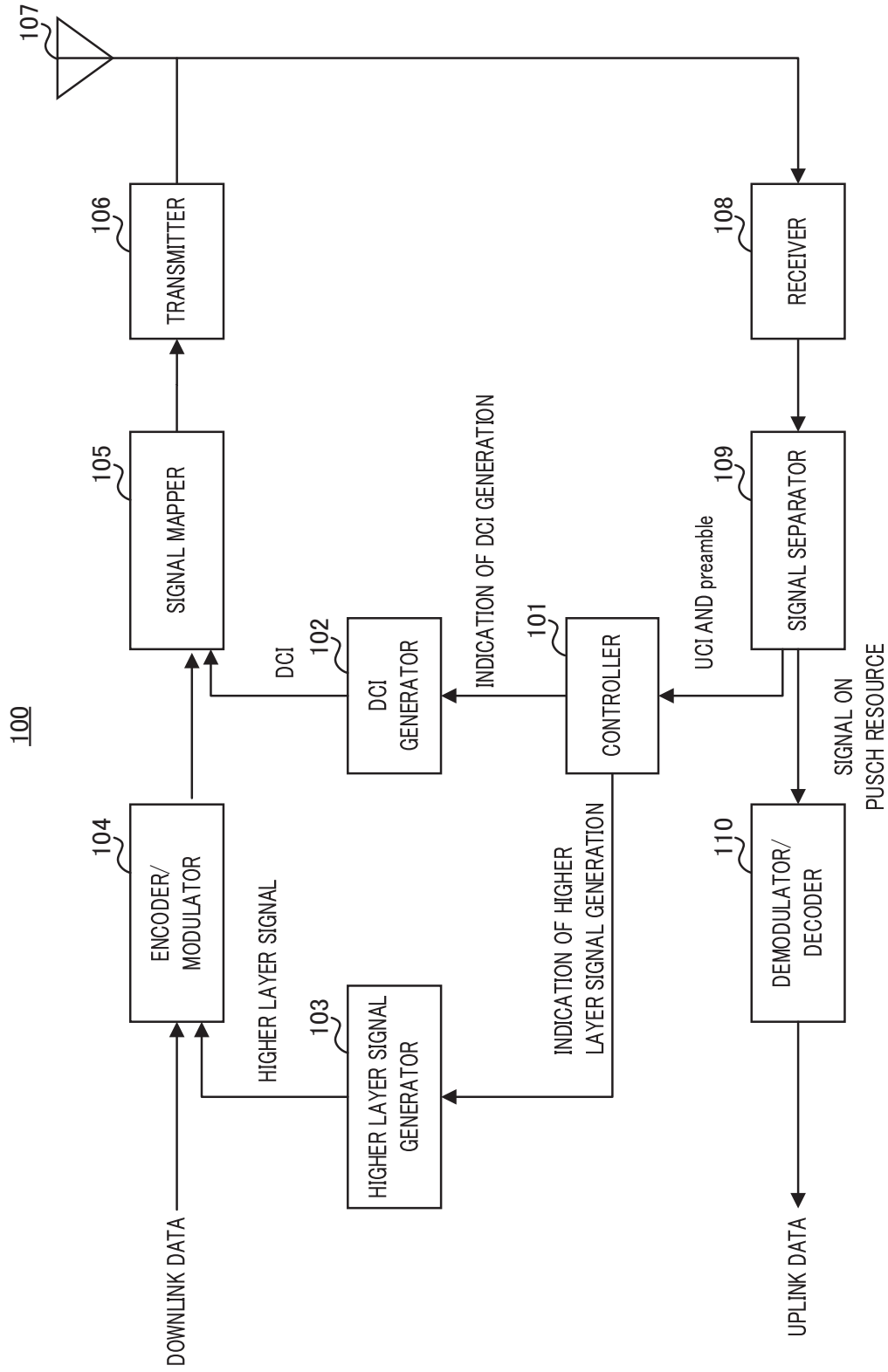


FIG. 3

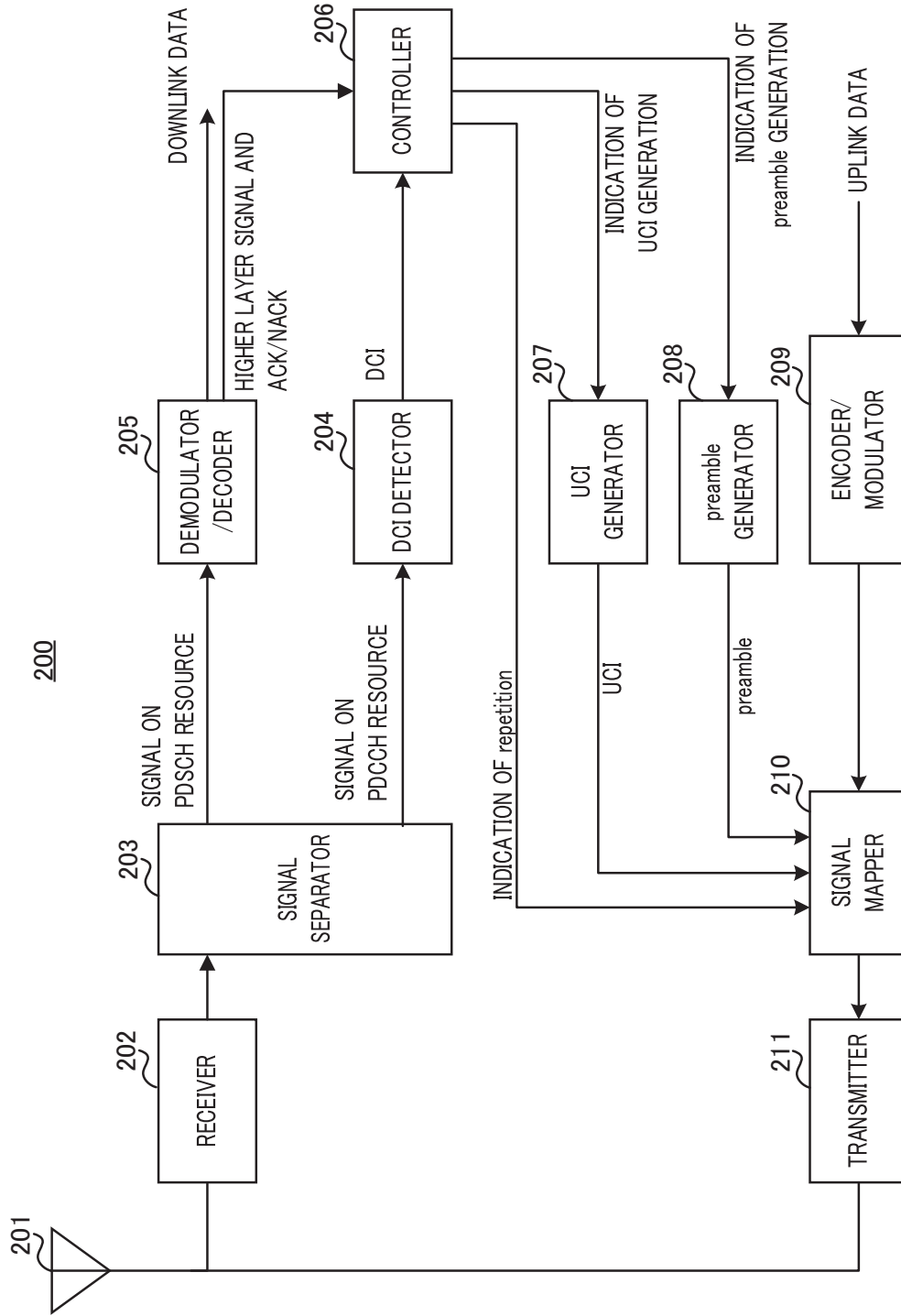


FIG. 4

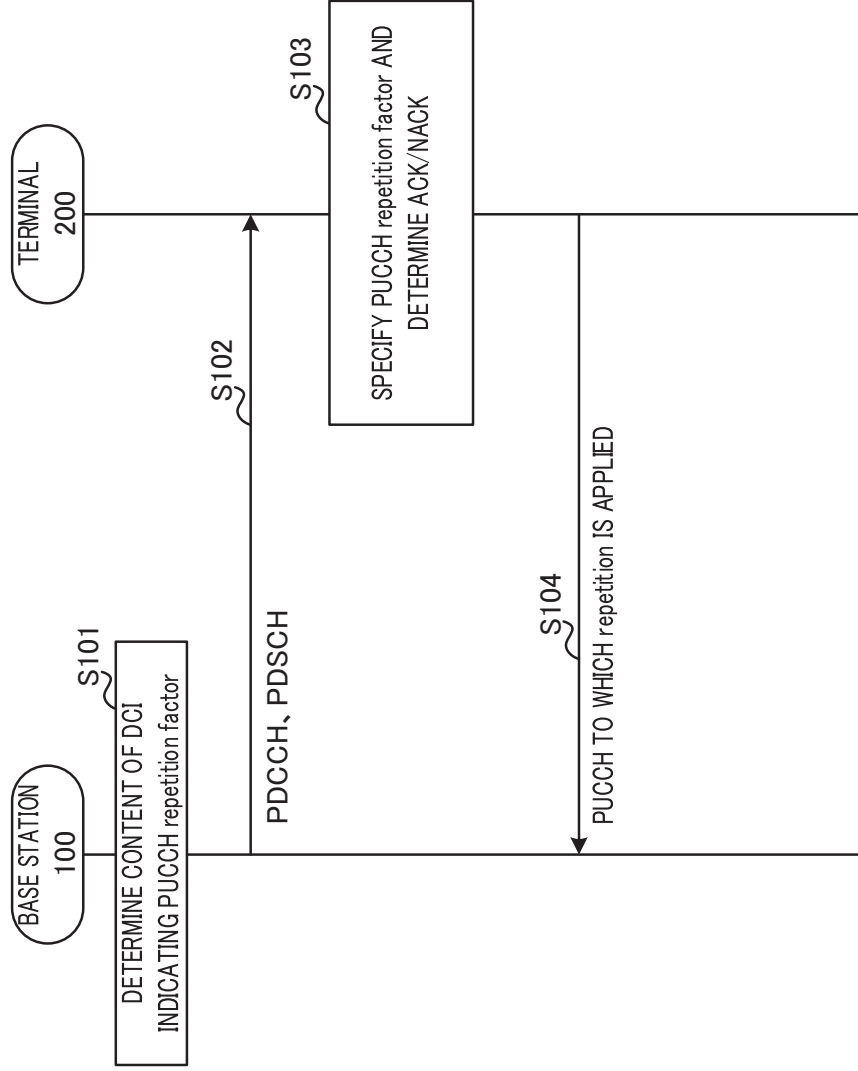


FIG. 5

Reserved bit (two bits) (e.g., Downlink assignment index (two bits))	Repetition factor
00	RF_{00}
01	RF_{01}
10	RF_{10}
11	RF_{11}

FIG. 6

Two bits of PRI	Repetition factor
00	RF_{00}
01	RF_{01}
10	RF_{10}
11	RF_{11}

FIG. 7

Two bits of PDSCH-to-HARQ-timing-indicator	Repetition factor
00	RF_{00}
01	RF_{01}
10	RF_{10}
11	RF_{11}

FIG. 8

One bit of TPC command for scheduled PUCCH	Repetition factor
0	RF_{00}
1	RF_{01}

FIG. 9

Two bits of MCS	Repetition factor
00	RF_{00}
01	RF_{01}
10	RF_{10}
11	RF_{11}

FIG. 10

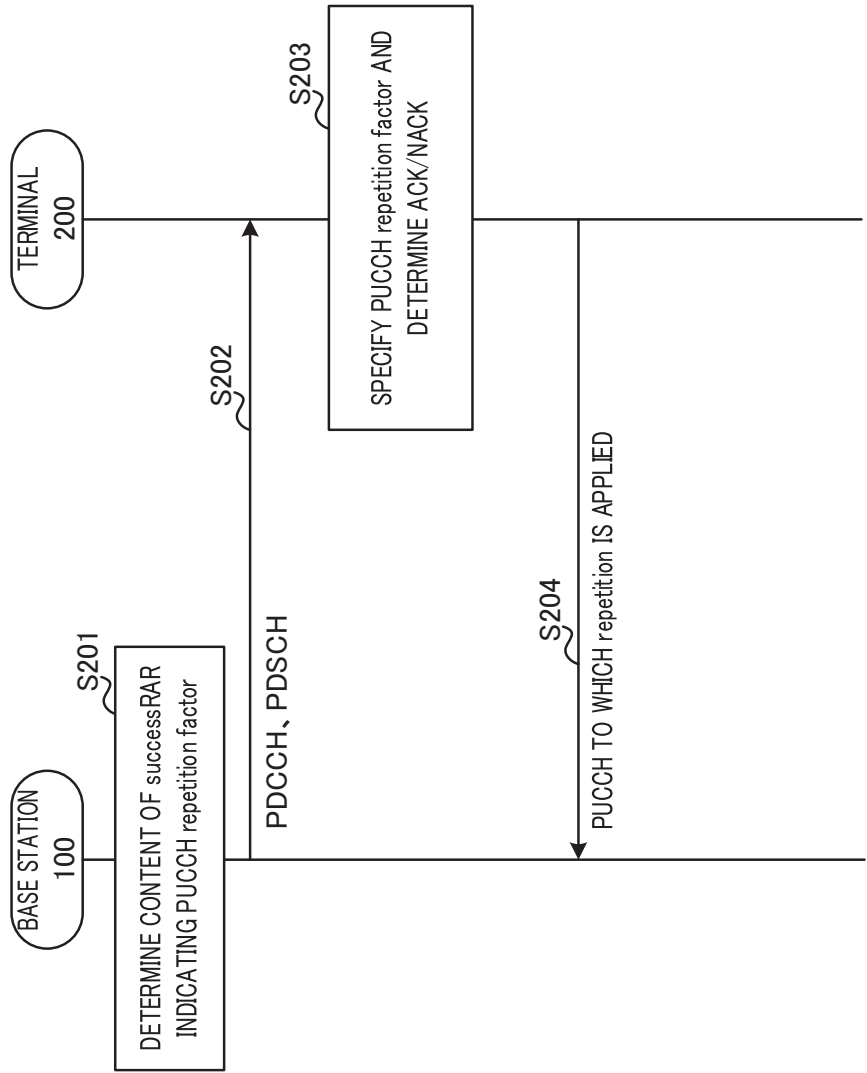


FIG. 11

Two bits of PRI	Repetition factor
00	RF_{00}
01	RF_{01}
10	RF_{10}
11	RF_{11}

FIG. 12

Two bits of PDSCH-to-HARQ-timing-indicator	Repetition factor
00	RF_{00}
01	RF_{01}
10	RF_{10}
11	RF_{11}

FIG. 13

One bit of TPC command for scheduled PUCCH	Repetition factor
0	RF_{00}
1	RF_{01}

FIG. 14

Three bits of DCI or successRAR	Repetition factor	Configuration of frequency hopping
000	1	None (Disabled)
001	2	None (Disabled)
010	4	None (Disabled)
011	8	None (Disabled)
100	8	Enabled, Intra-slot hopping
101	8	Enabled, Inter-slot hopping
110	16	Enabled, Intra-slot hopping
111	16	Enabled, Inter-slot hopping

FIG. 15

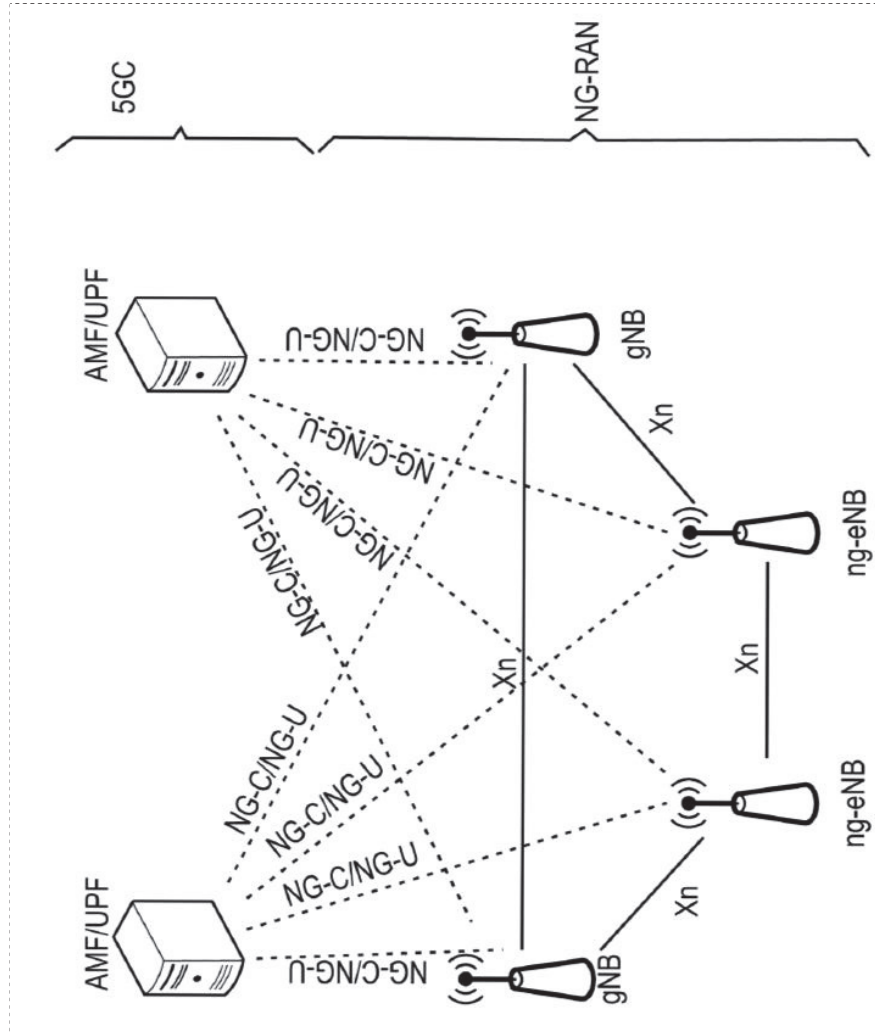


FIG. 16

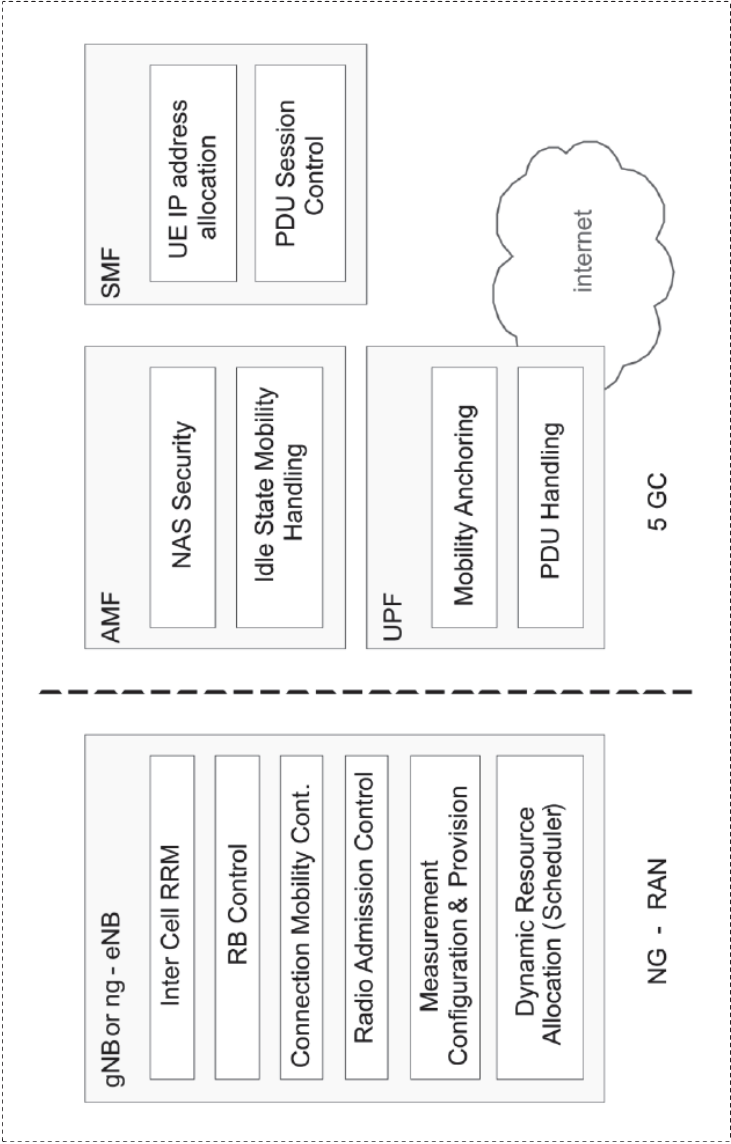


FIG. 17

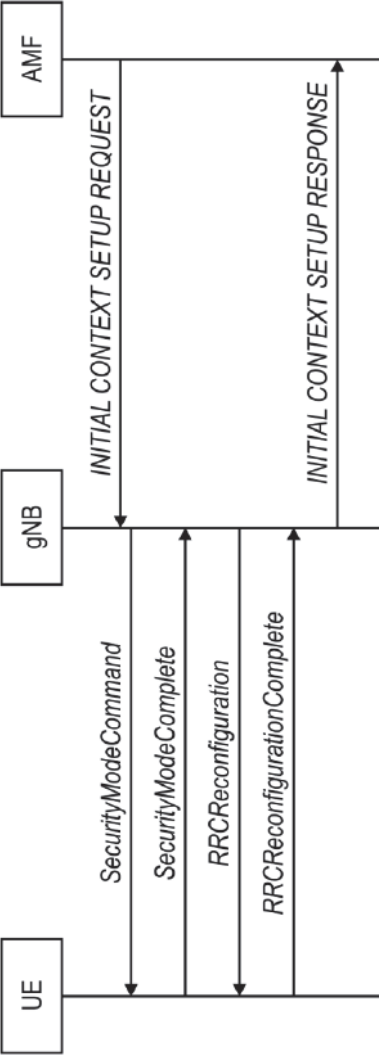


FIG. 18

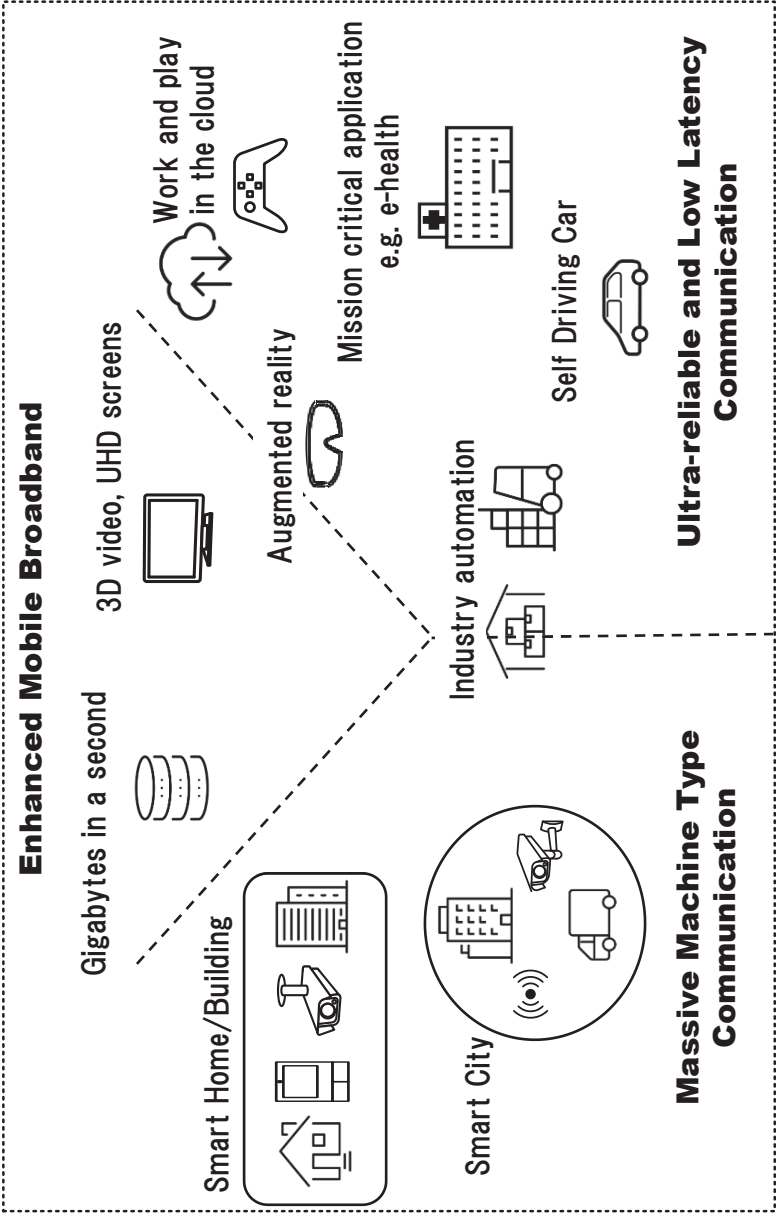


FIG. 19

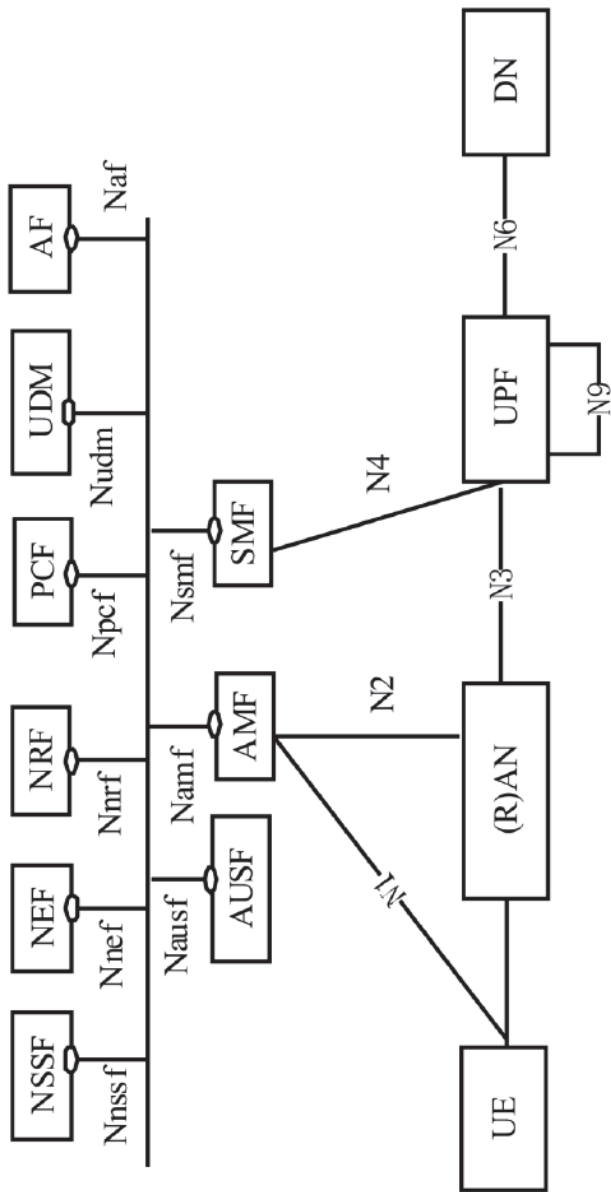


FIG. 20