

FIG. 1

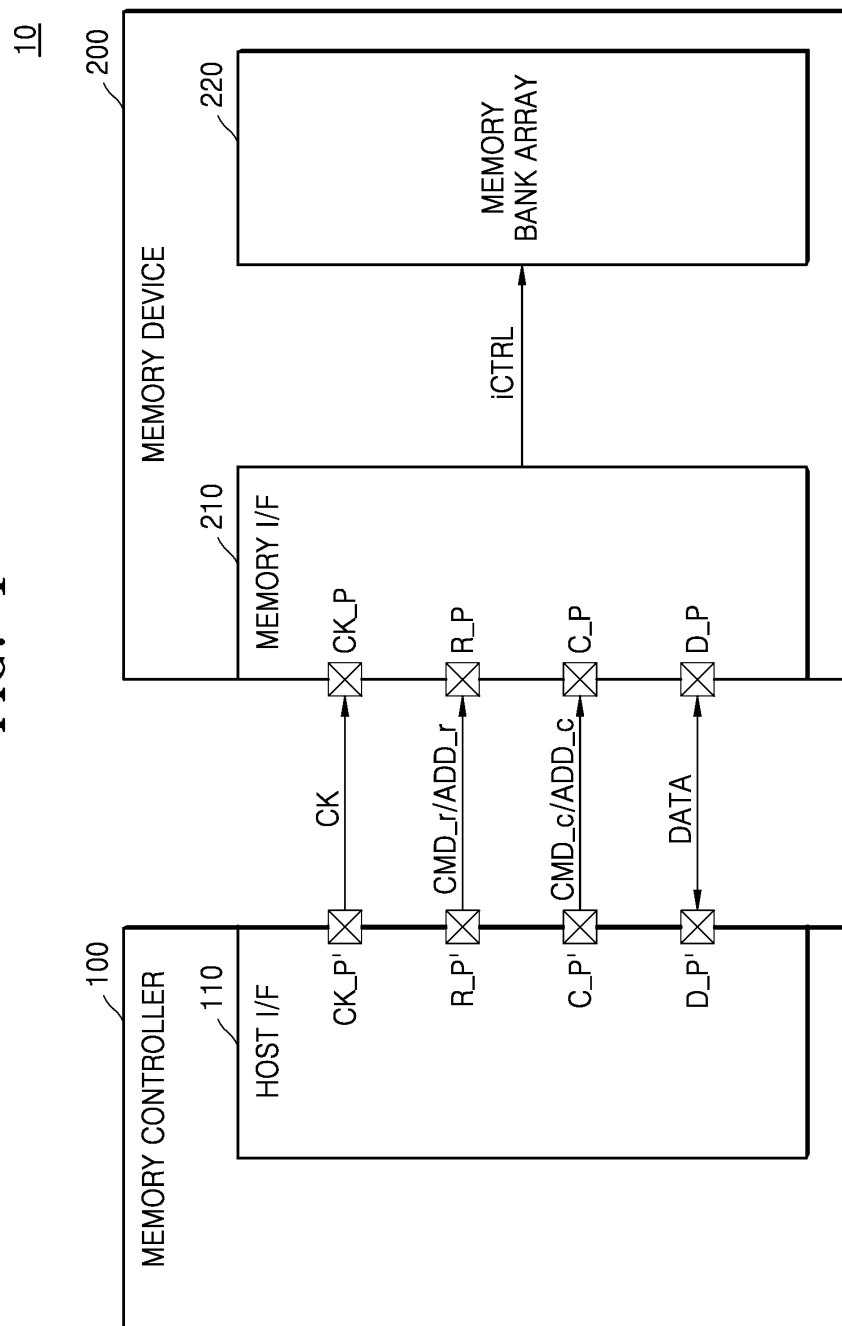


FIG. 2

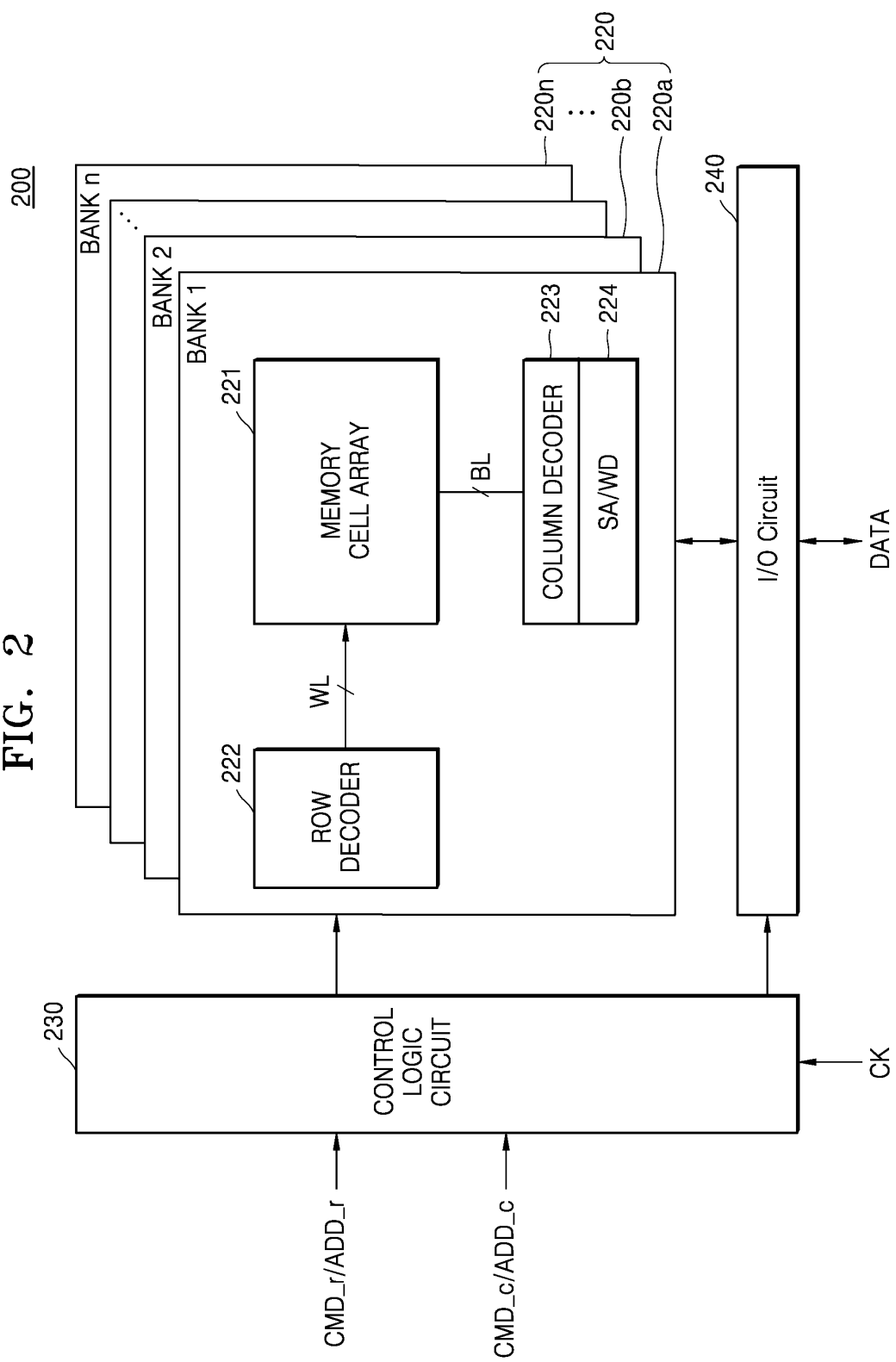


FIG. 3A

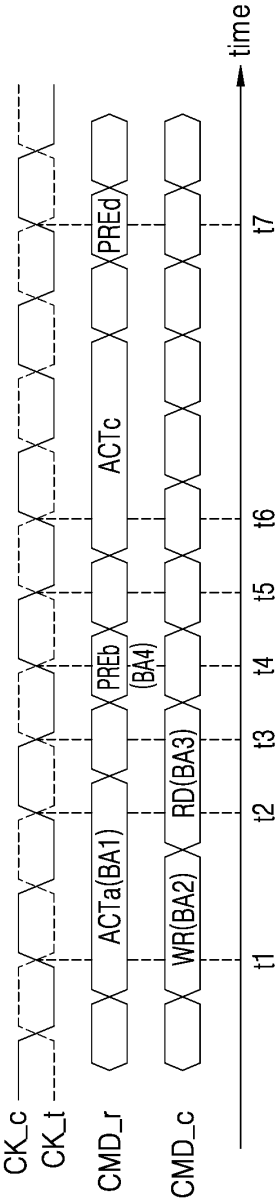


FIG. 3B

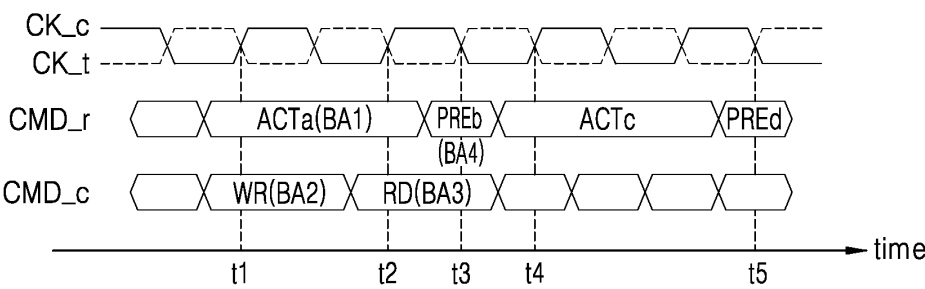


FIG. 3C

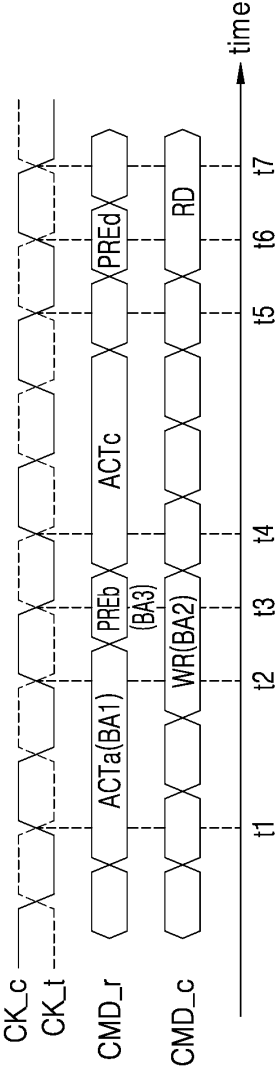


FIG. 4A

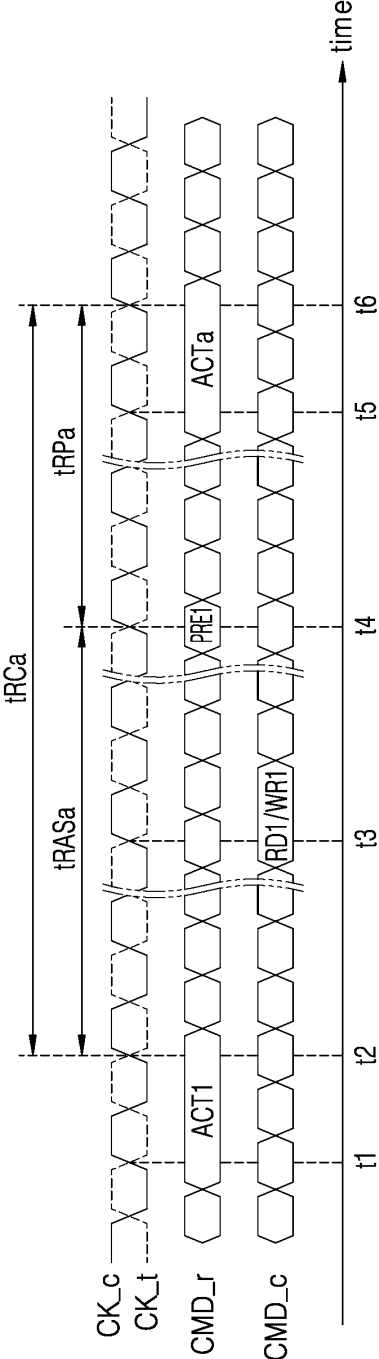


FIG. 5

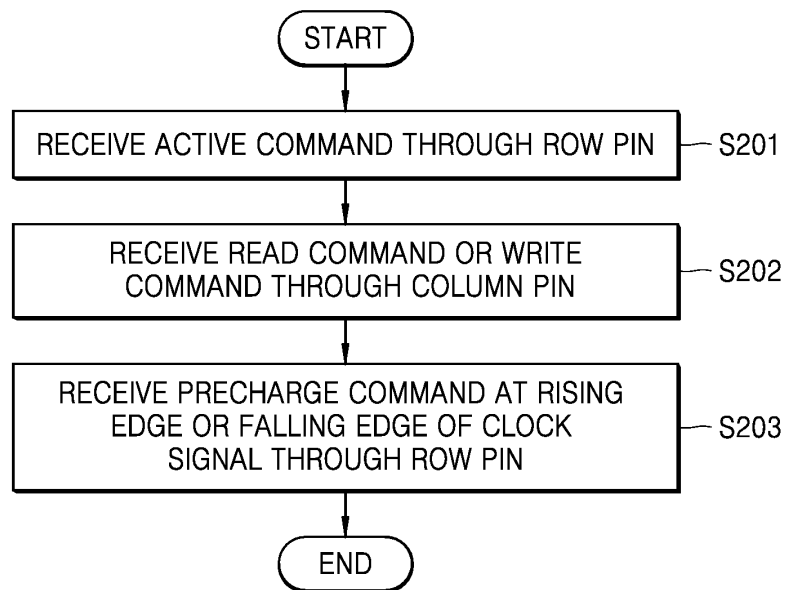


FIG. 6

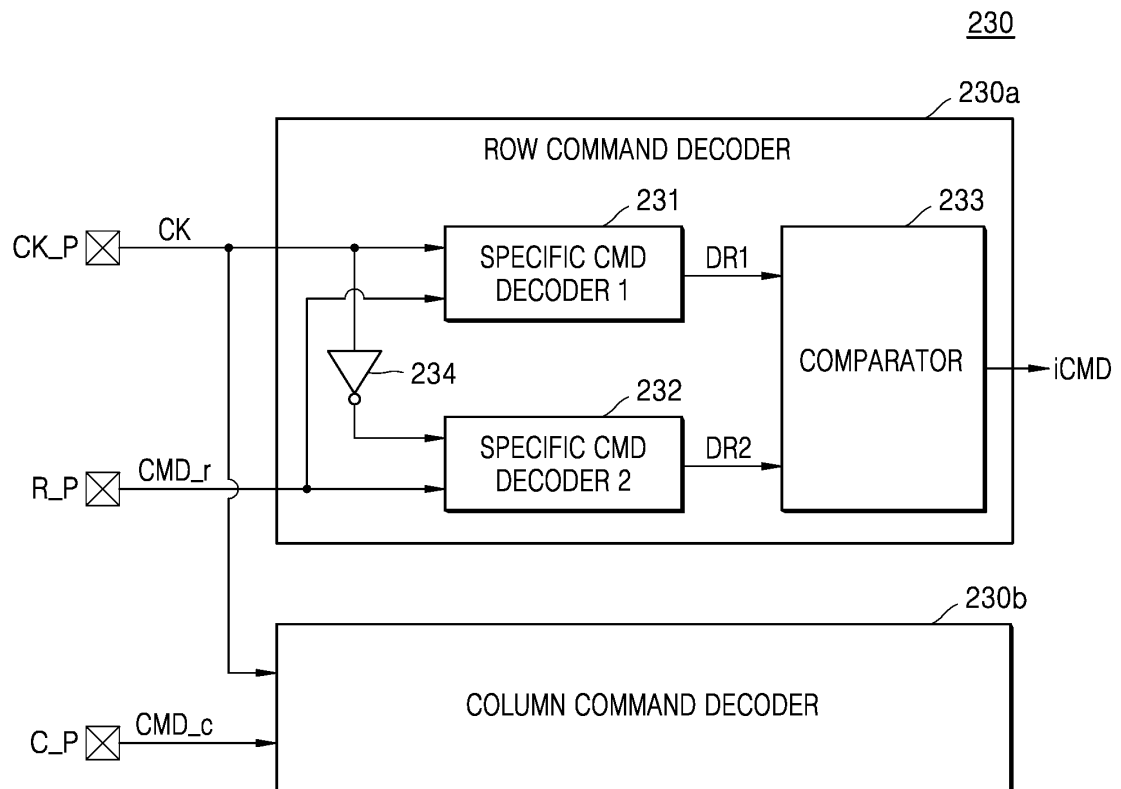


FIG. 7

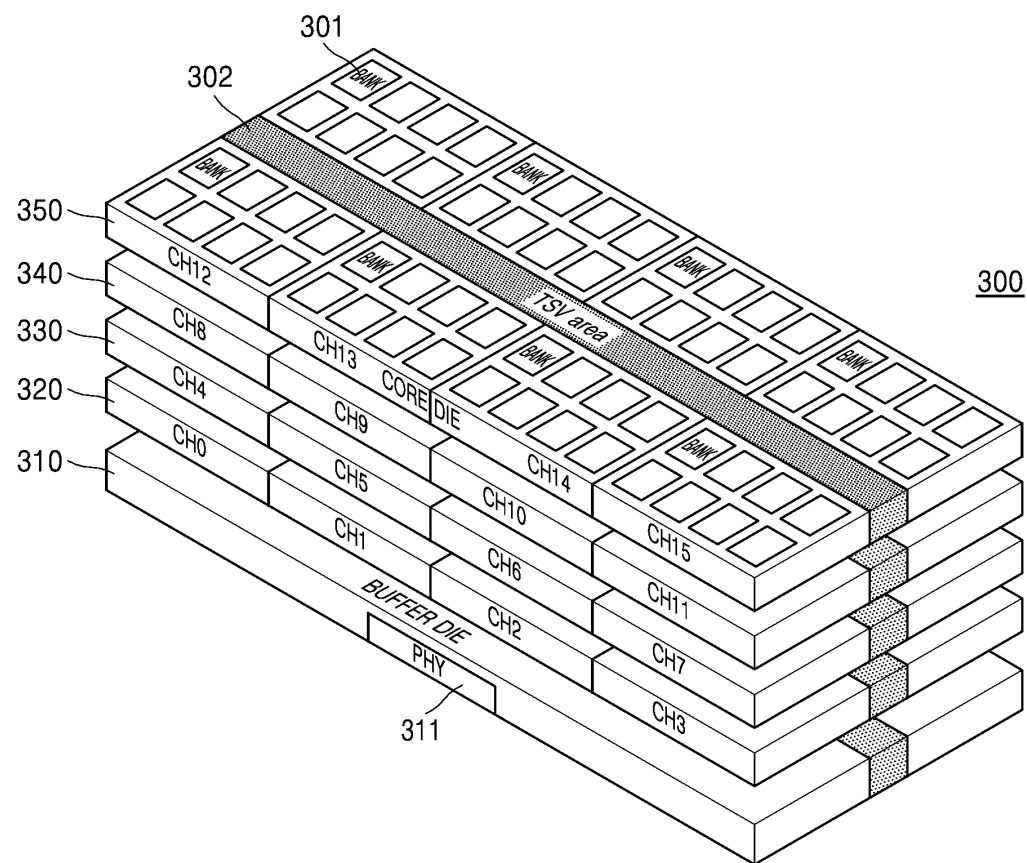


FIG. 8

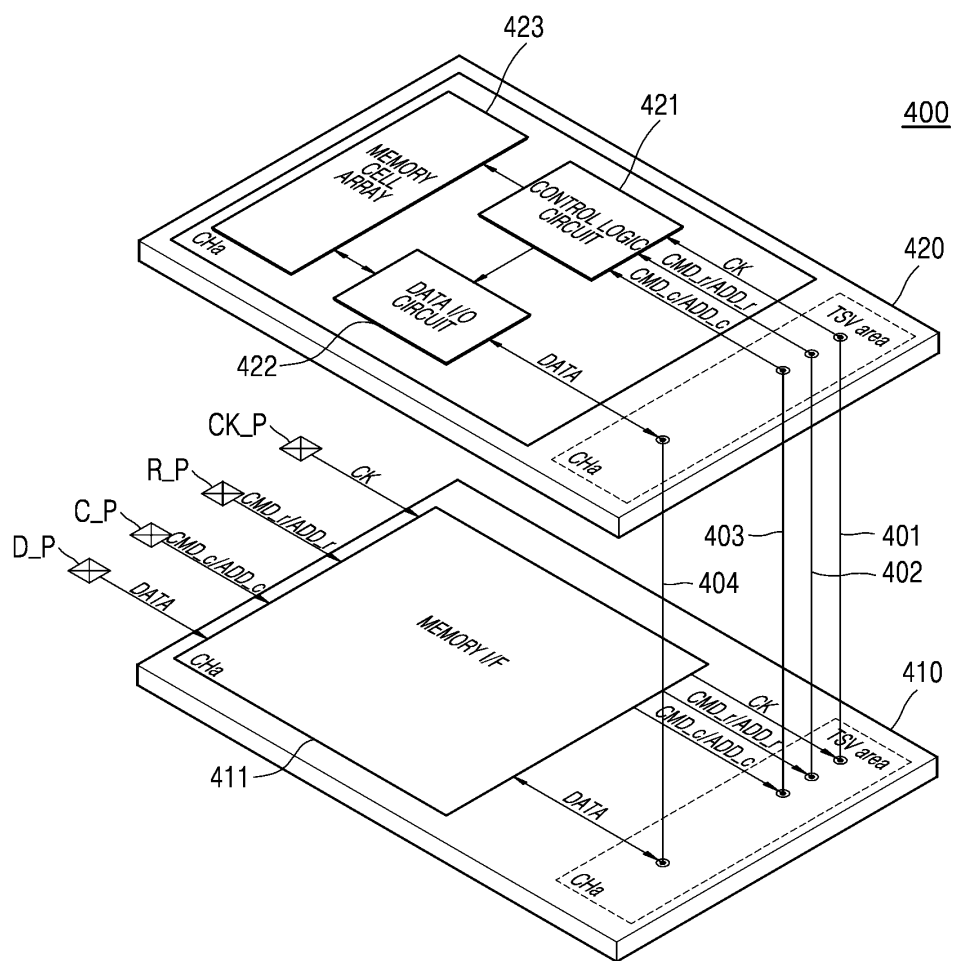


FIG. 9

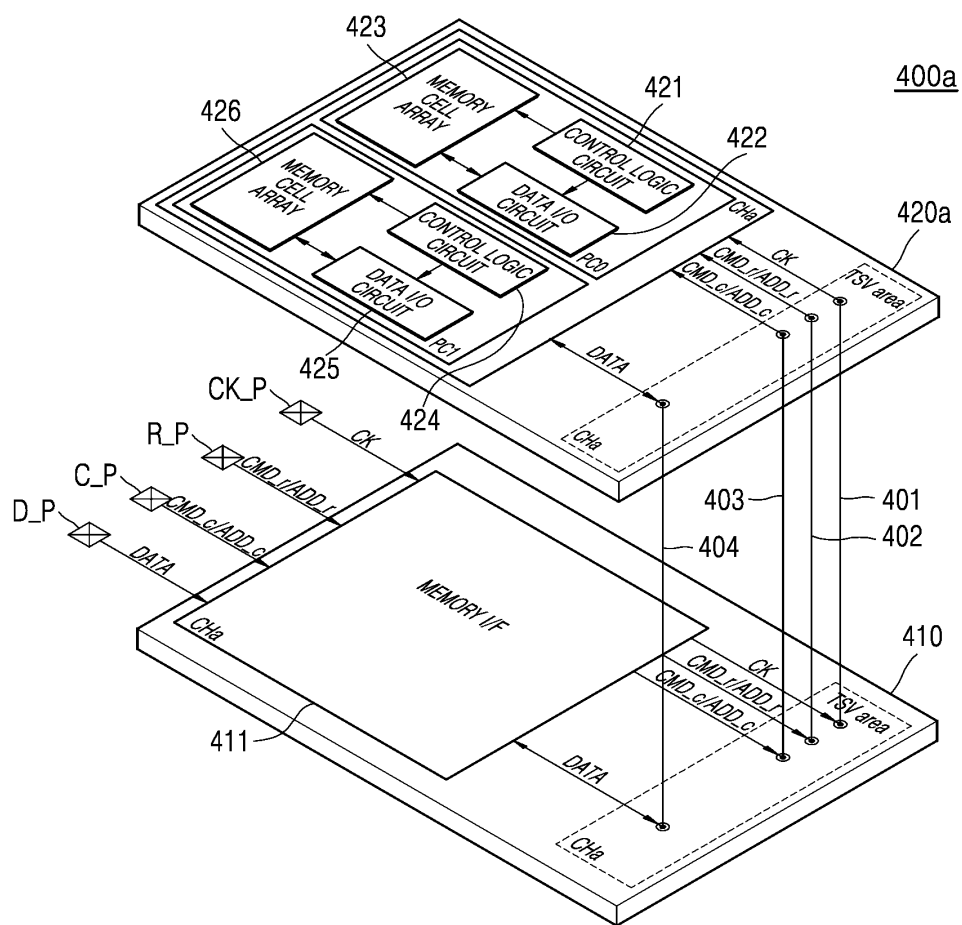


FIG. 10

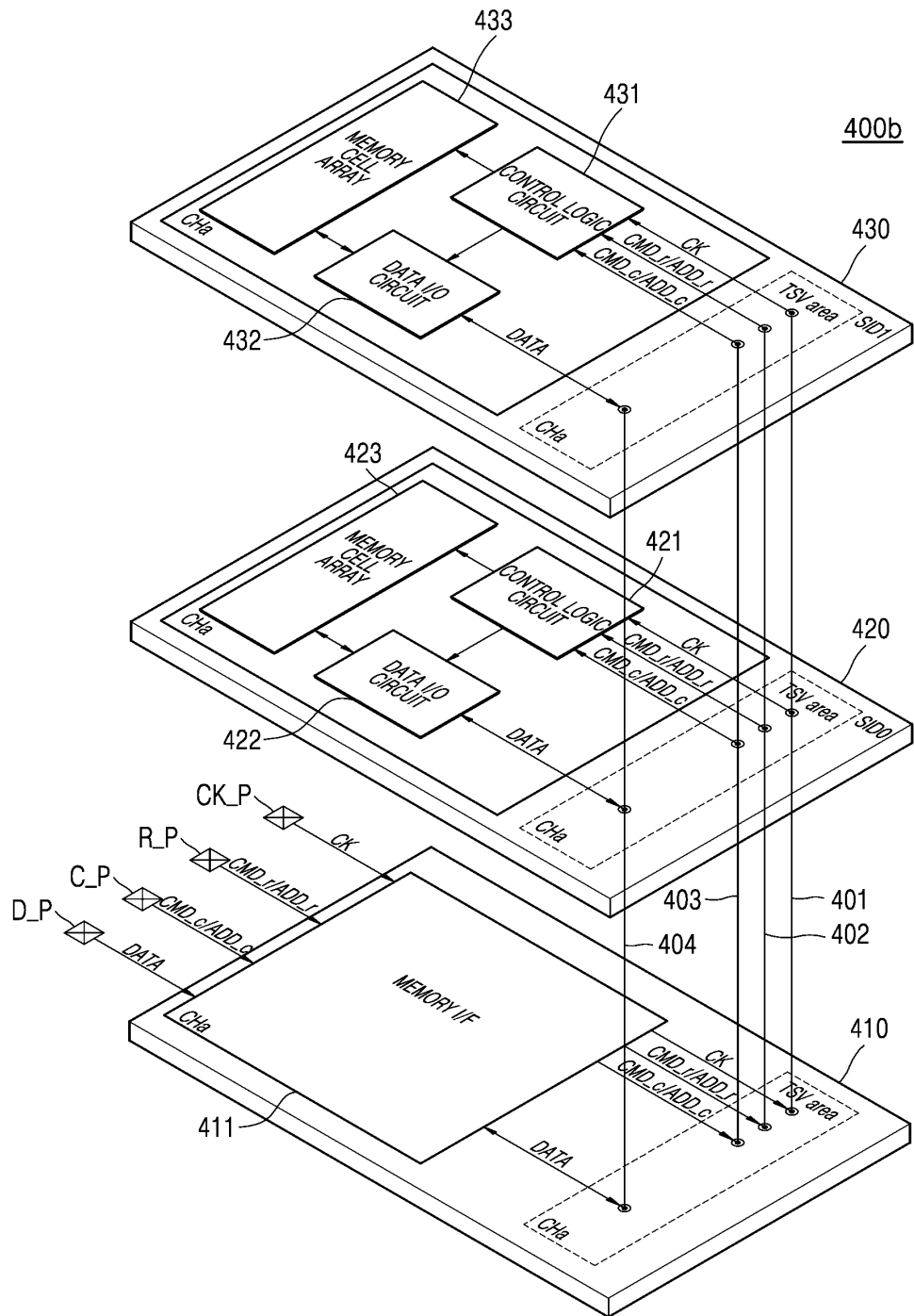


FIG. 11A

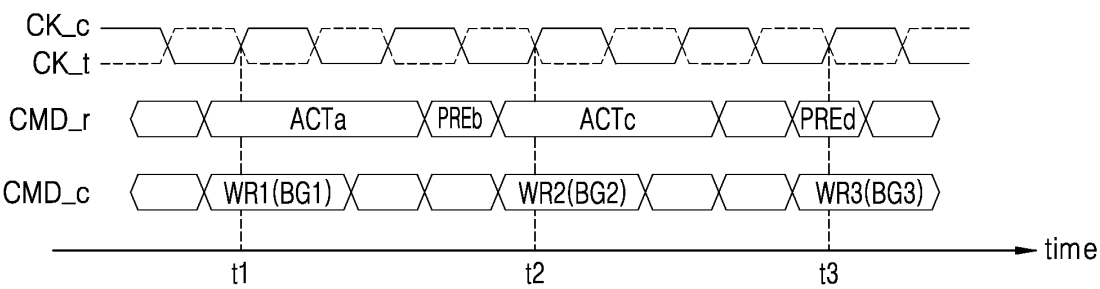


FIG. 11B

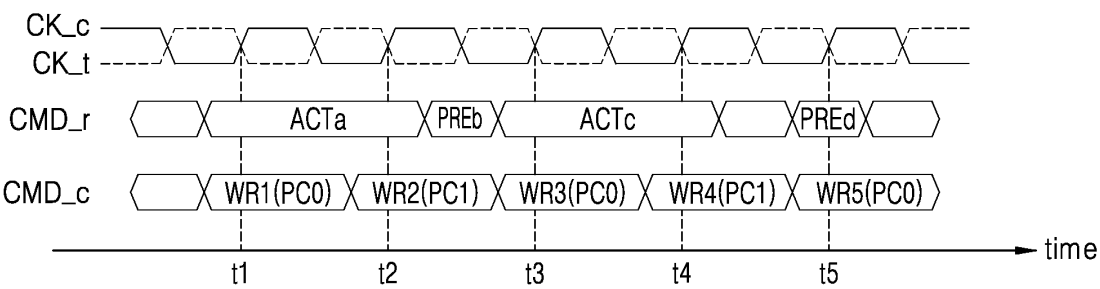


FIG. 12A

ROW COMMAND	CLOCK CYCLE	R_P0	R_P1	R_P2	R_P3	R_P4	R_P5	R_P6	R_P7	R_P8	R_P9
RNOP	R or F	H	H	H	H	V	V	V	V	V	V
ACT	R	L	H	H	PC	SID0/V	SID1/V	BA0	BA1	BA2	BA3
	F	H	H	RA8	RA9	RA10	RA11	RA12	RA13/V	RA14/V	RA15/V
	R	H	H	RA0	RA1	RA2	RA3	RA4	RA5	RA6	RA7
PREpb	R or F	H	L	L	PC	SID0/V	SID1/V	BA0	BA1	BA2	BA3
PREab	R or F	H	L	H	PC	V	V	V	V	V	V
REFpb	R	L	L	L	PC	SID0/V	SID1/V	BA0	BA1	BA2	BA3
REFab	R	H	H	L	PC	V	V	V	V	L	V
RFMpb	R	L	L	H	PC	SID0/V	SID1/V	BA0	BA1	BA2	BA3
RFMab	R	H	H	L	PC	V	V	V	V	H	V
PDE	R	L	H	L	H	V	V	V	V	V	V
	F	L	H	L	H	V	V	V	V	V	V
SRE	R	L	H	L	L	V	V	V	V	V	V
	F	L	H	L	L	V	V	V	V	V	V
PDX/SRX	R	H	H	H	H	V	V	V	V	V	V

FIG. 12B

COLUMN COMMAND	CLOCK CYCLE	C_P0	C_P1	C_P2	C_P3	C_P4	C_P5	C_P6	C_P7
CNOP	R	H	H	H	V	V	V	V	V
	F	V	V	V	V	V	V	V	V
RD	R	H	L	H	L	PC	SID0/V	SID1/V	BA0
	F	BA1	BA2	BA3	CA0	CA1	CA2	CA3	CA4
RDA	R	H	L	H	H	PC	SID0/V	SID1/V	BA0
	F	BA1	BA2	BA3	CA0	CA1	CA2	CA3	CA4
WR	R	H	L	L	L	PC	SID0/V	SID1/V	BA0
	F	BA1	BA2	BA3	CA0	CA1	CA2	CA3	CA4
WRA	R	H	L	L	H	PC	SID0/V	SID1/V	BA0
	F	BA1	BA2	BA3	CA0	CA1	CA2	CA3	CA4
MRS	R	L	L	L	MA4	OP5	OP6	OP7	MA0
	F	MA1	MA2	MA3	OP0	OP1	OP2	OP3	OP4
MRR	R	L	L	H	MA4	V	V	V	MA0
	F	MA1	MA2	MA3	V	V	V	V	V

FIG. 13

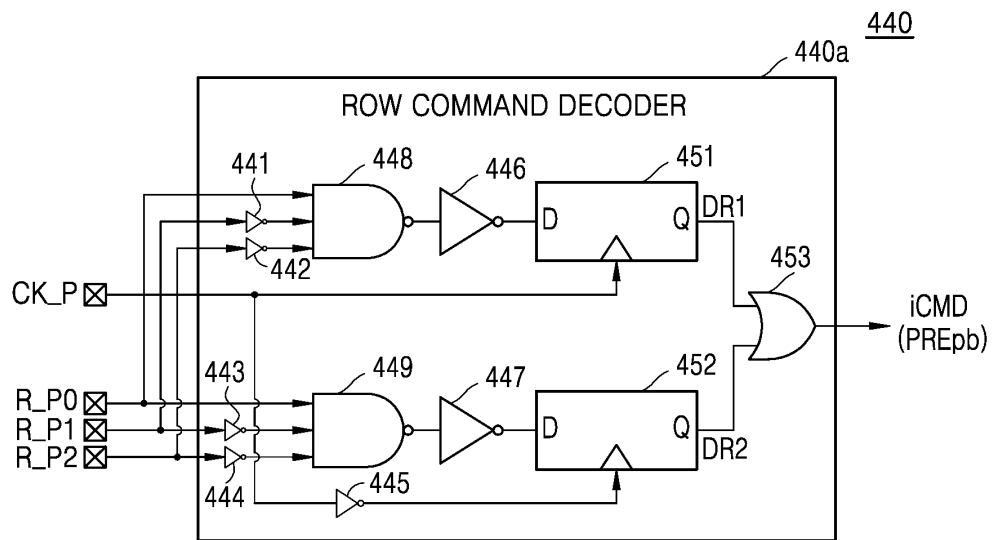


FIG. 14

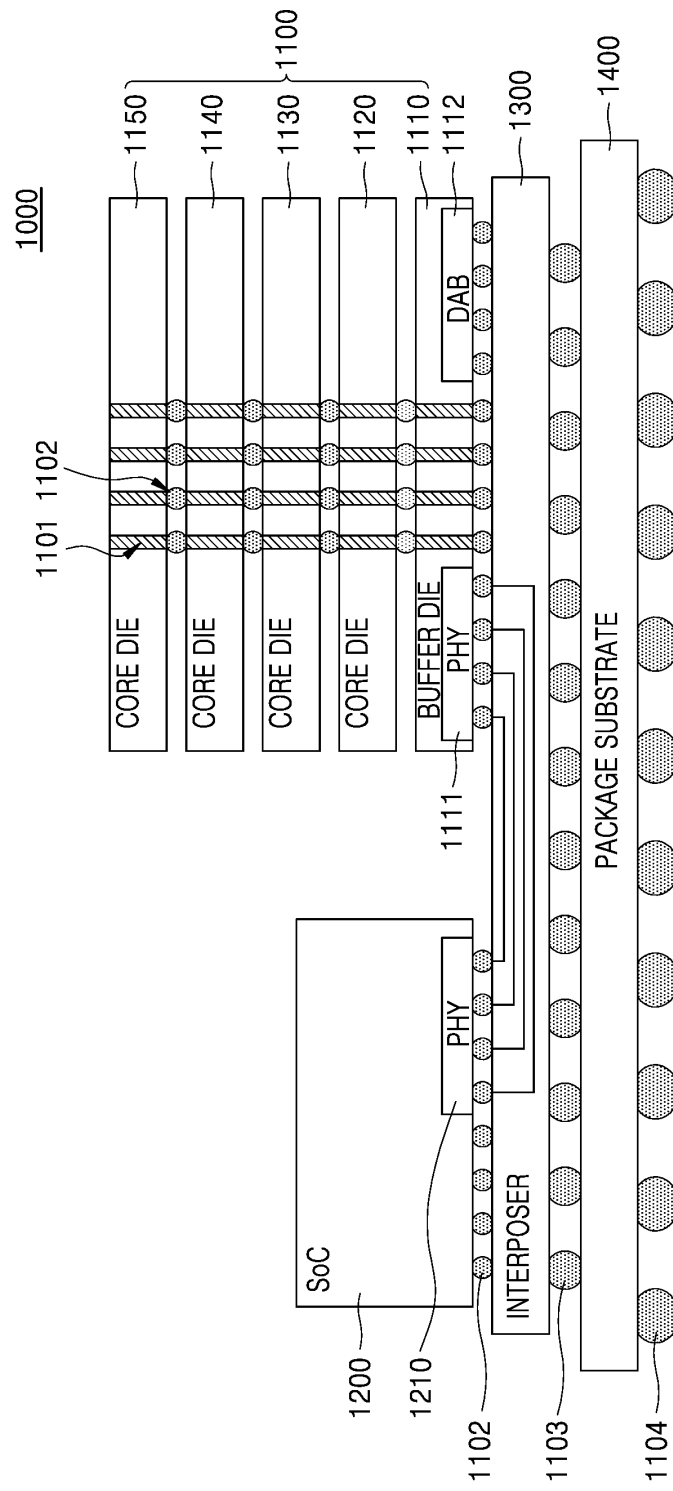


FIG. 15

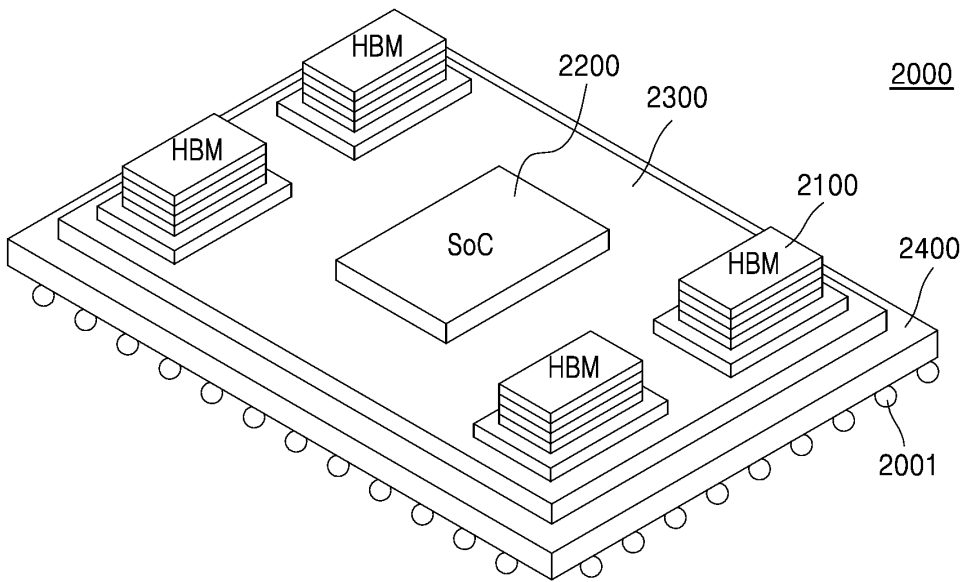


FIG. 16

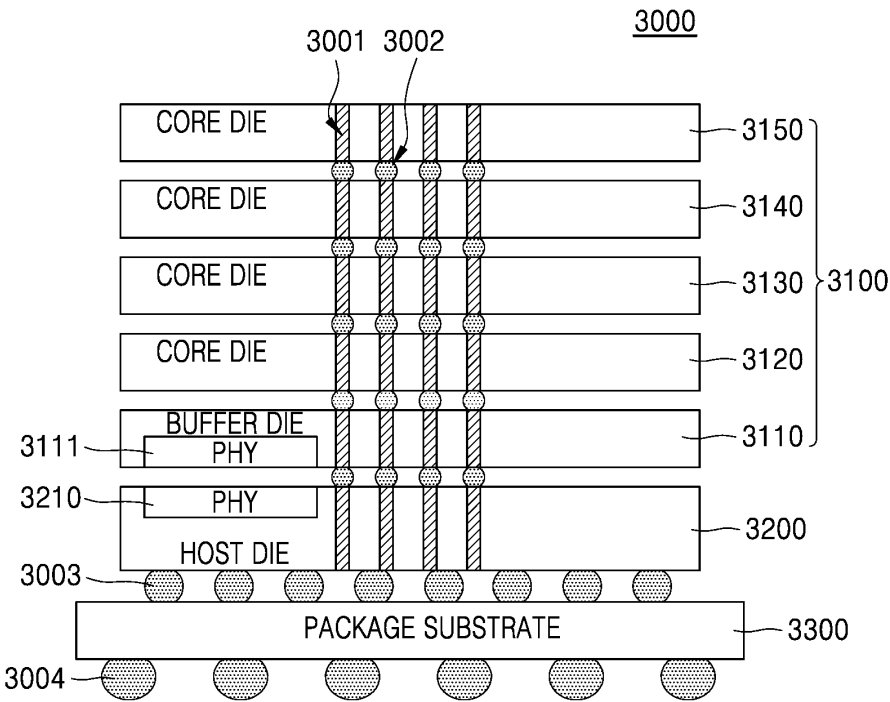


FIG. 17

