

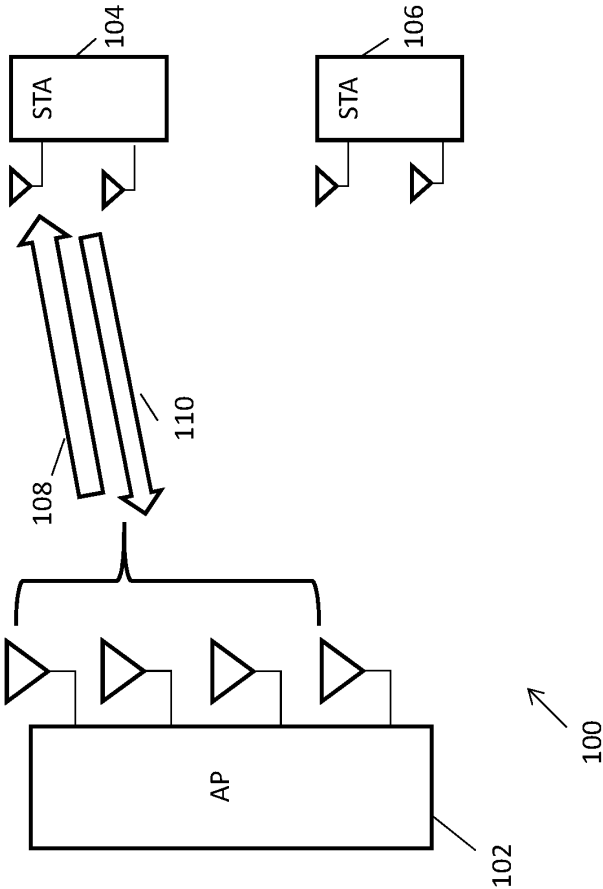


Fig. 1A

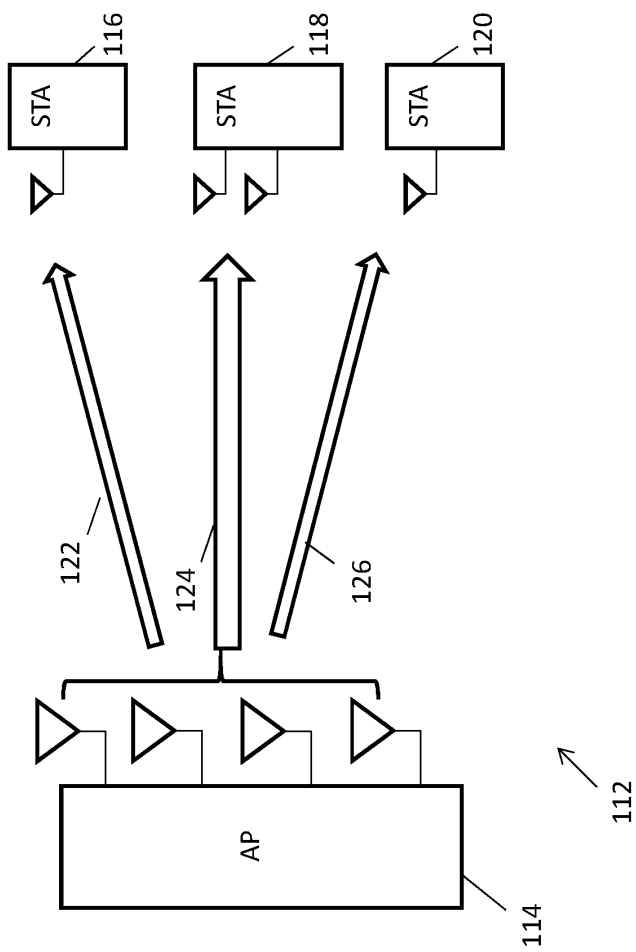


Fig. 1B

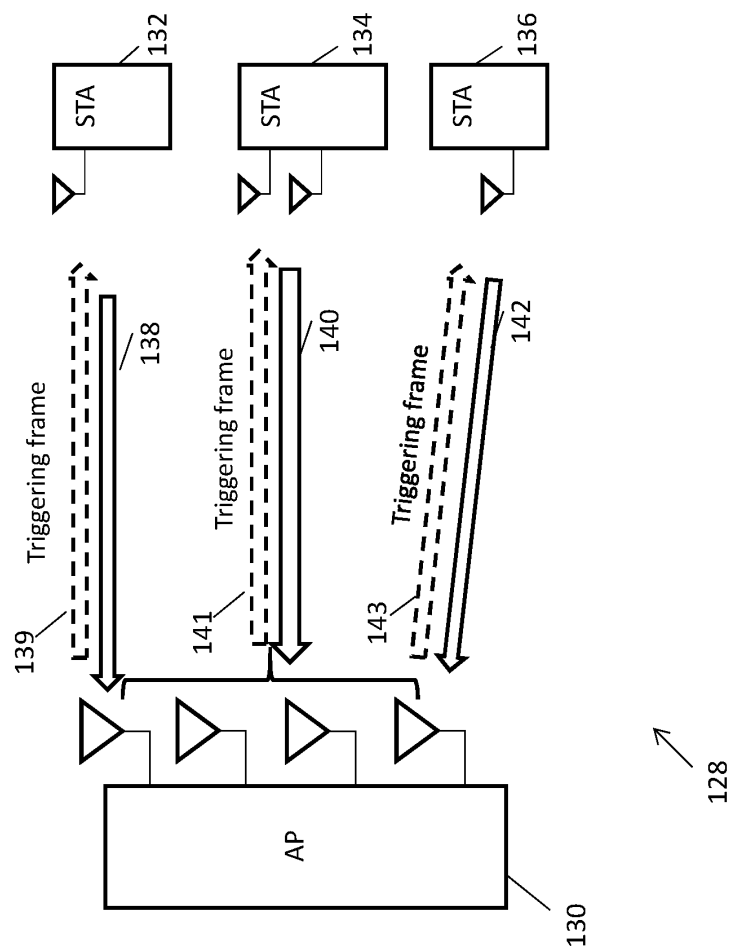


Fig. 1C

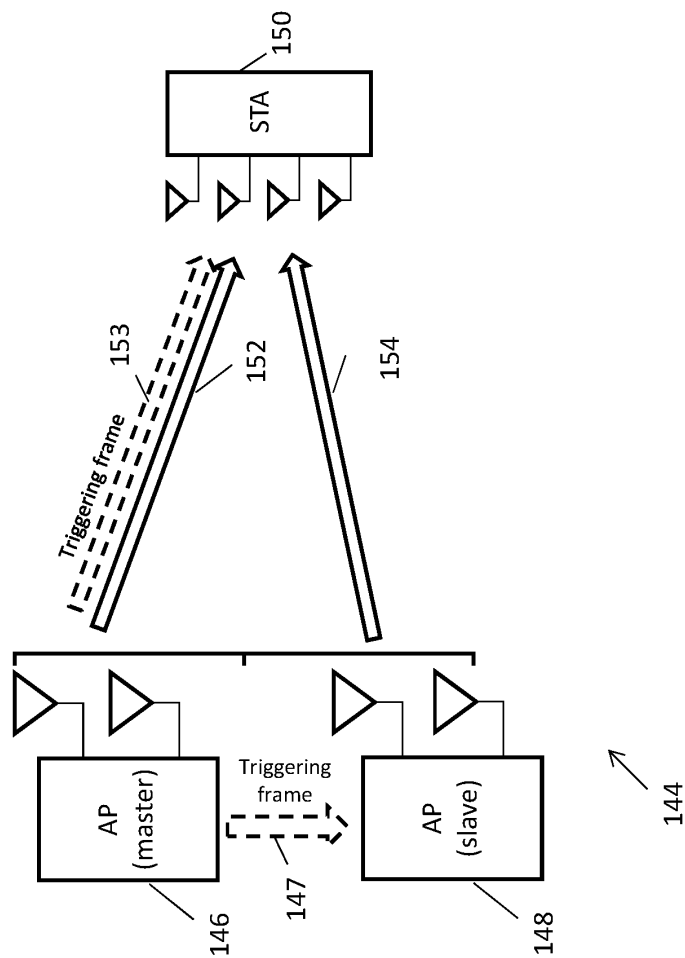


Fig. 1D

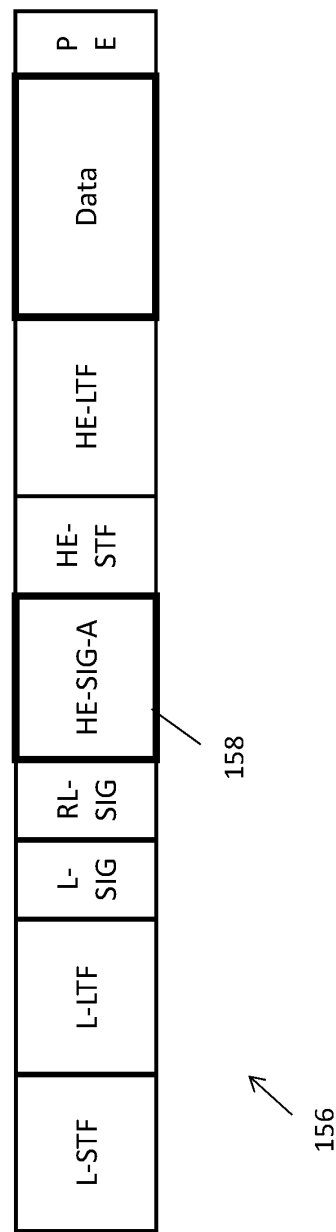


Fig. 2A

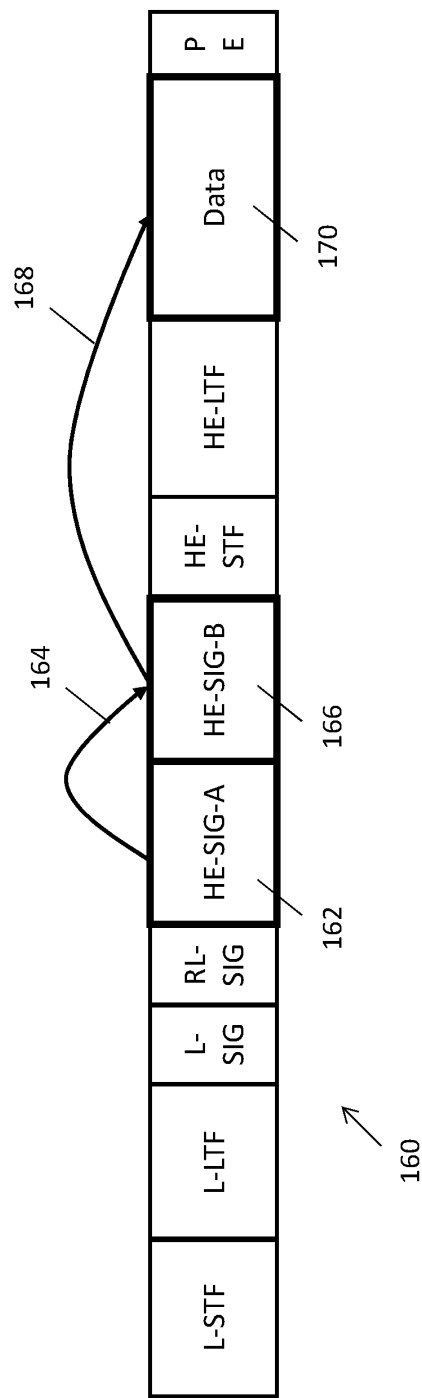


Fig. 2B

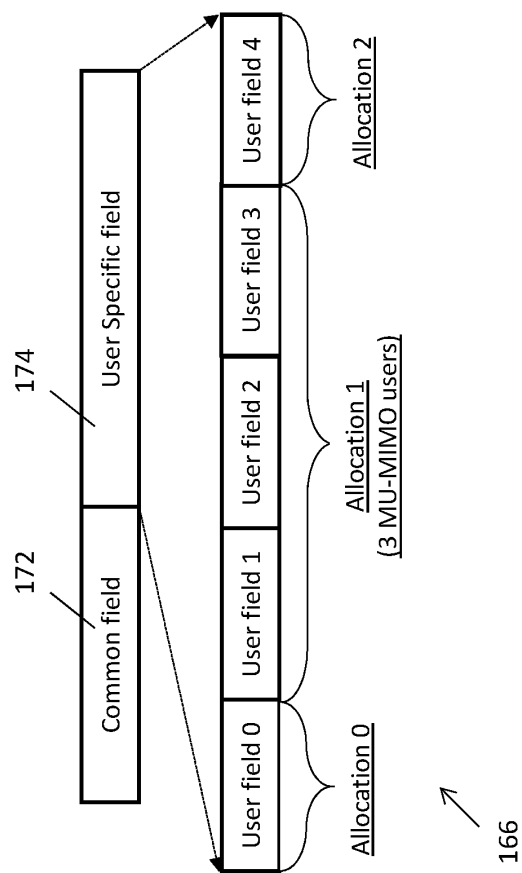


Fig. 2C

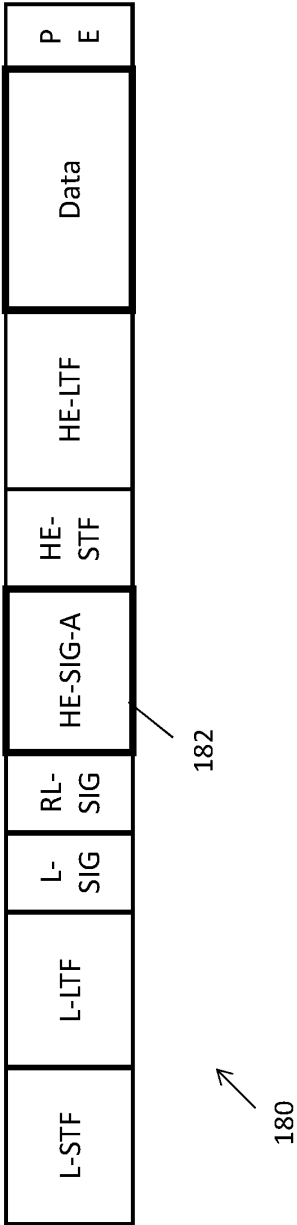


Fig. 2D

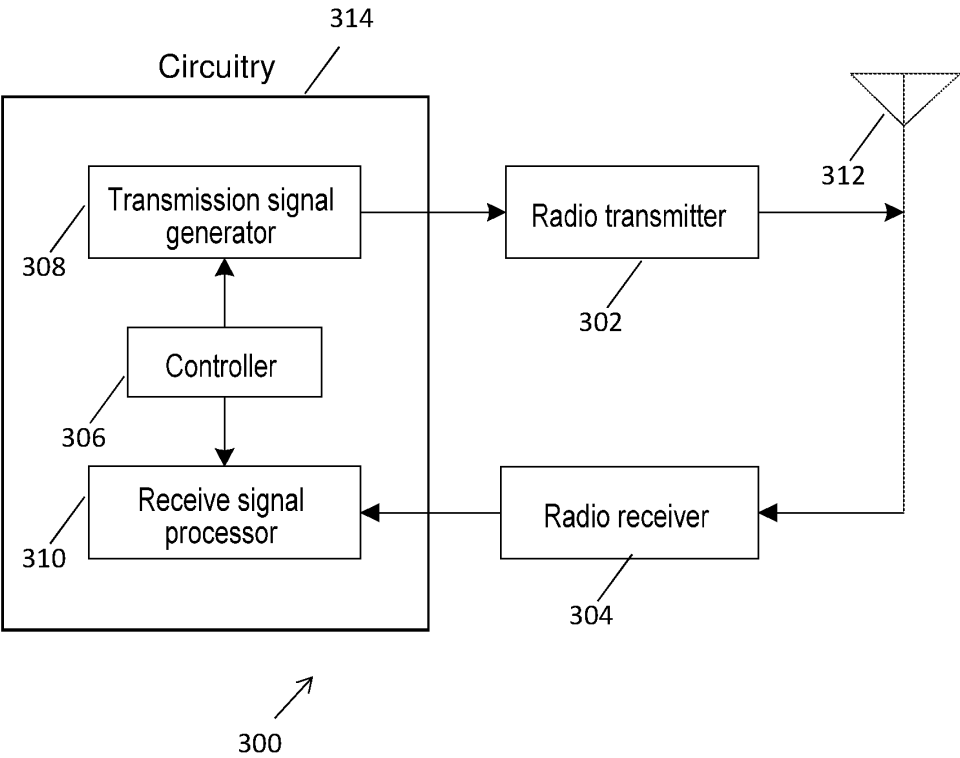


Fig. 3A

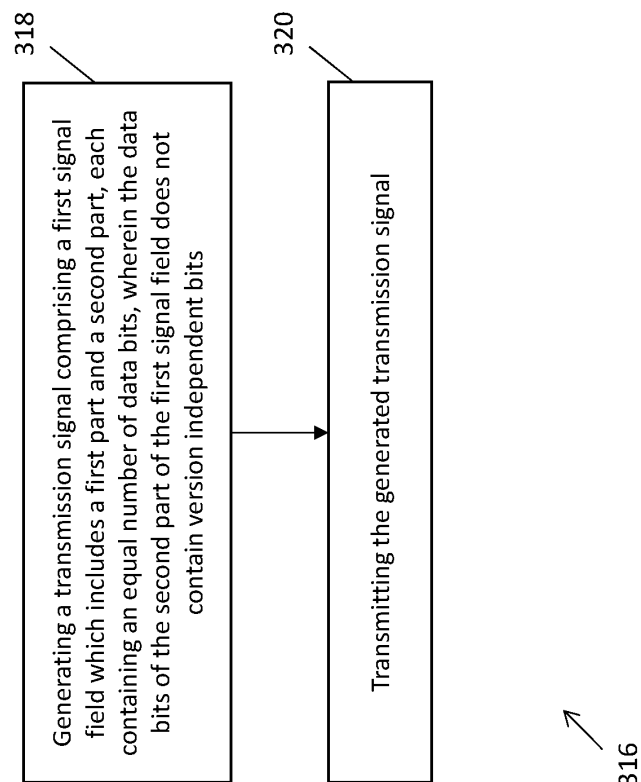


Fig. 3B

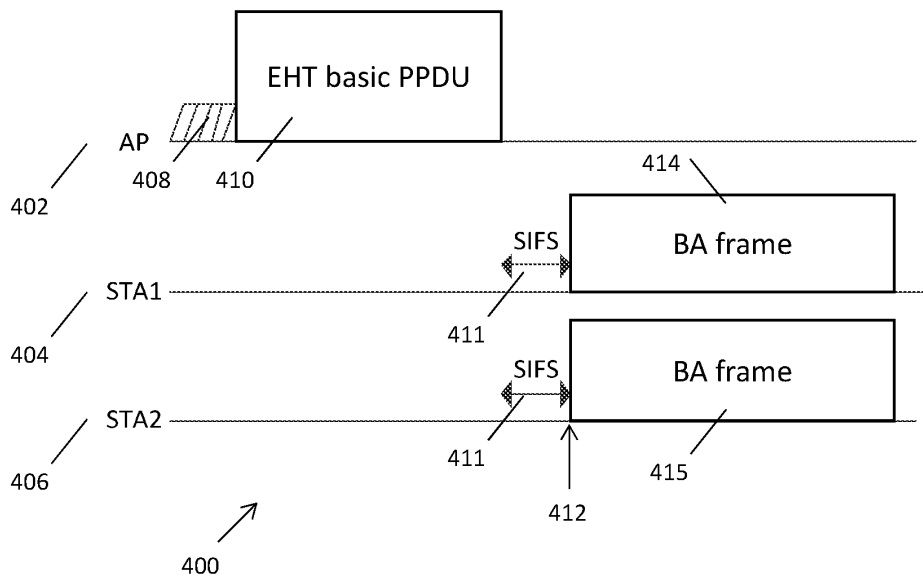


Fig. 4A

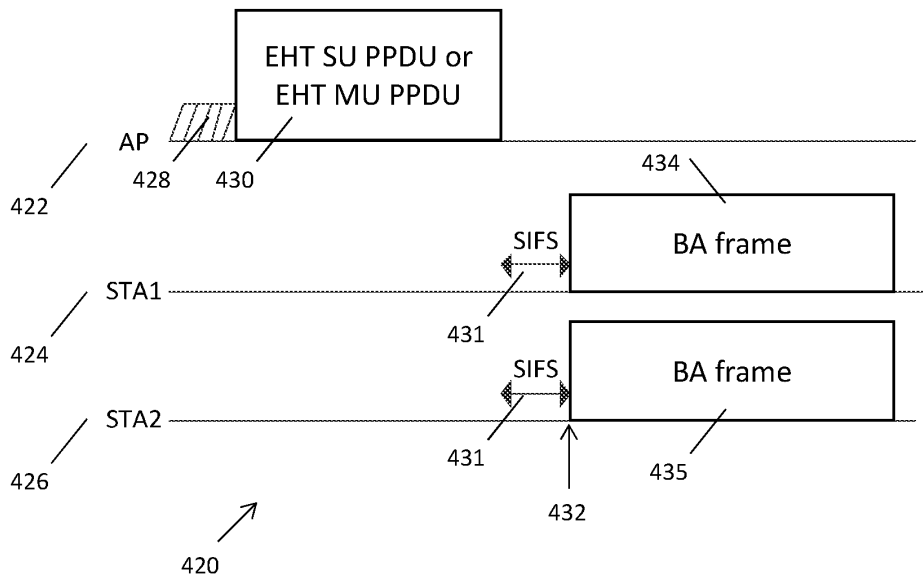


Fig. 4B

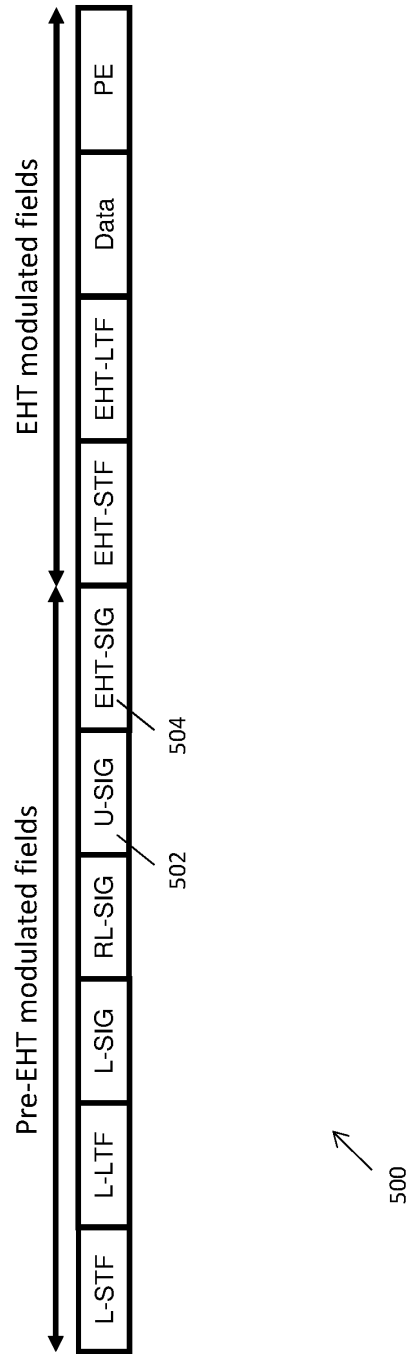


Fig. 5A

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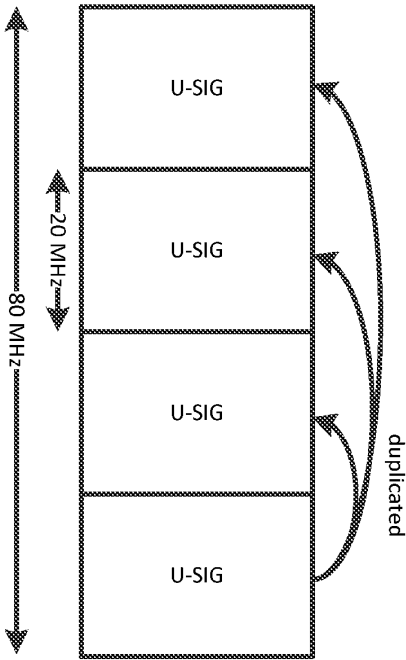


Fig. 5B

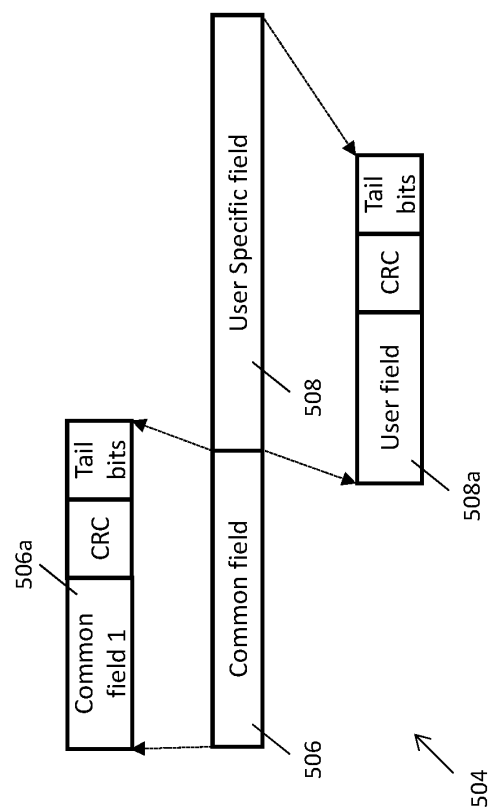


Fig. 5C

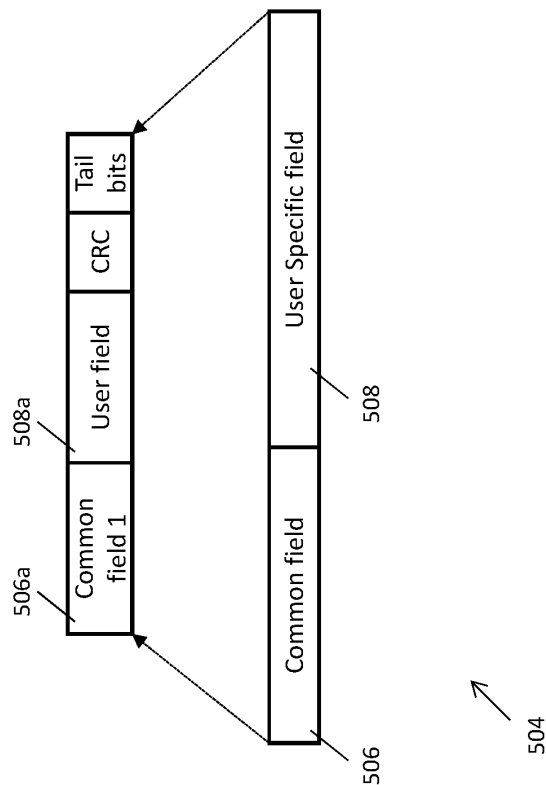


Fig. 5D

CBW (MHz)	<i>L</i>	
	1	2
20	1	N.A.
40	2	1
80	2	2
80+80, 160	2	2
160+160, 320	2	2

Fig. 5E

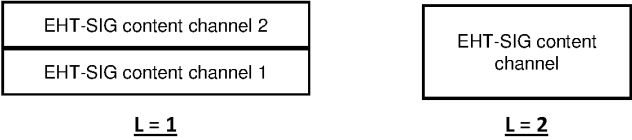


Fig. 5F

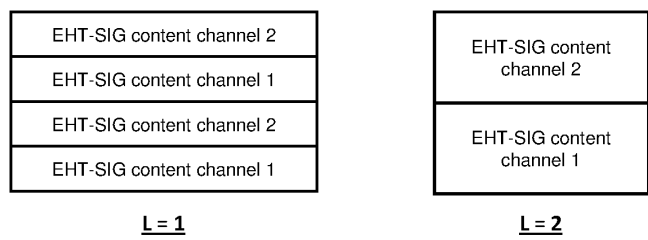


Fig. 5G

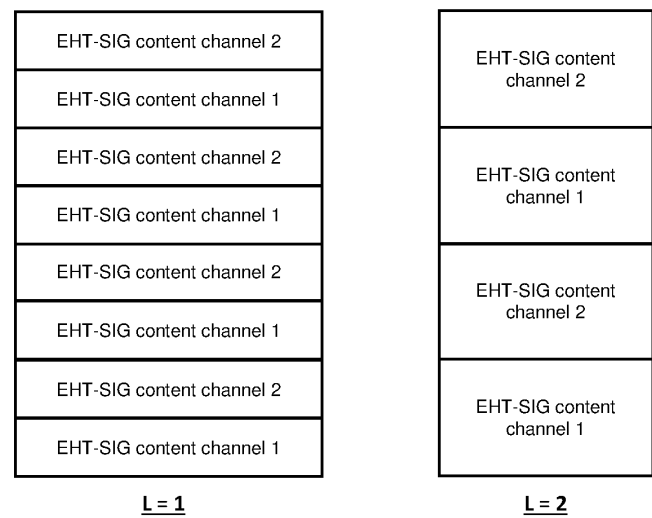


Fig. 5H

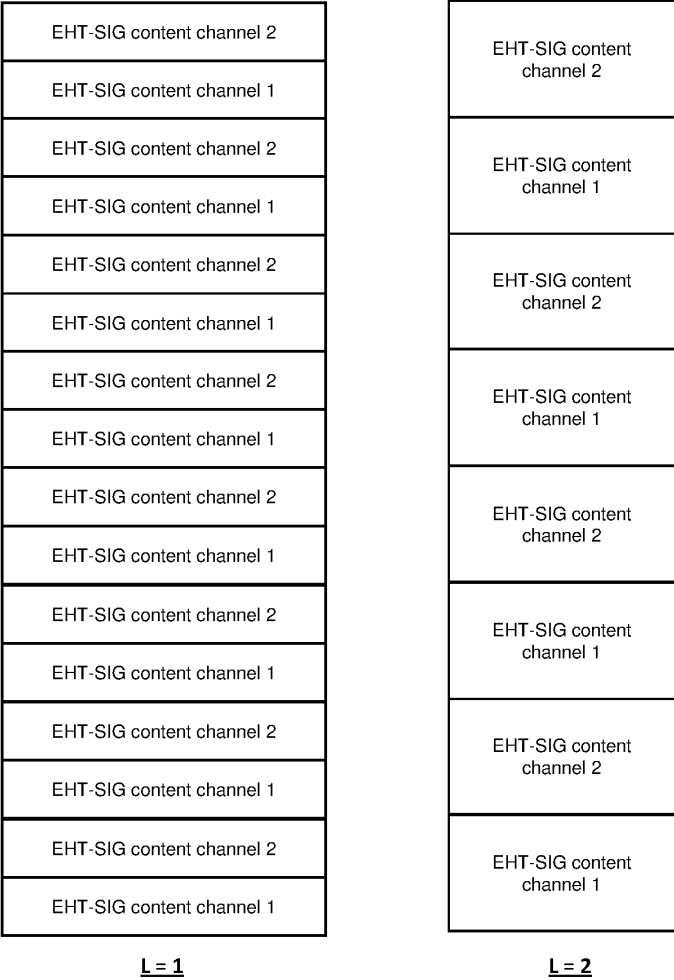


Fig. 5I

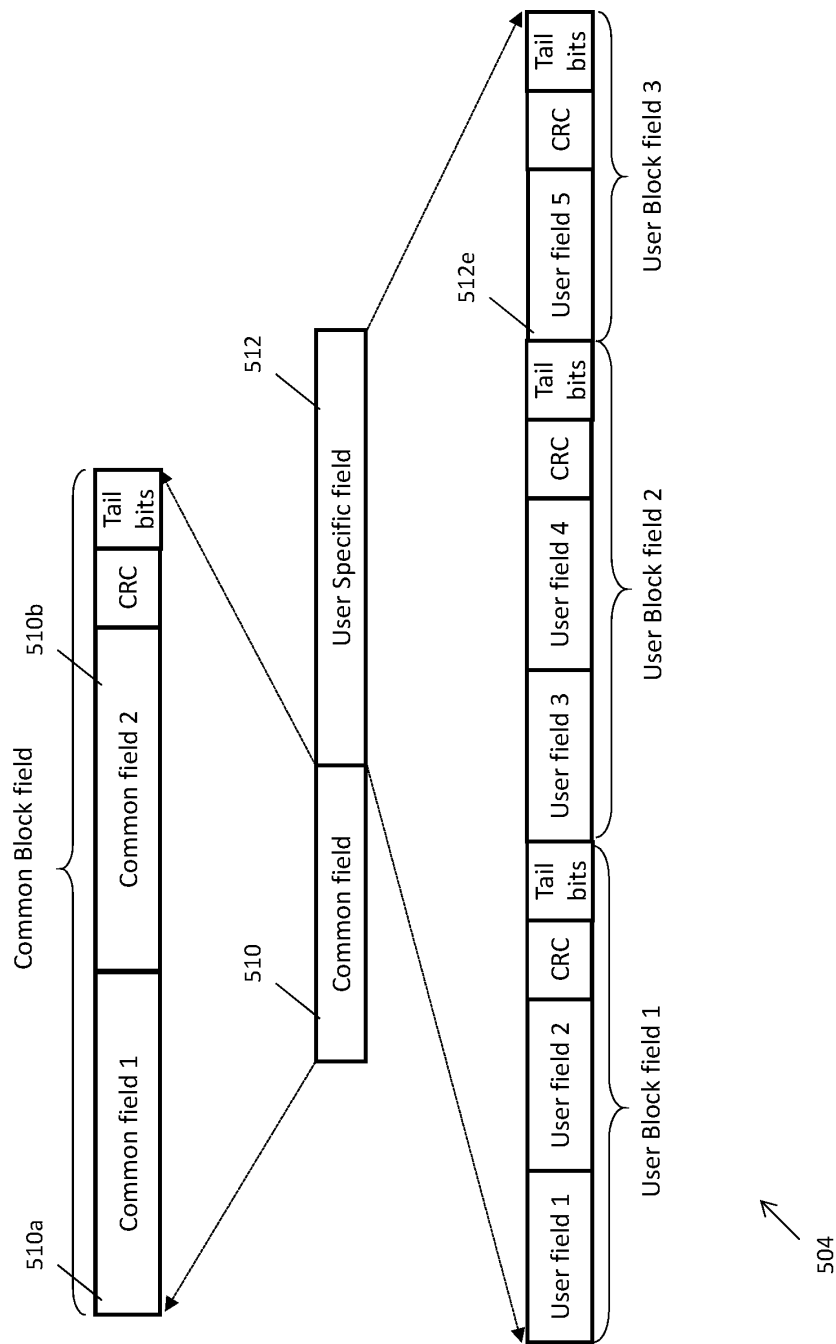


Fig. 5J

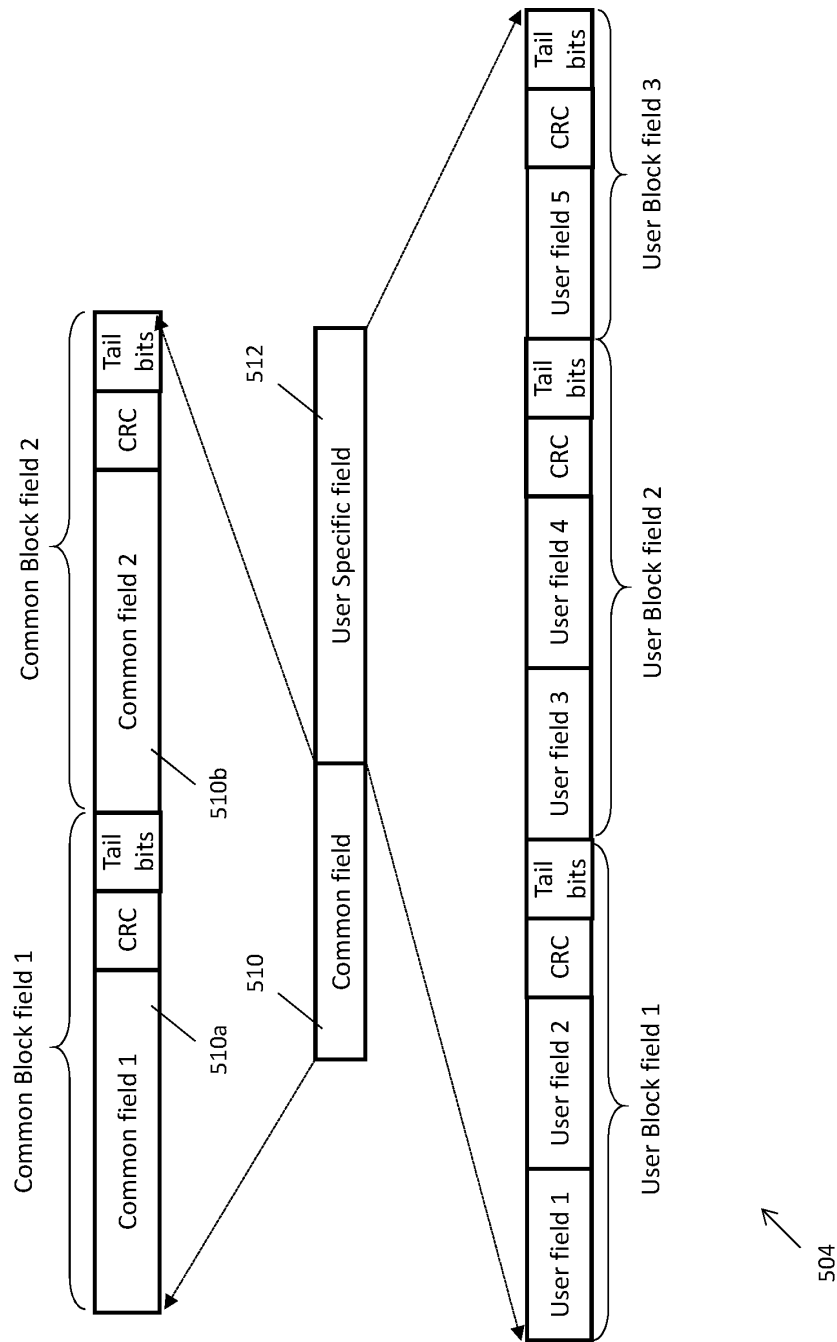


Fig. 5K

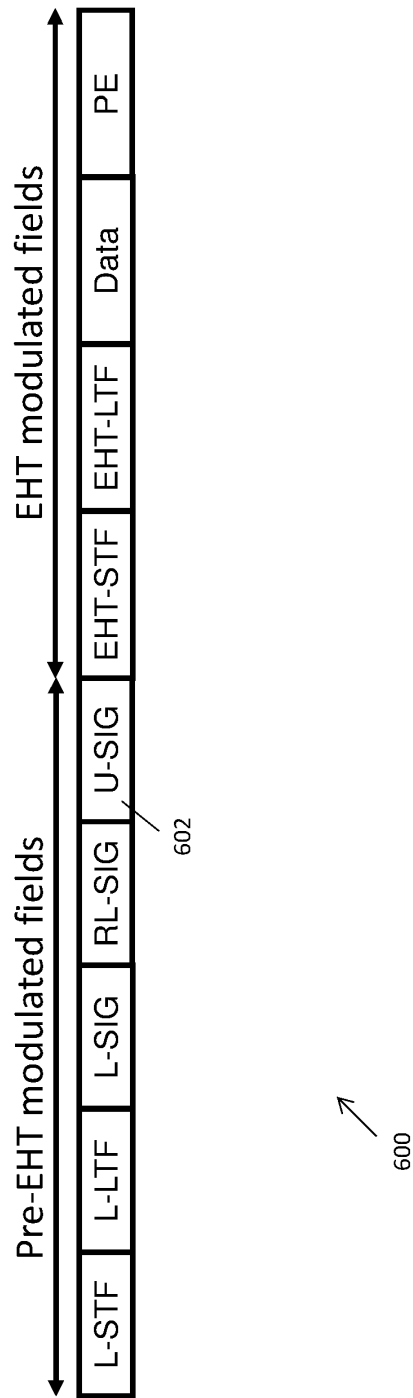


Fig. 6

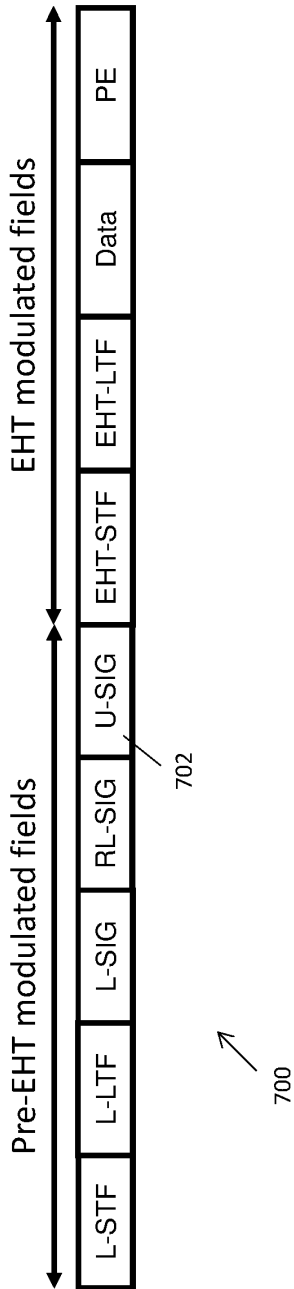


Fig. 7A

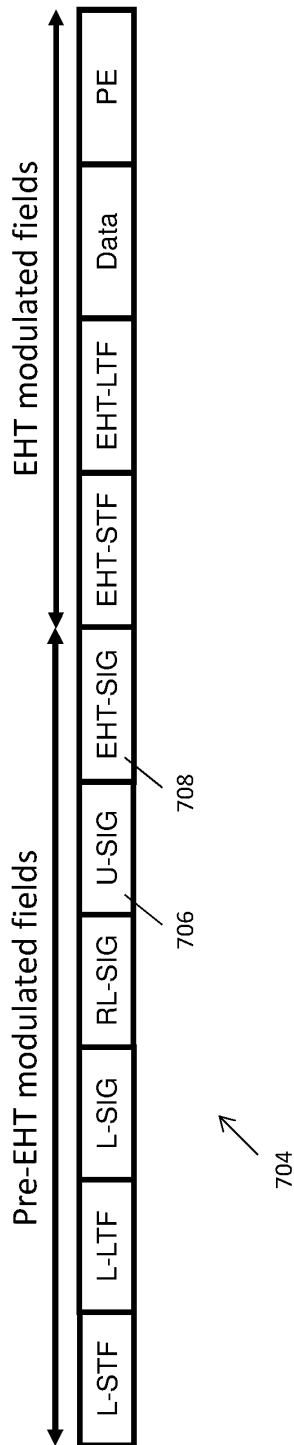


Fig. 7B

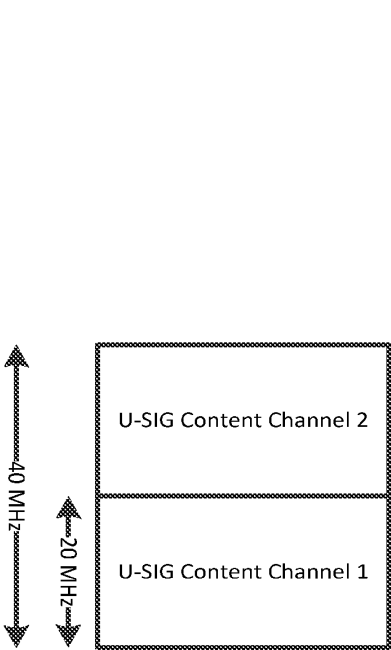


Fig. 7C

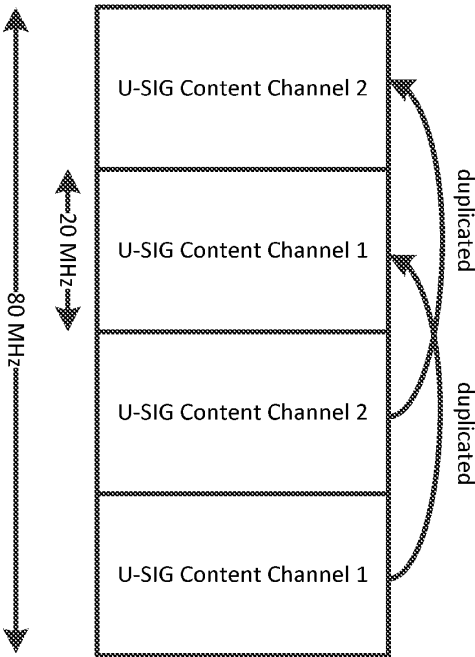


Fig. 7D

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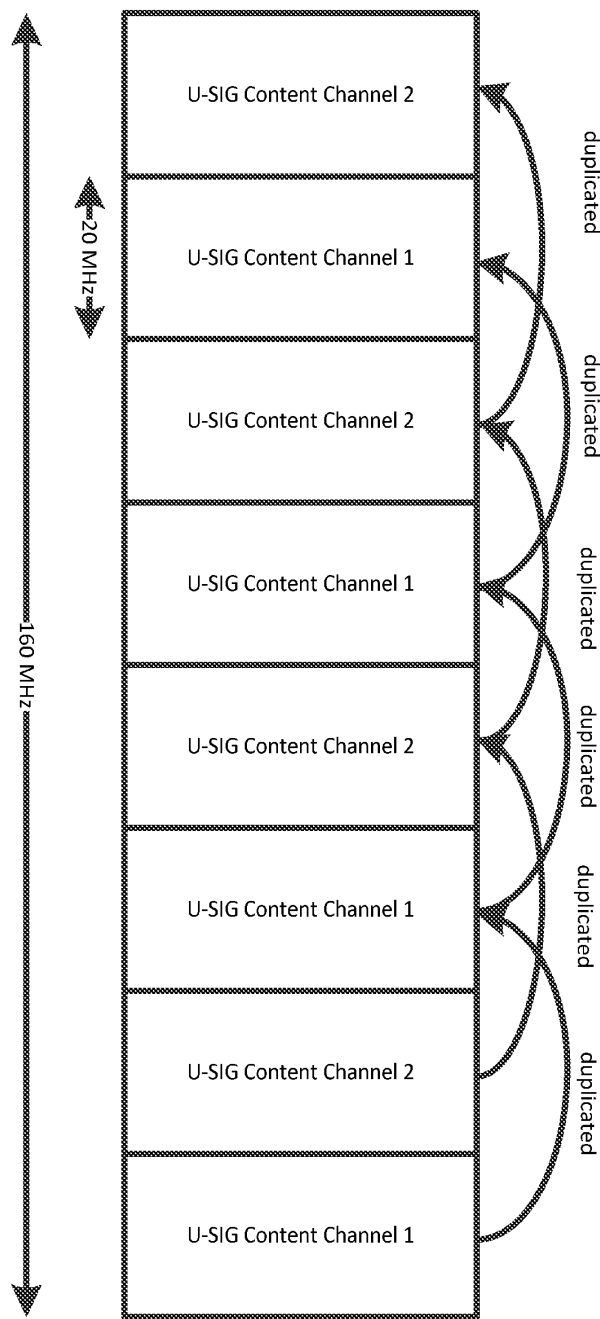


Fig. 7E

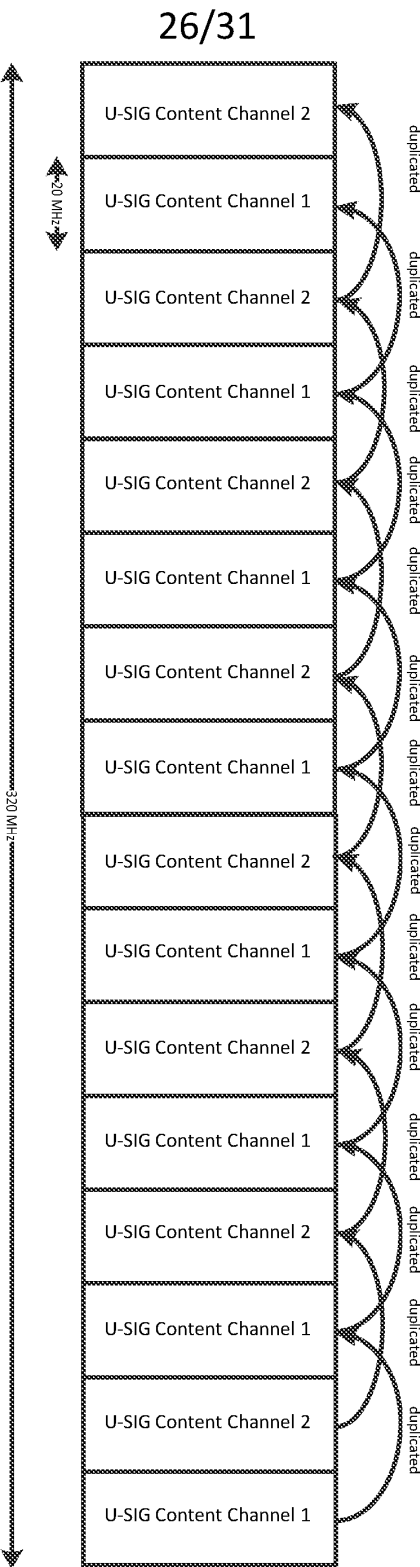
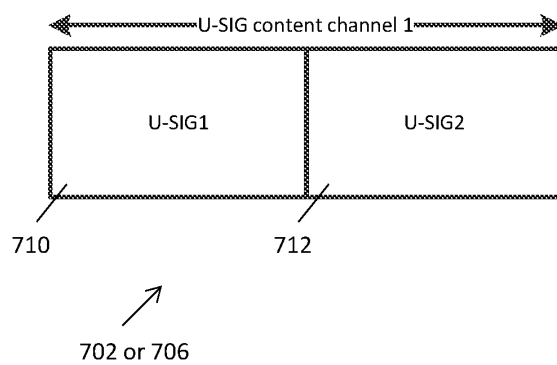
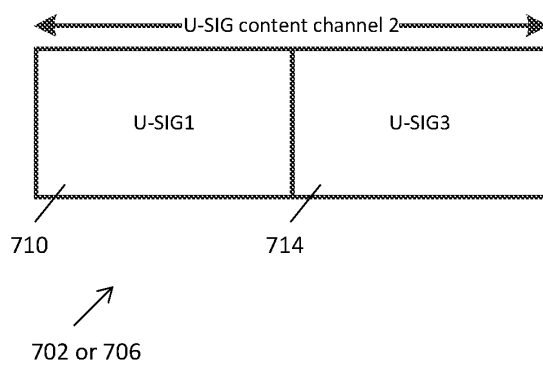


Fig. 7F

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**Fig. 7G****Fig. 7H**

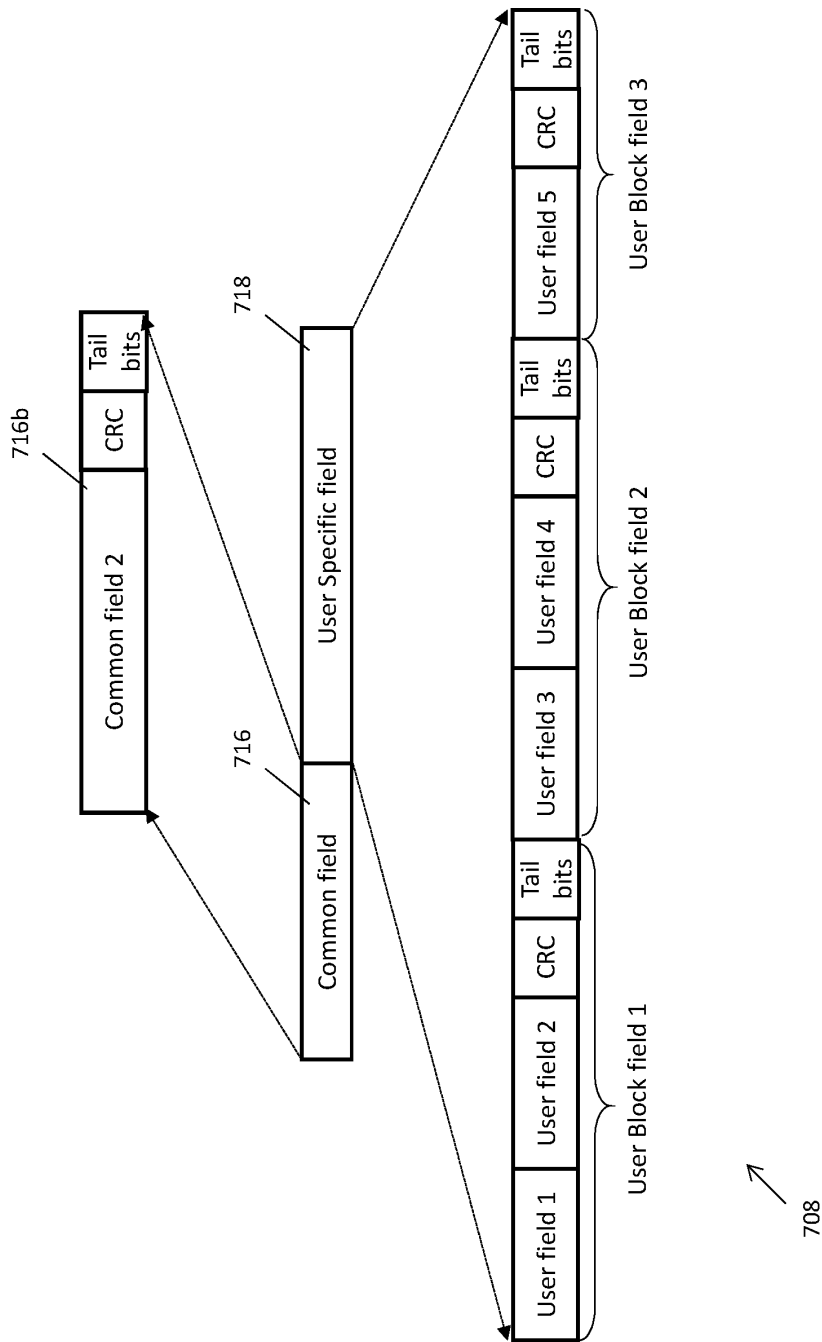


Fig. 7I

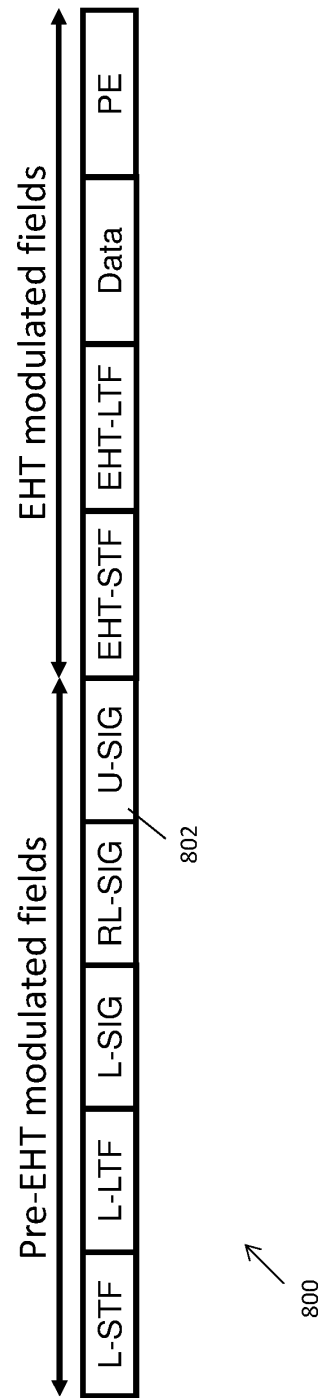
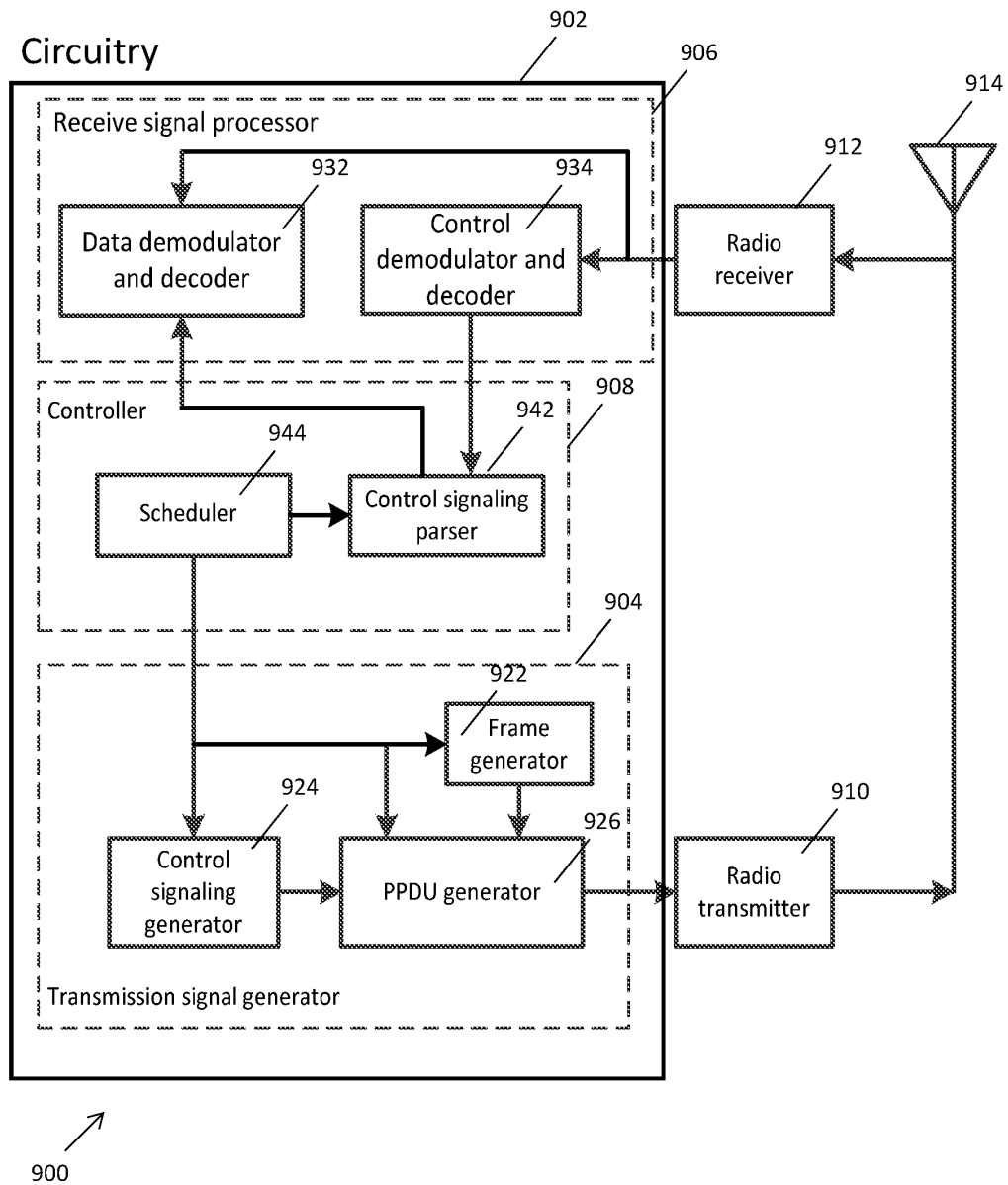
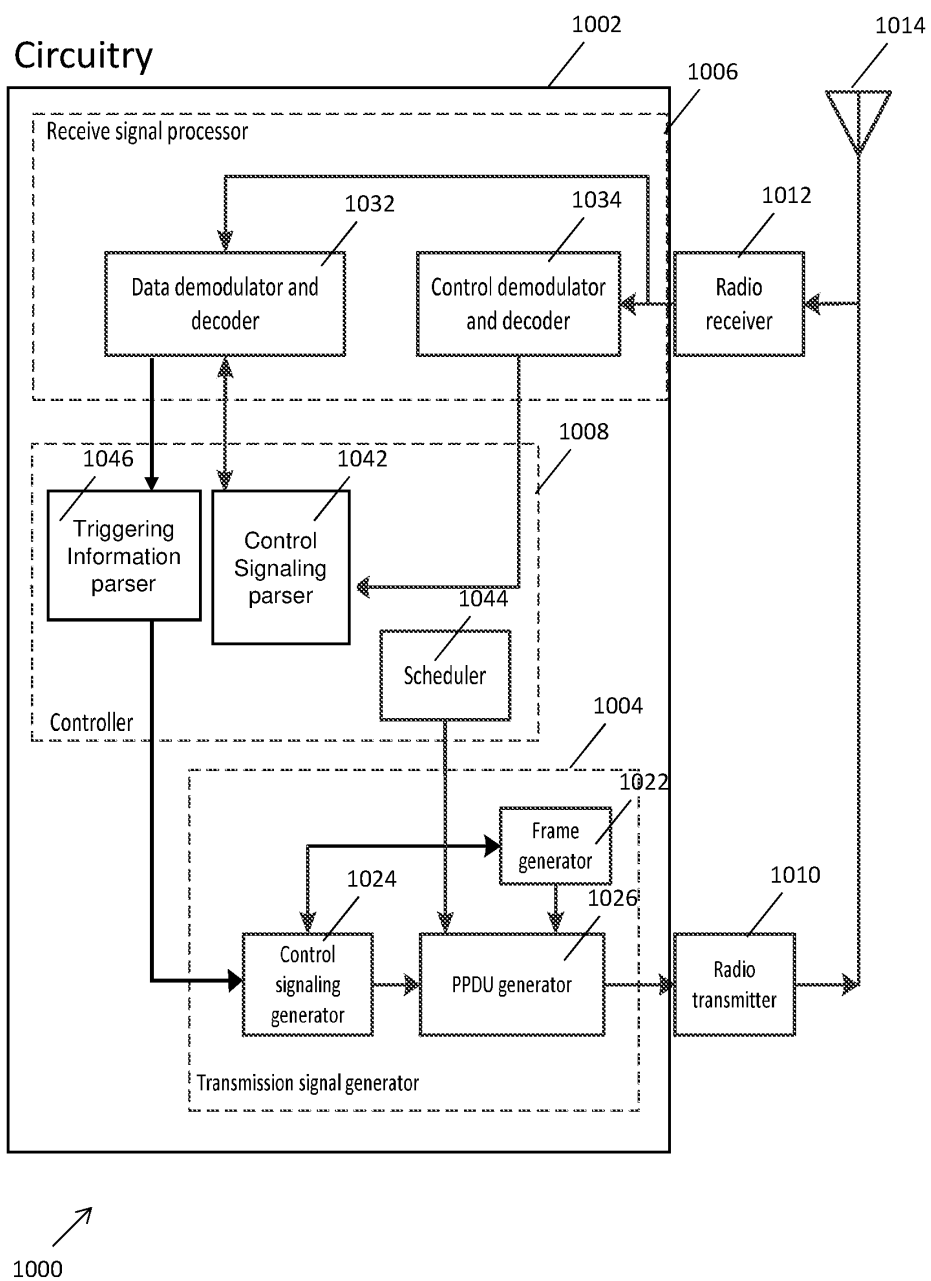


Fig. 8

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**Fig. 9**

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**Fig. 10**