

1/44

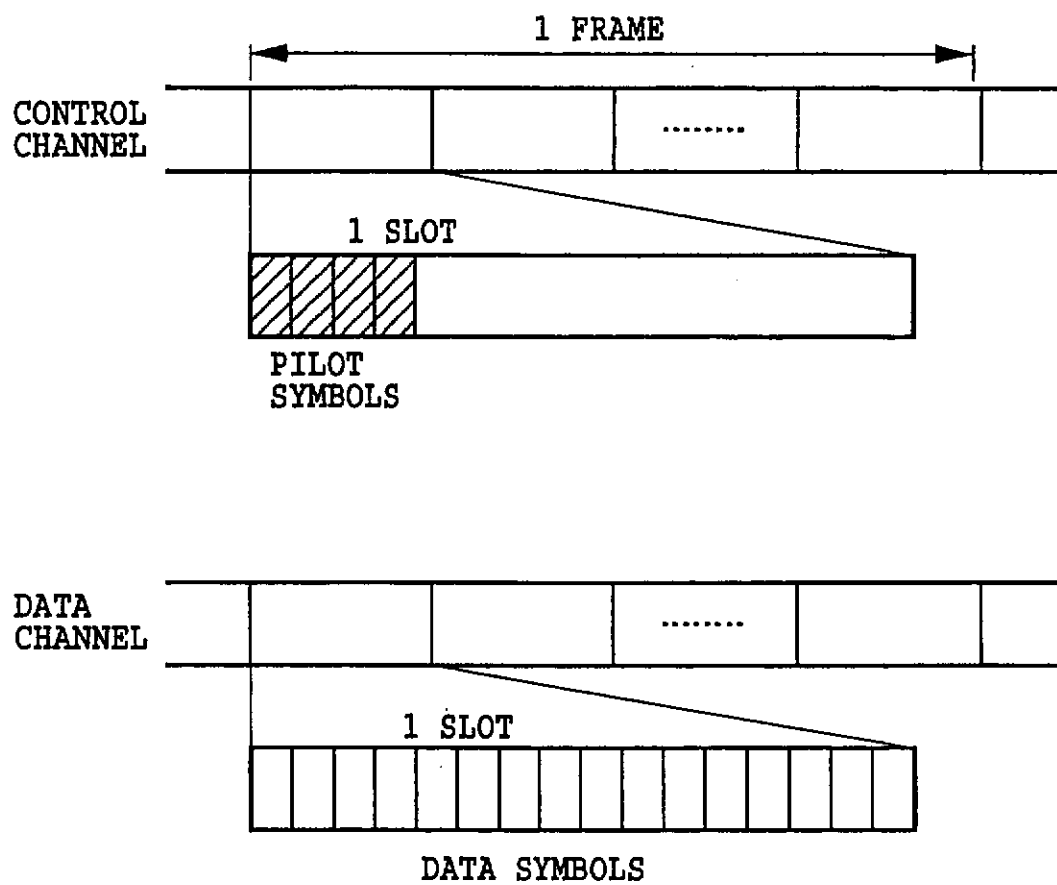


FIG.1

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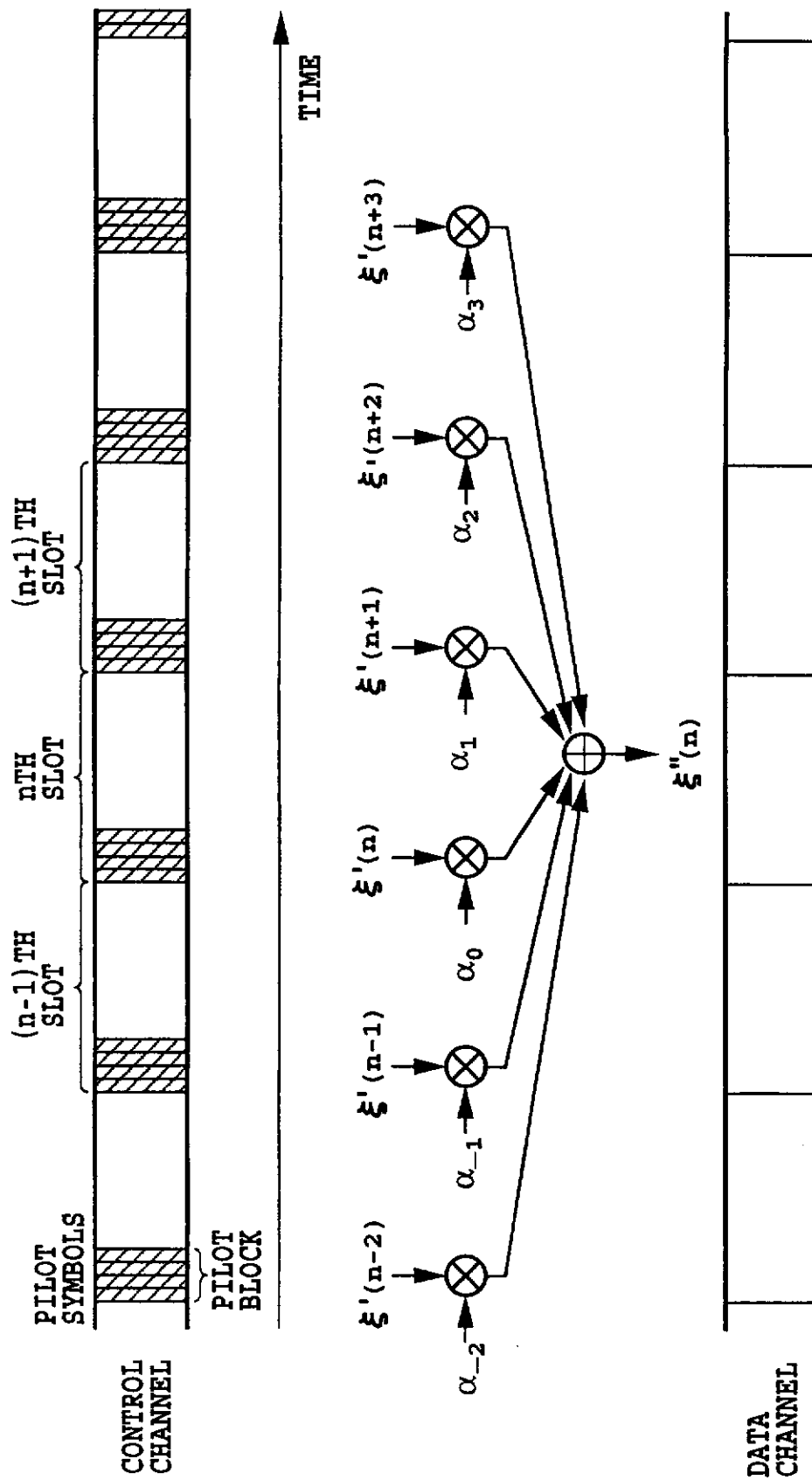


FIG.2

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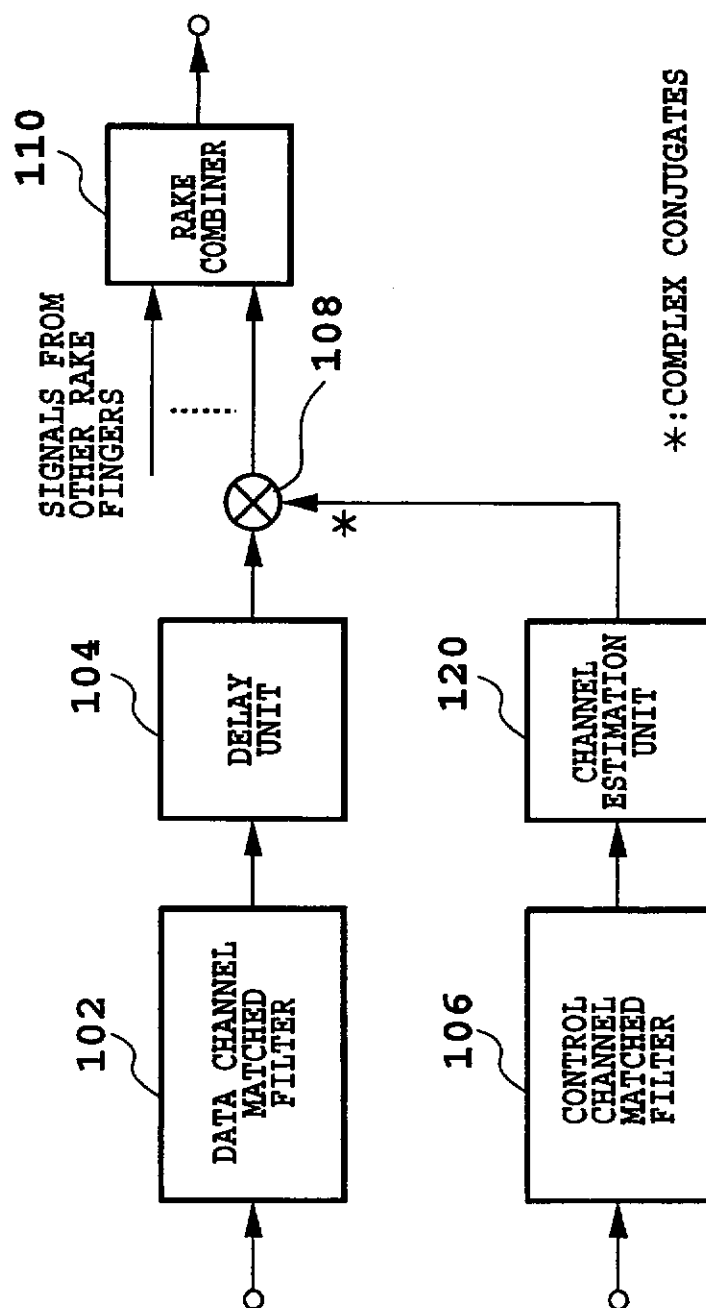


FIG. 3

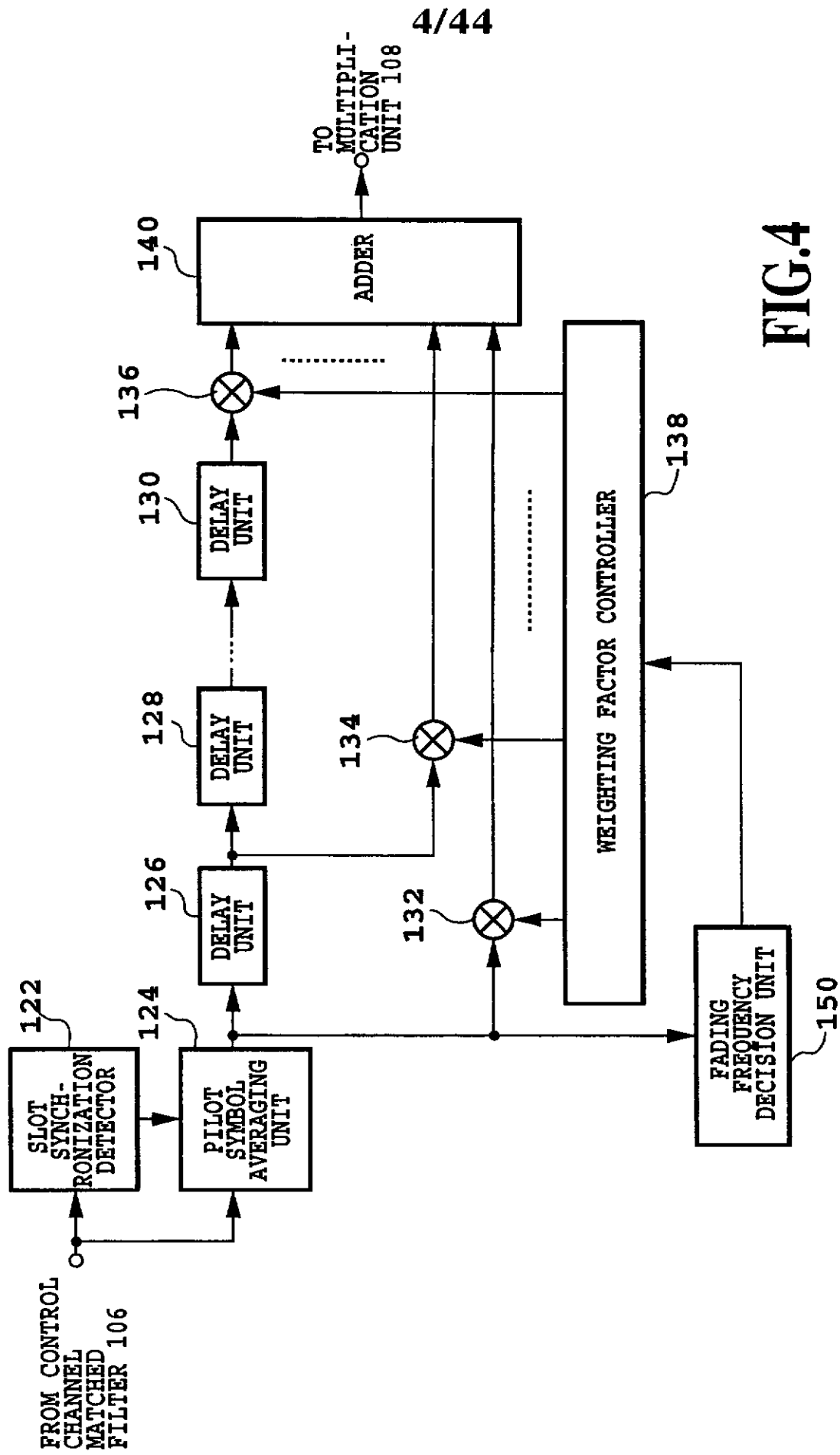


FIG.4

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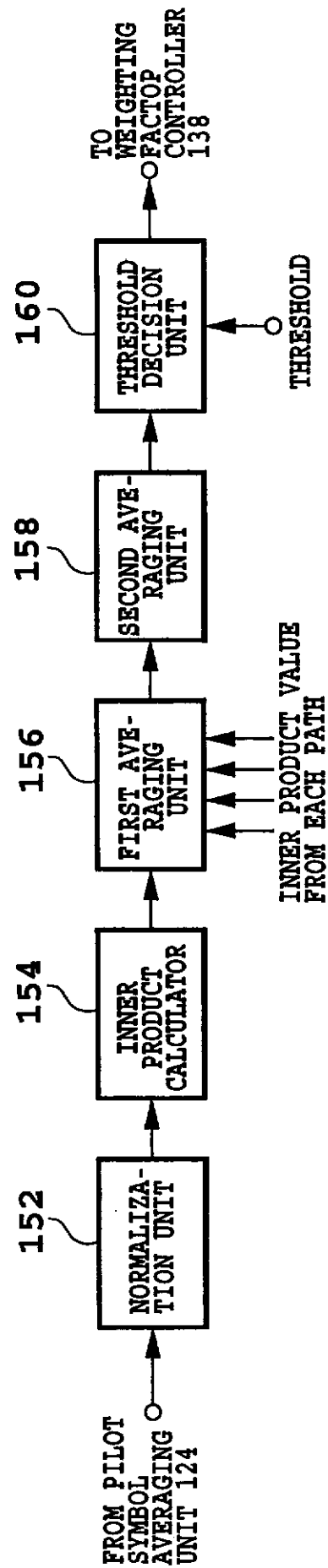


FIG. 5

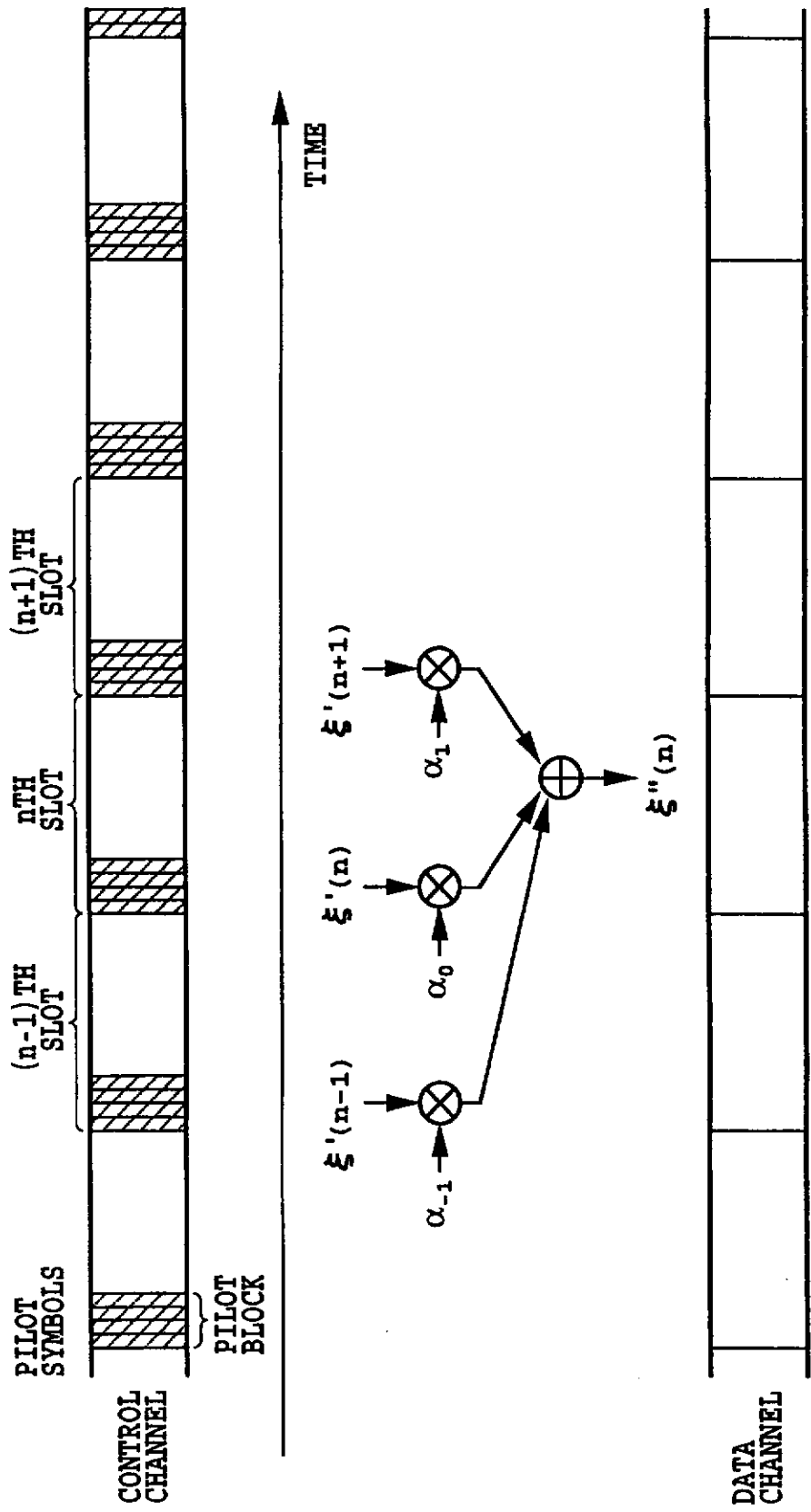


FIG.6

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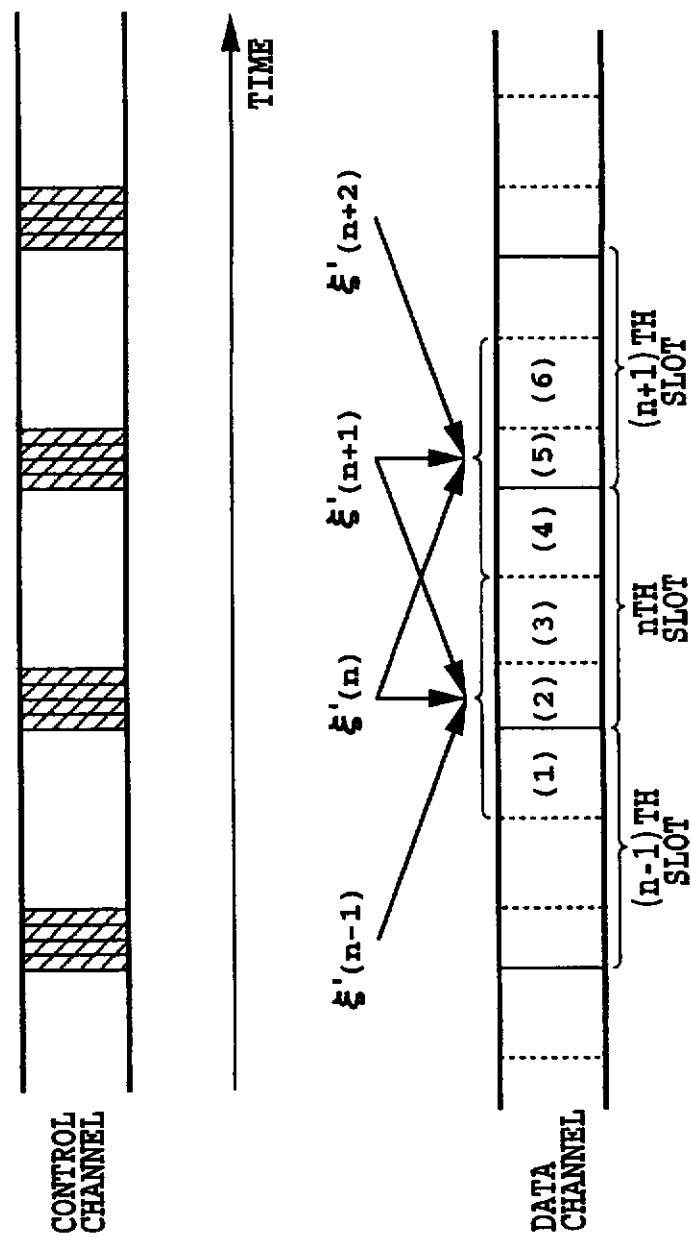


FIG.7

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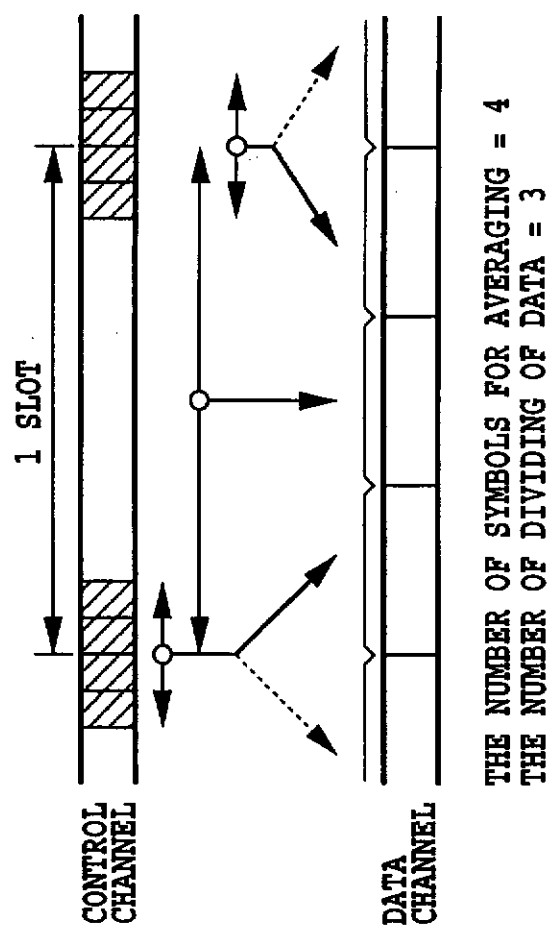


FIG.8

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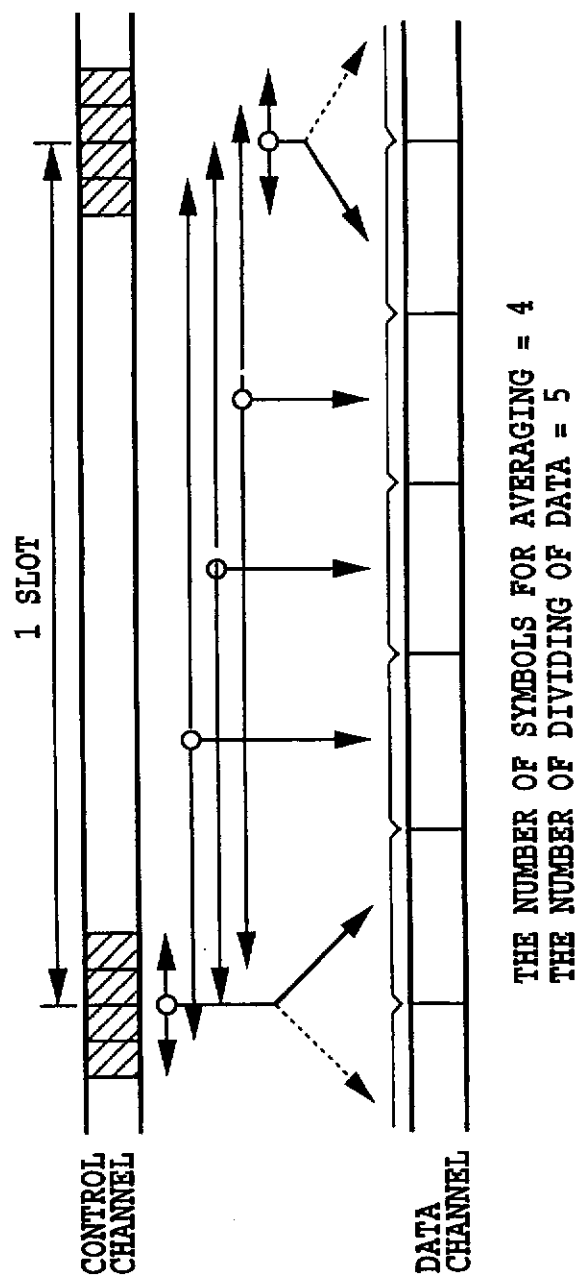


FIG.9

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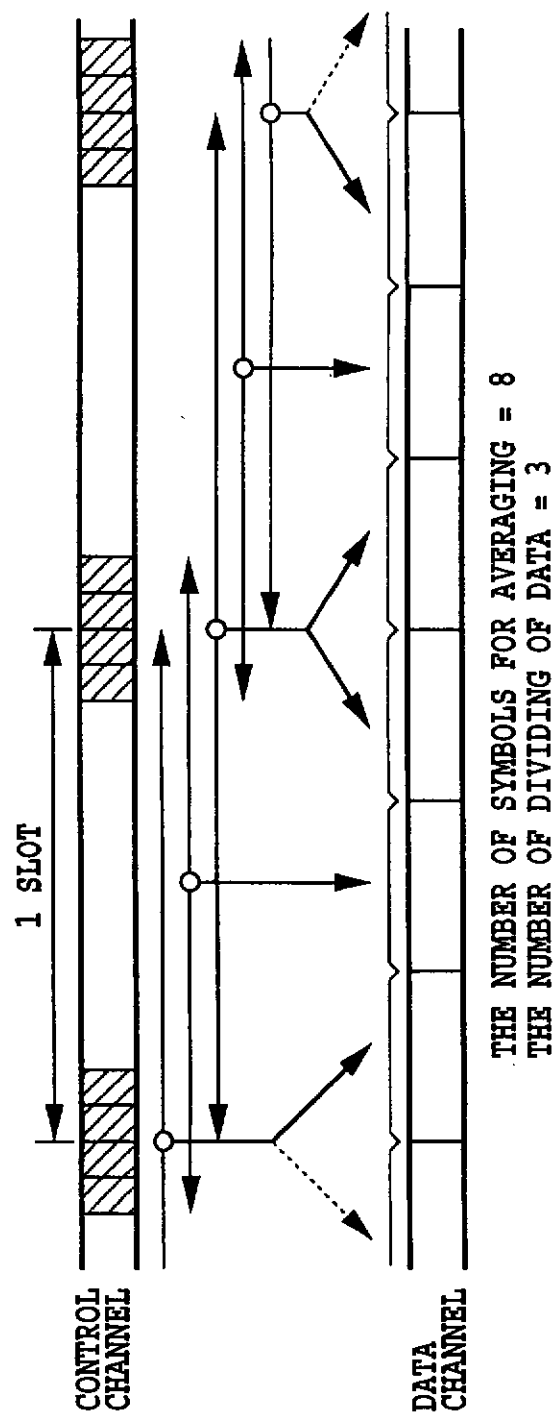


FIG.10

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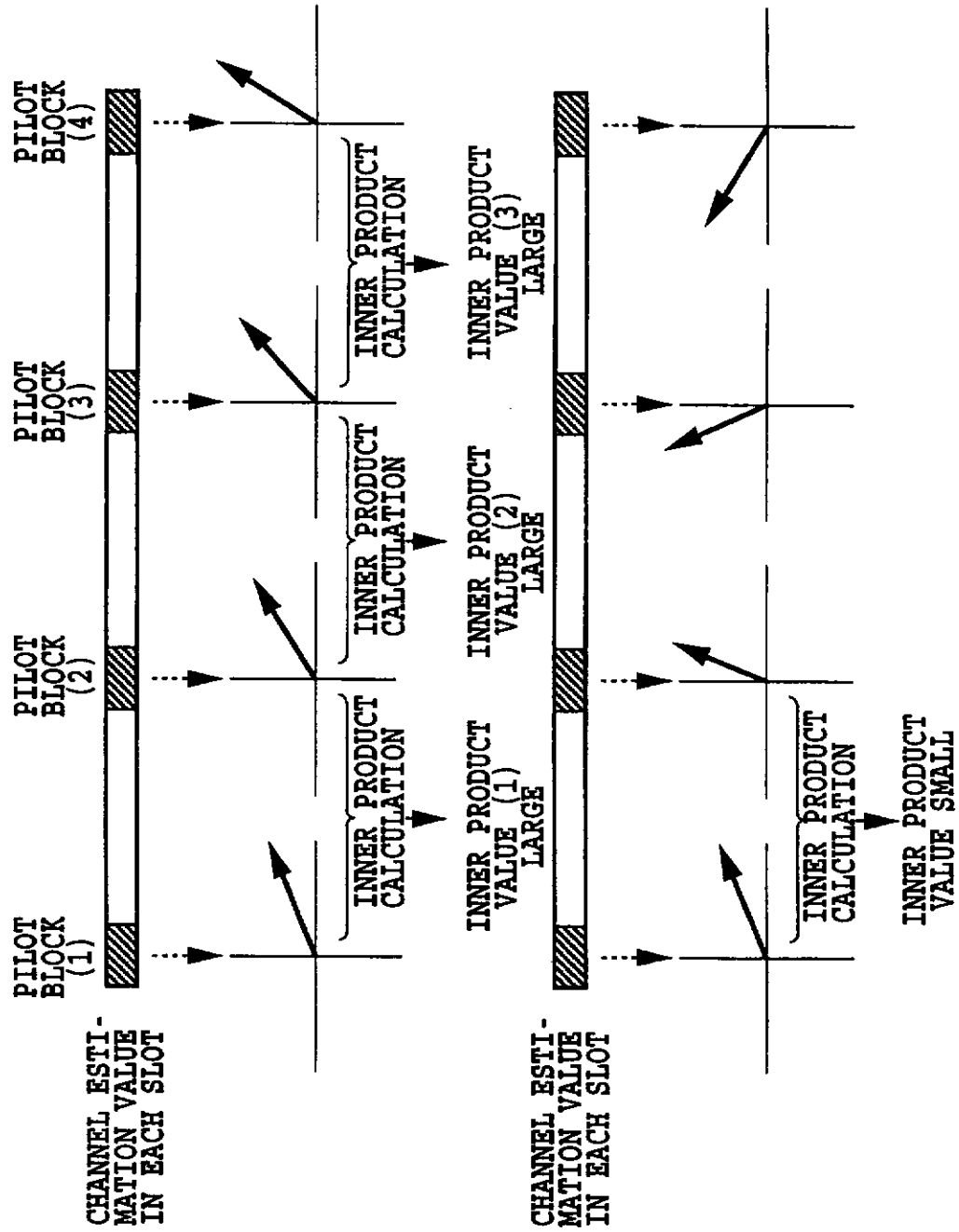
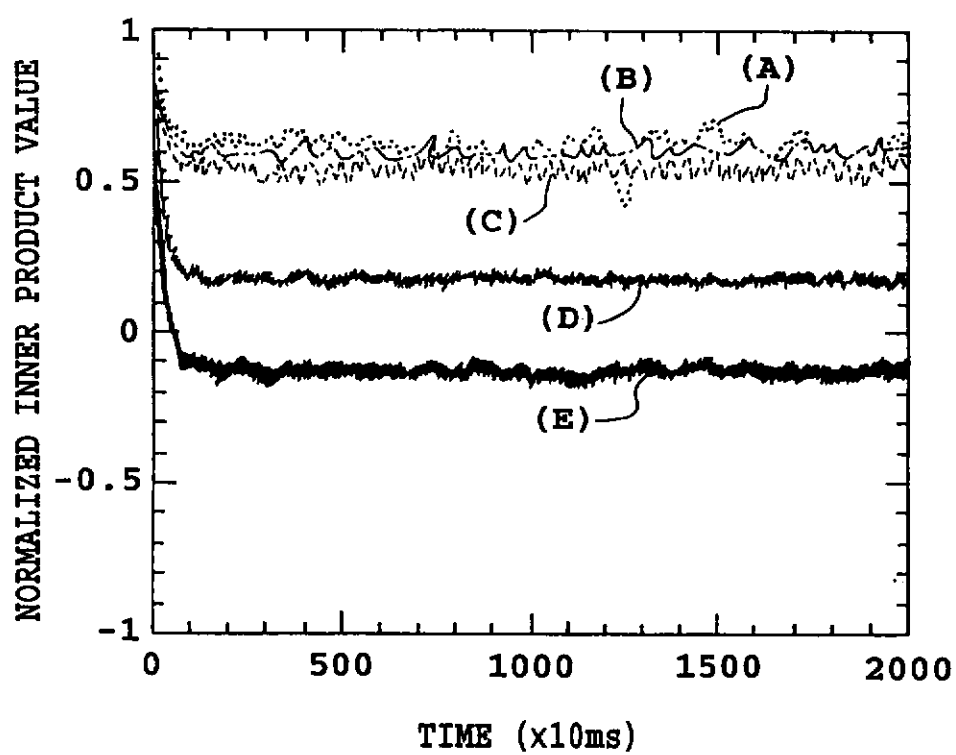


FIG.11A

FIG.11B

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- (A) ..... fD Tslot=0.001
- (B) -.-.-.- fD Tslot=0.01
- (C) ..... fD Tslot=0.1
- (D) ——— fD Tslot=0.3
- (E) ——— fD Tslot=0.5

FIG.12

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FIG.13

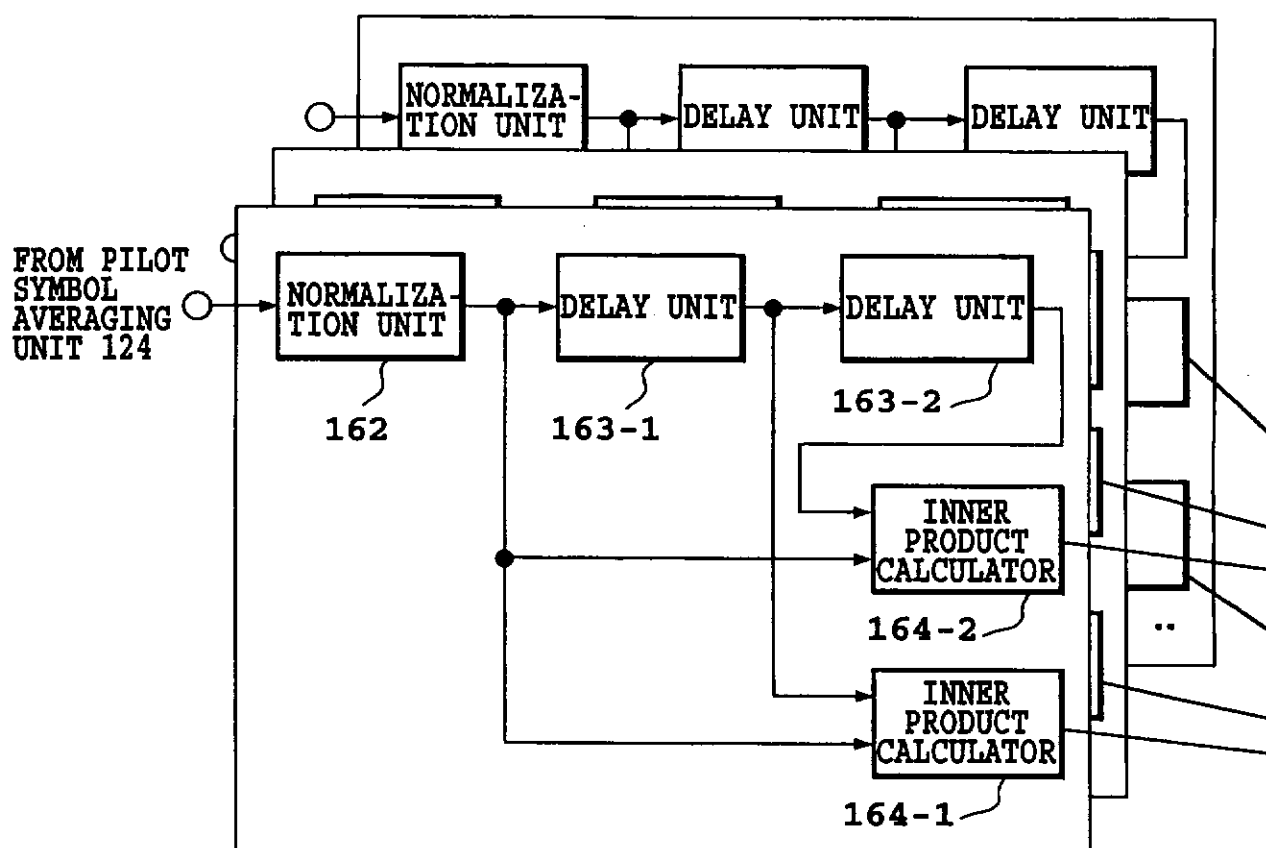
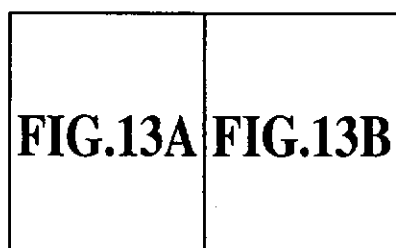


FIG.13A

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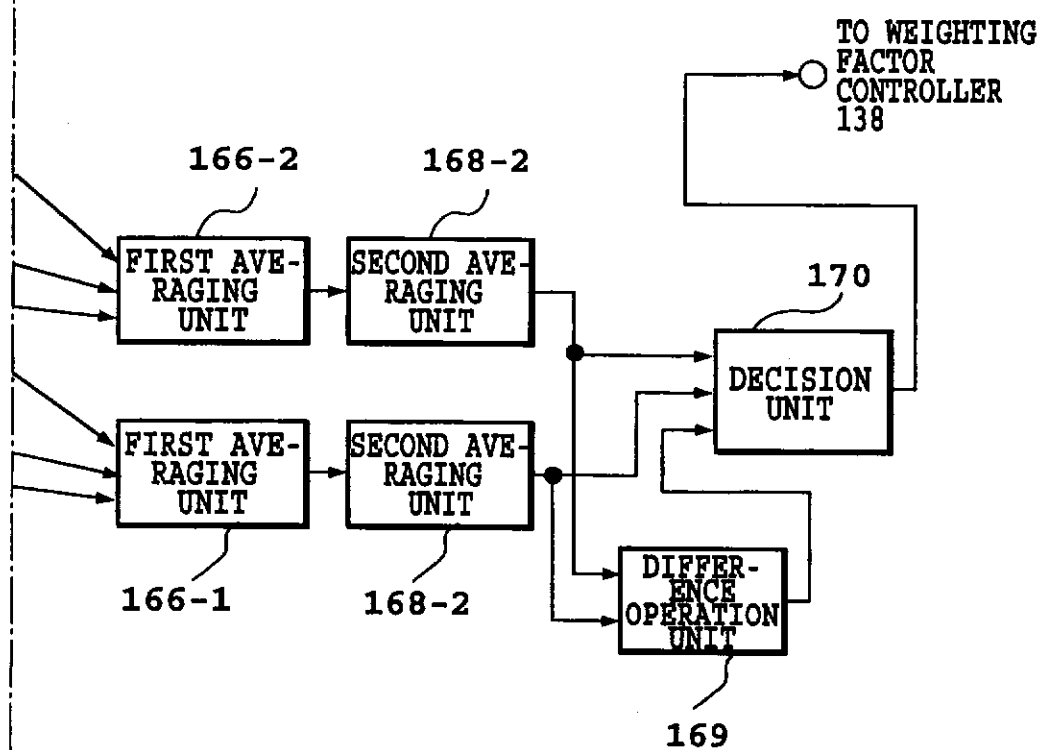


FIG.13B

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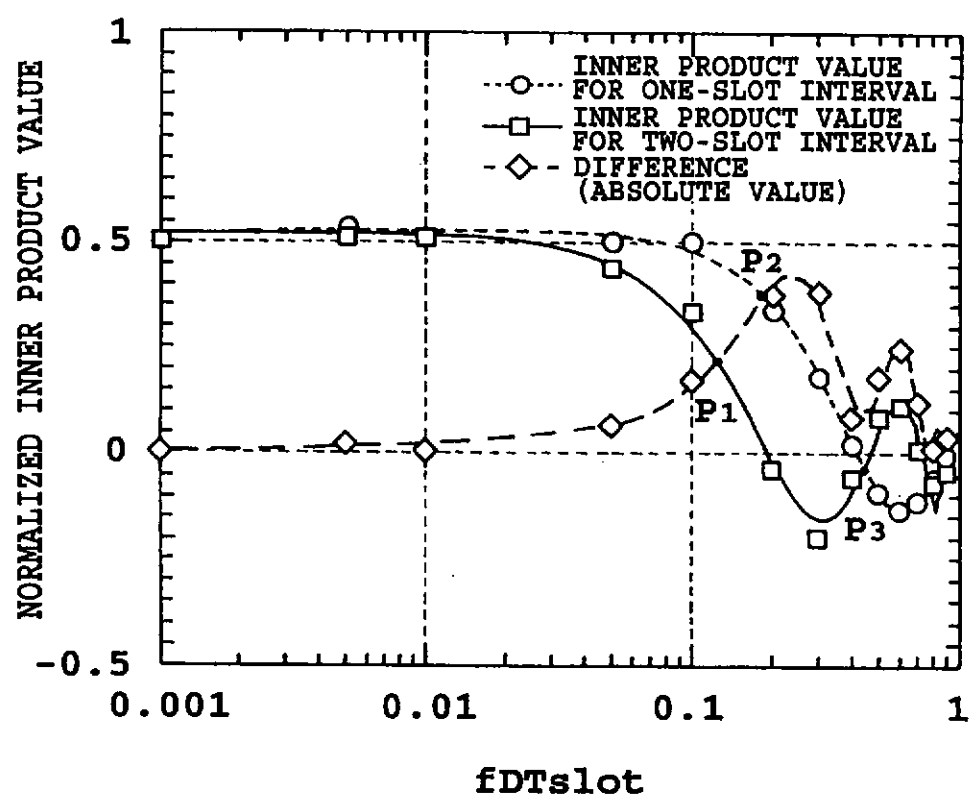


FIG.14

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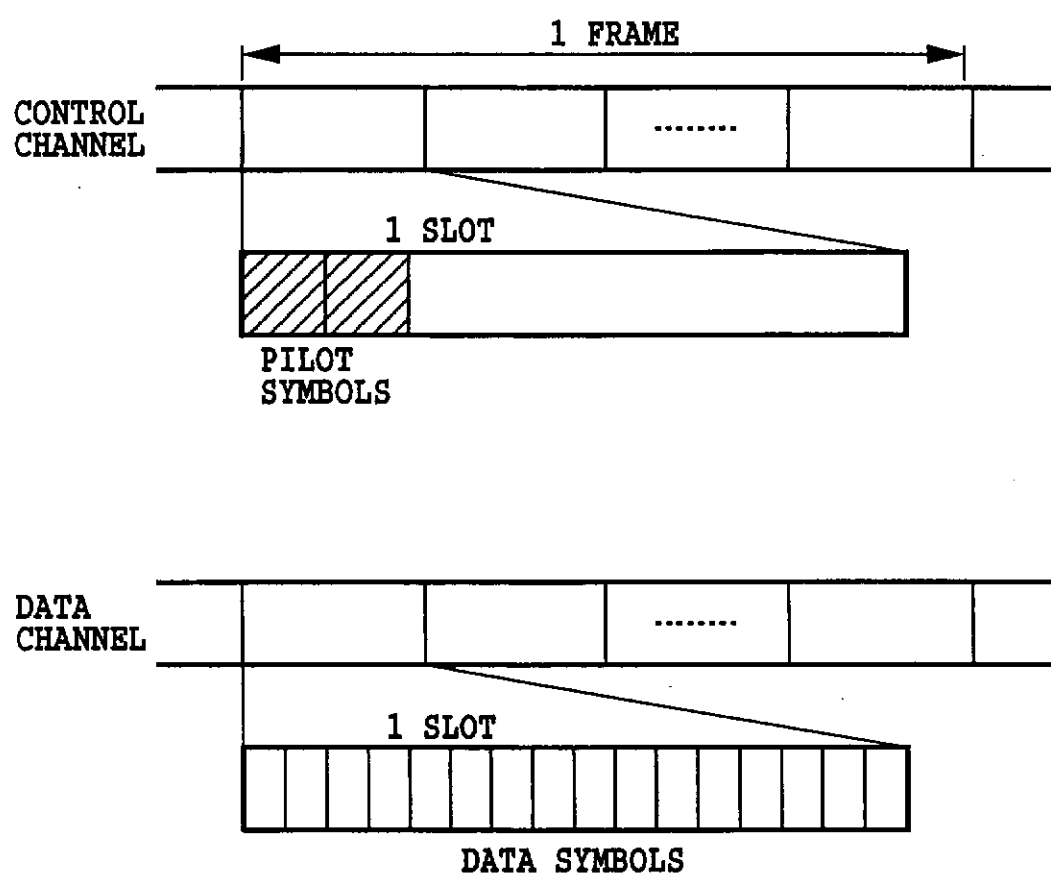


FIG.15

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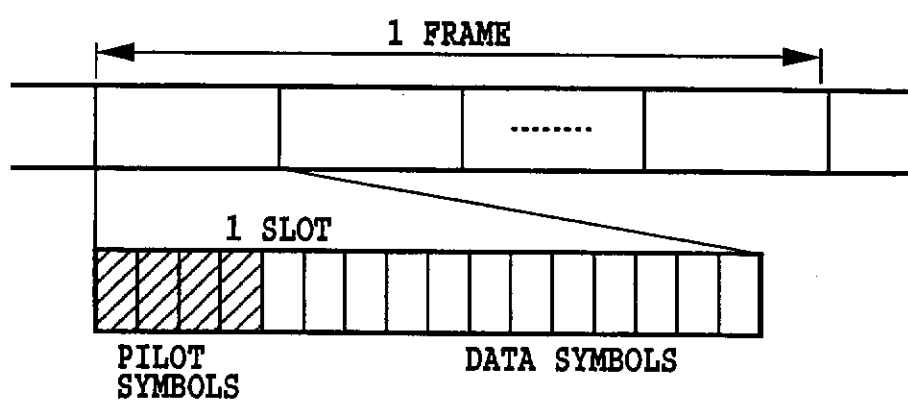


FIG.16

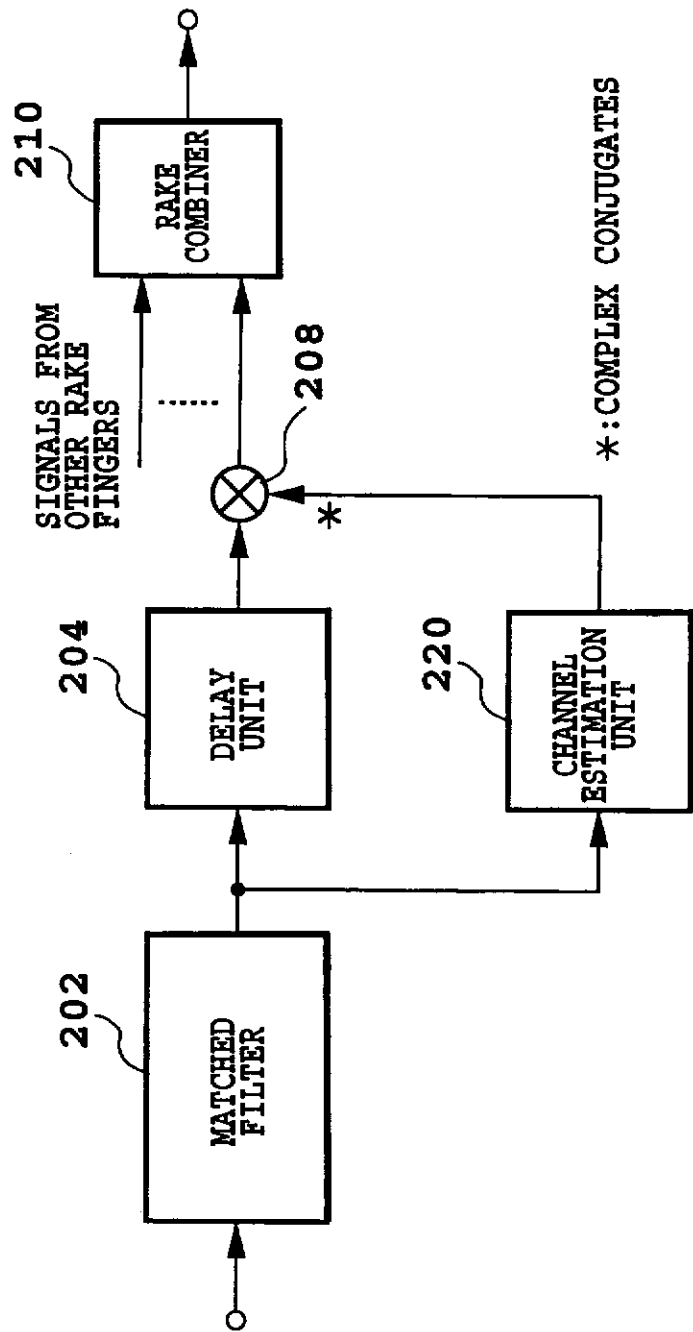


FIG.17

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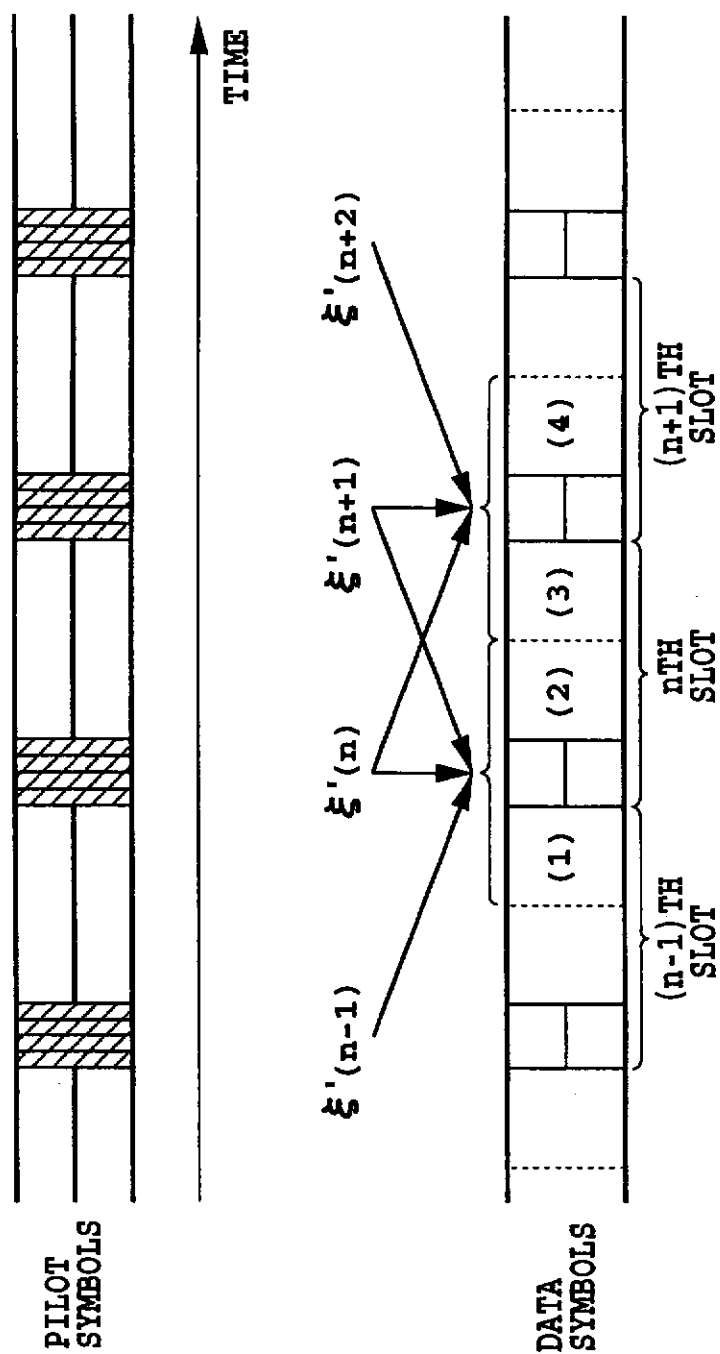


FIG.18

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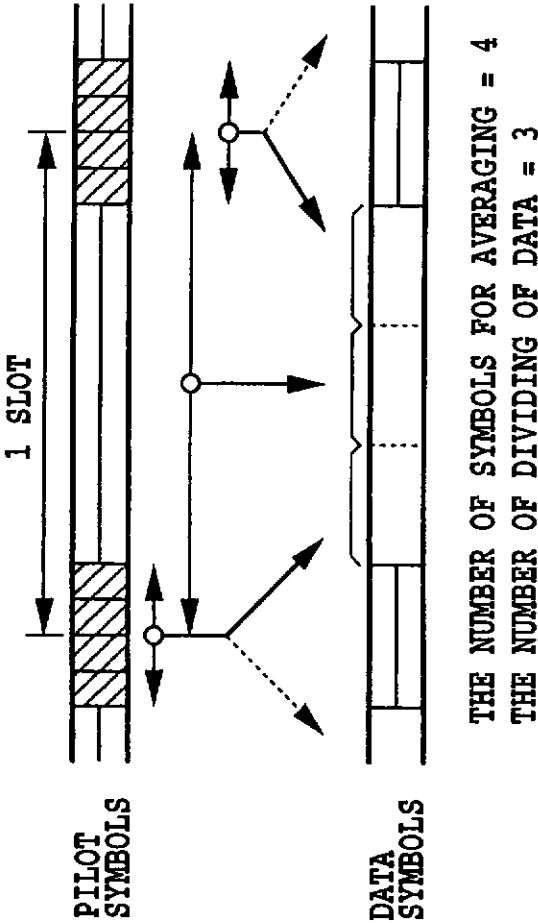


FIG.19

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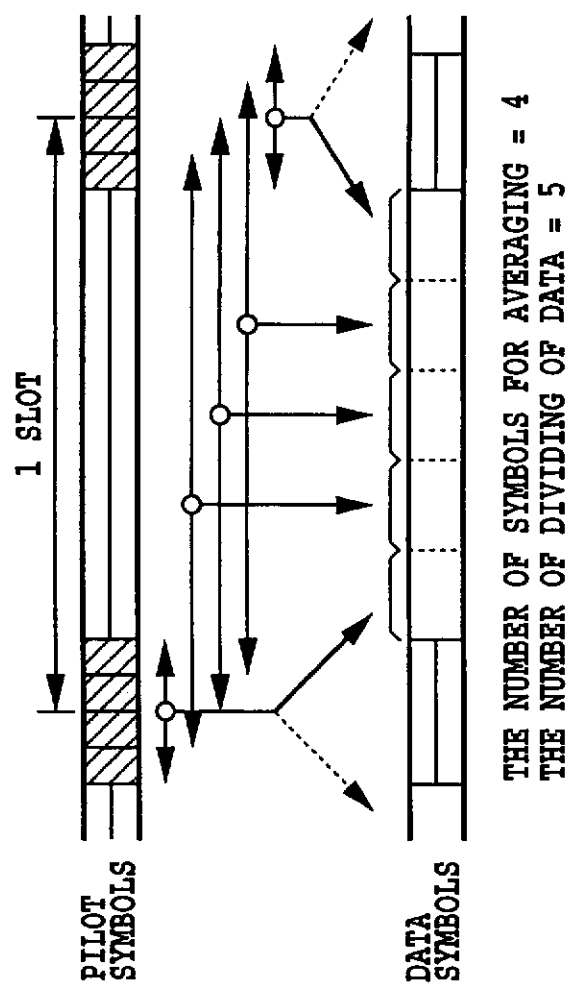


FIG.20

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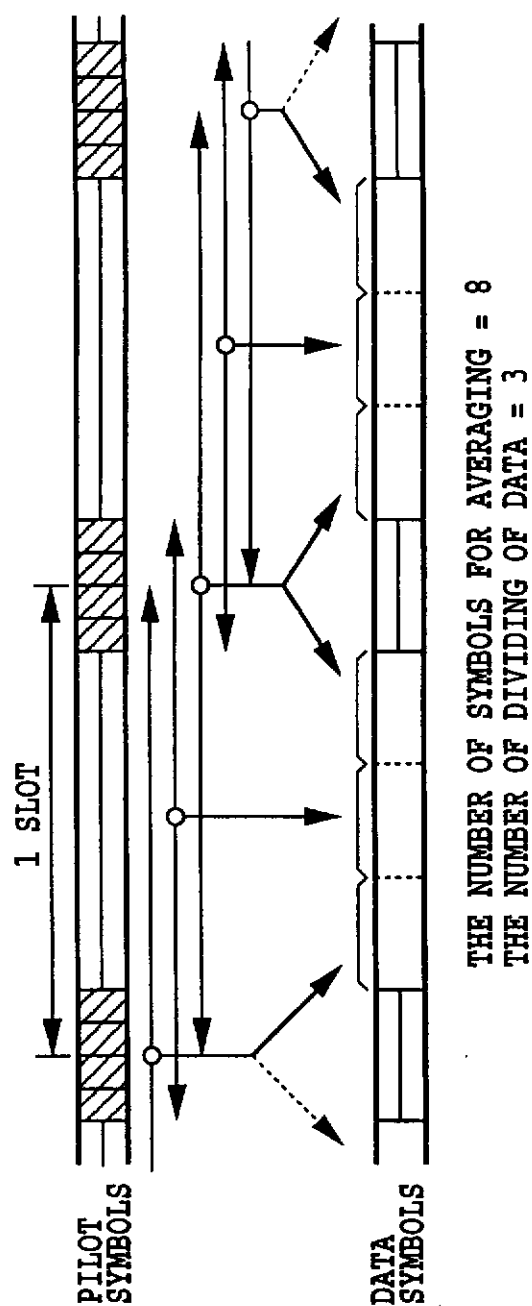


FIG.21

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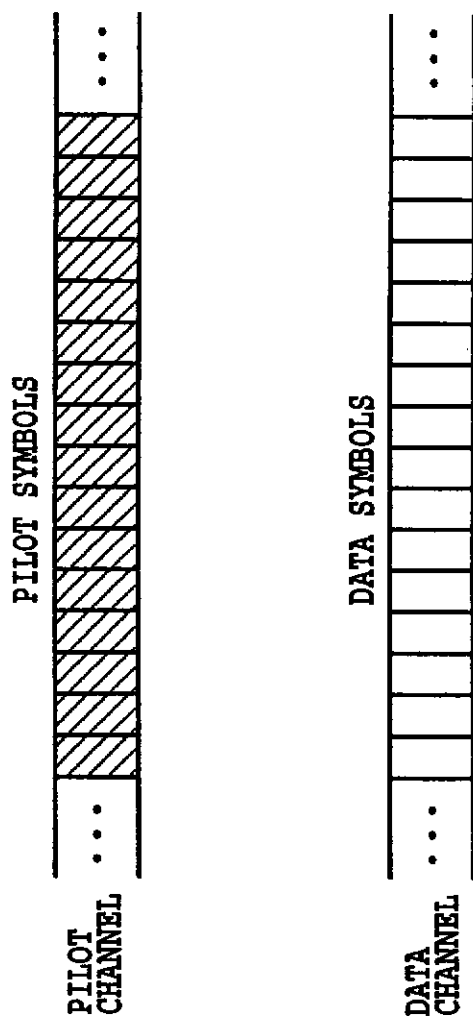


FIG.22

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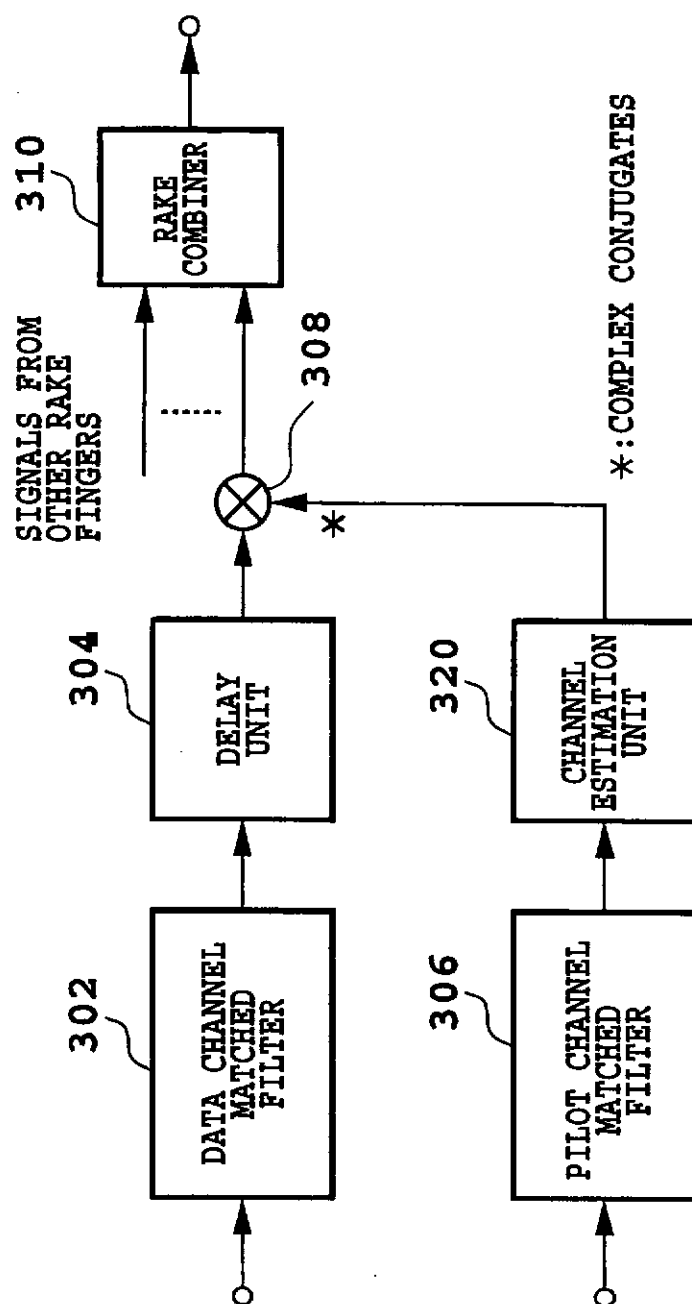


FIG. 23

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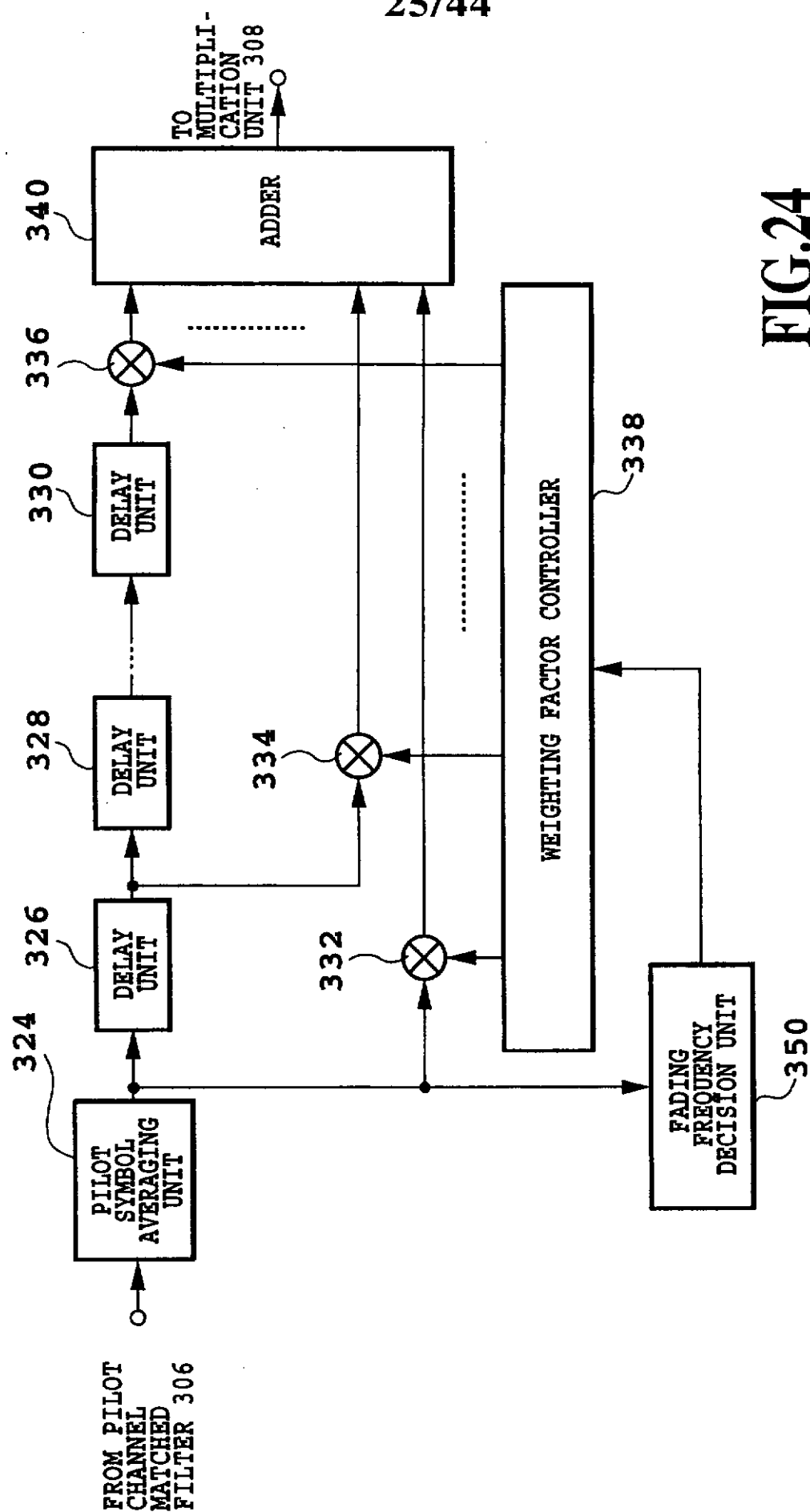


FIG.24

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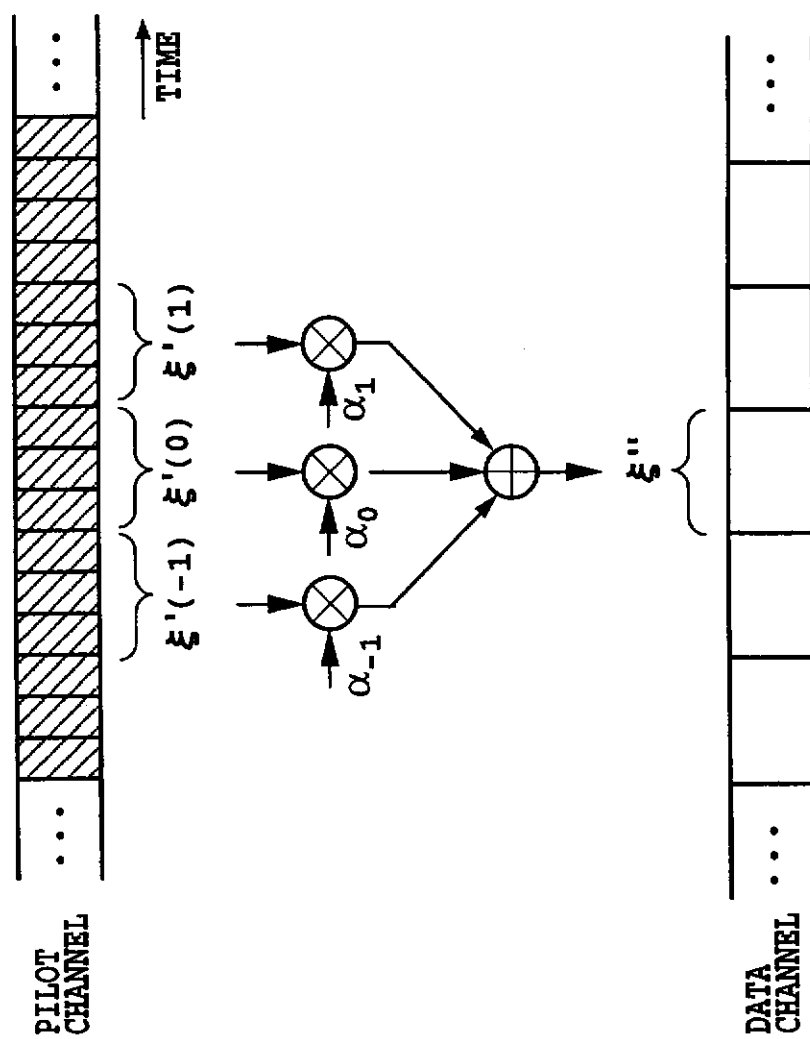


FIG.25

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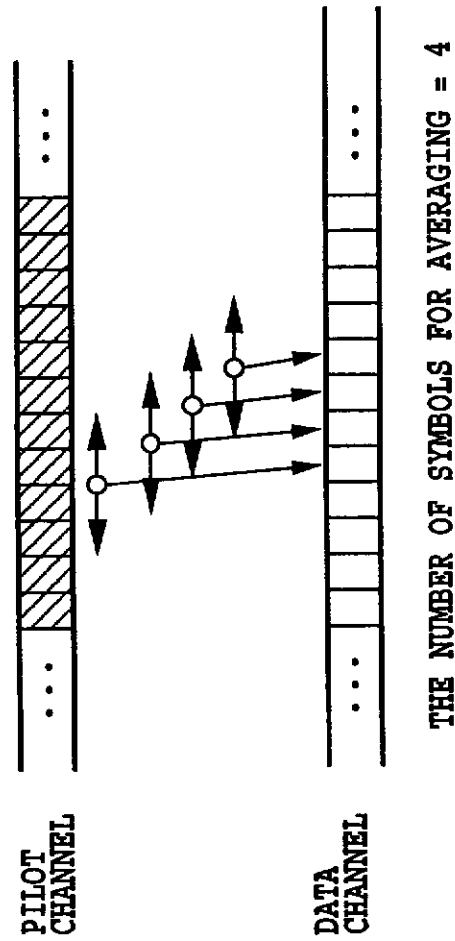


FIG.26

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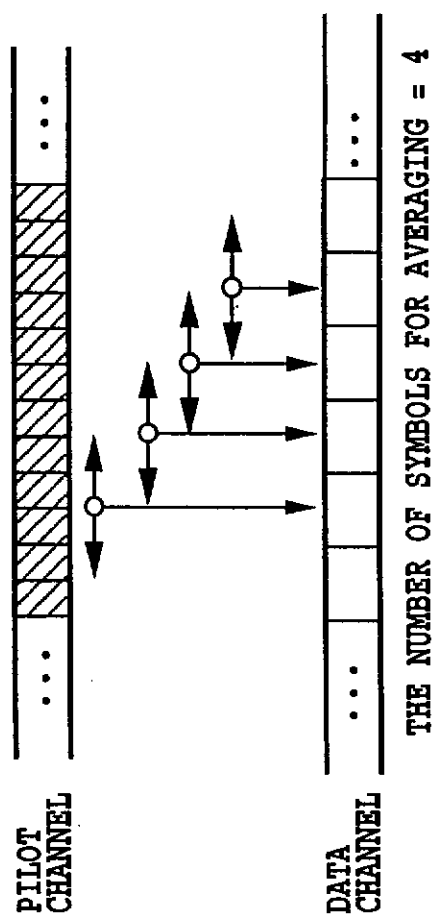


FIG.27

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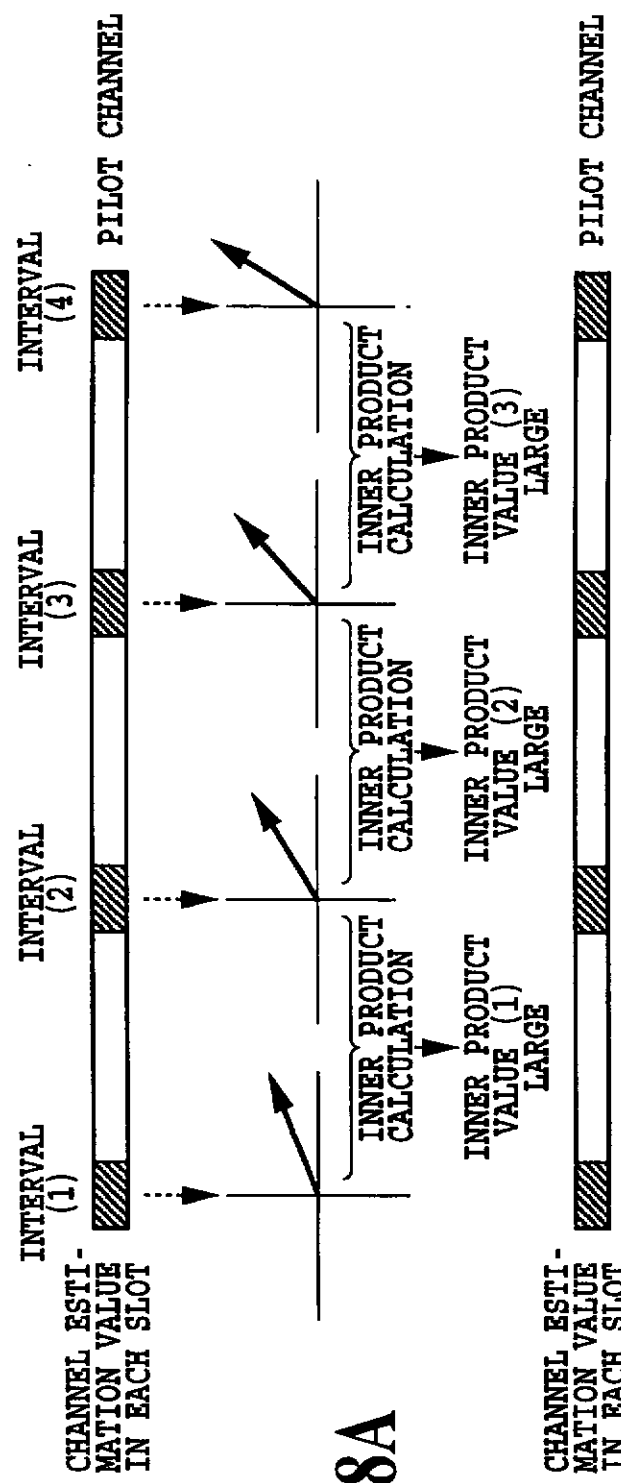


FIG. 28A

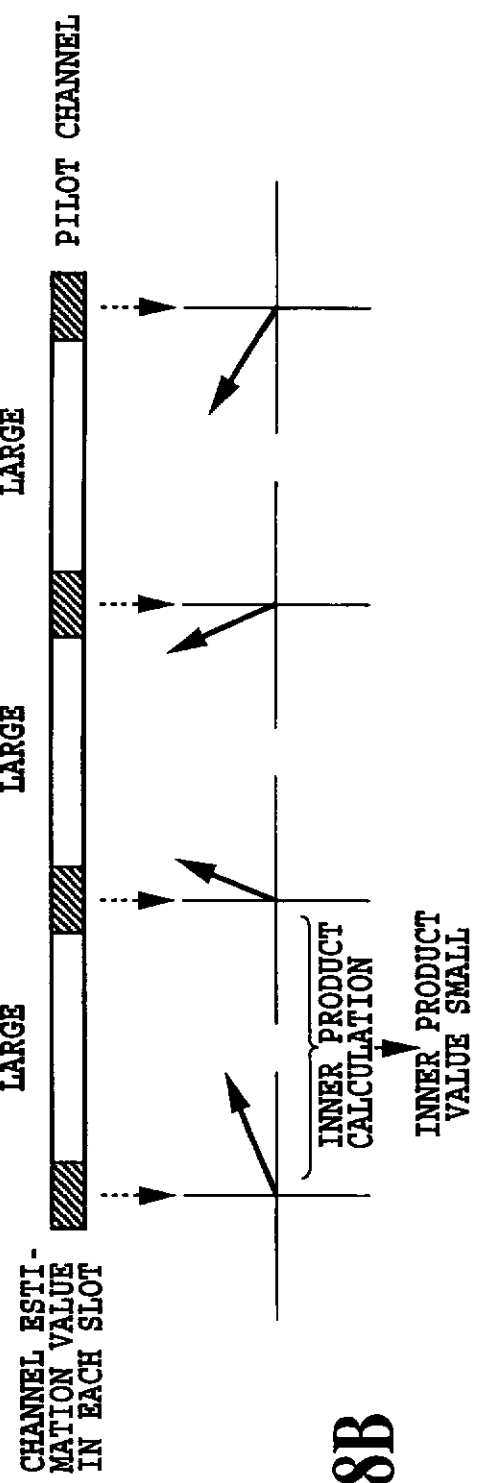


FIG. 28B



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FIG.30

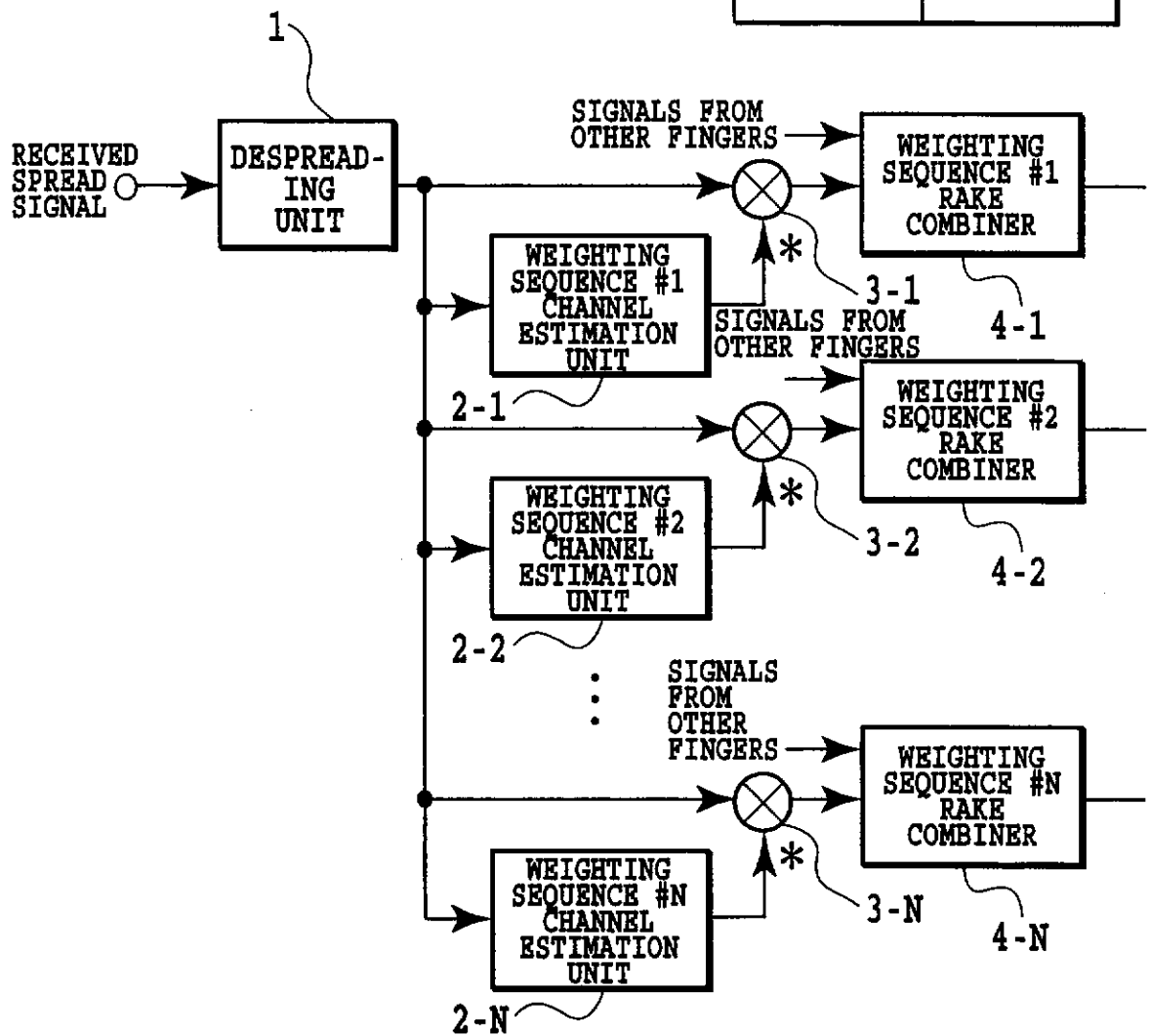
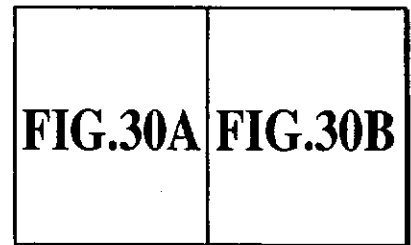


FIG.30A

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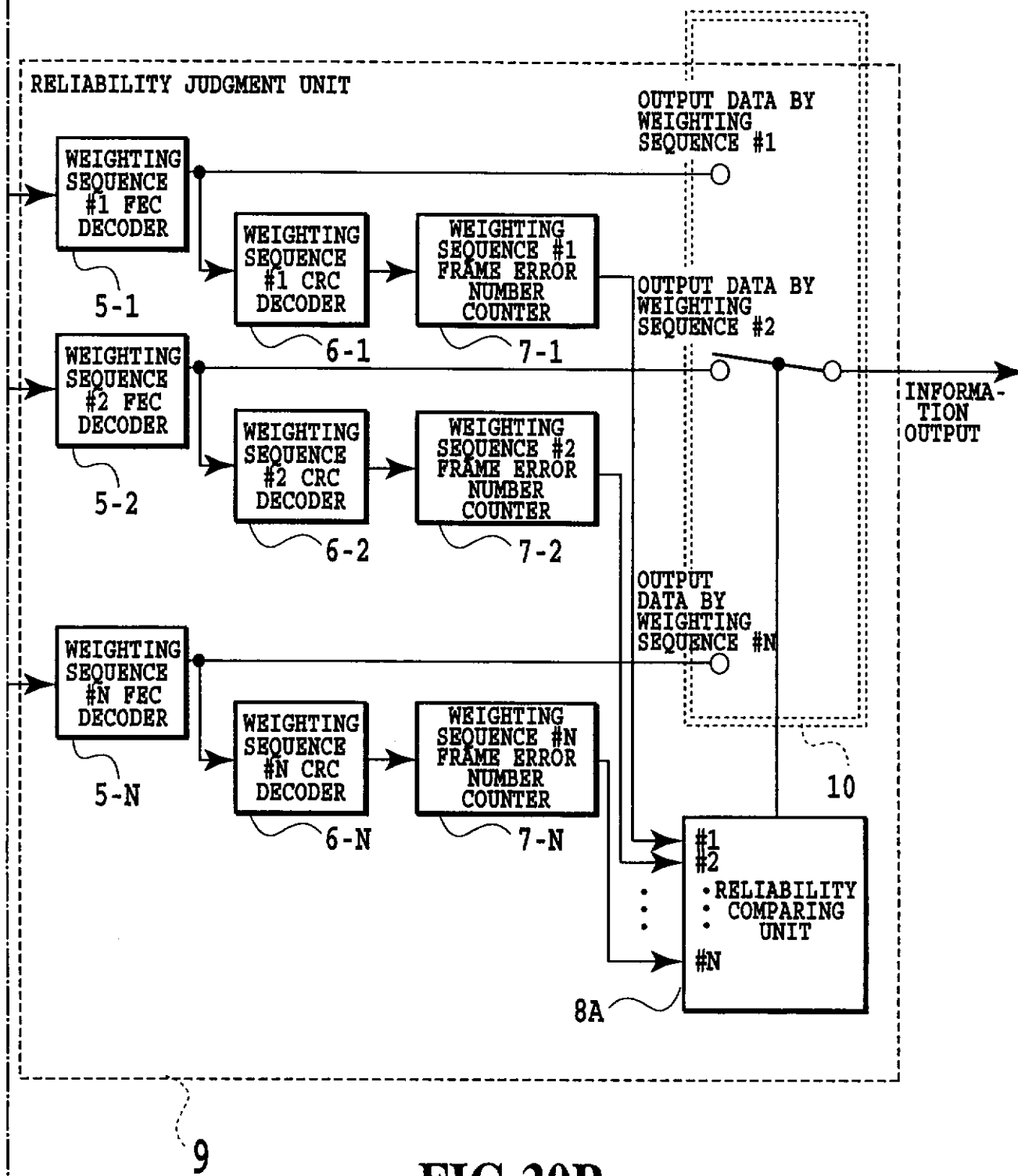


FIG.30B

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FIG.31

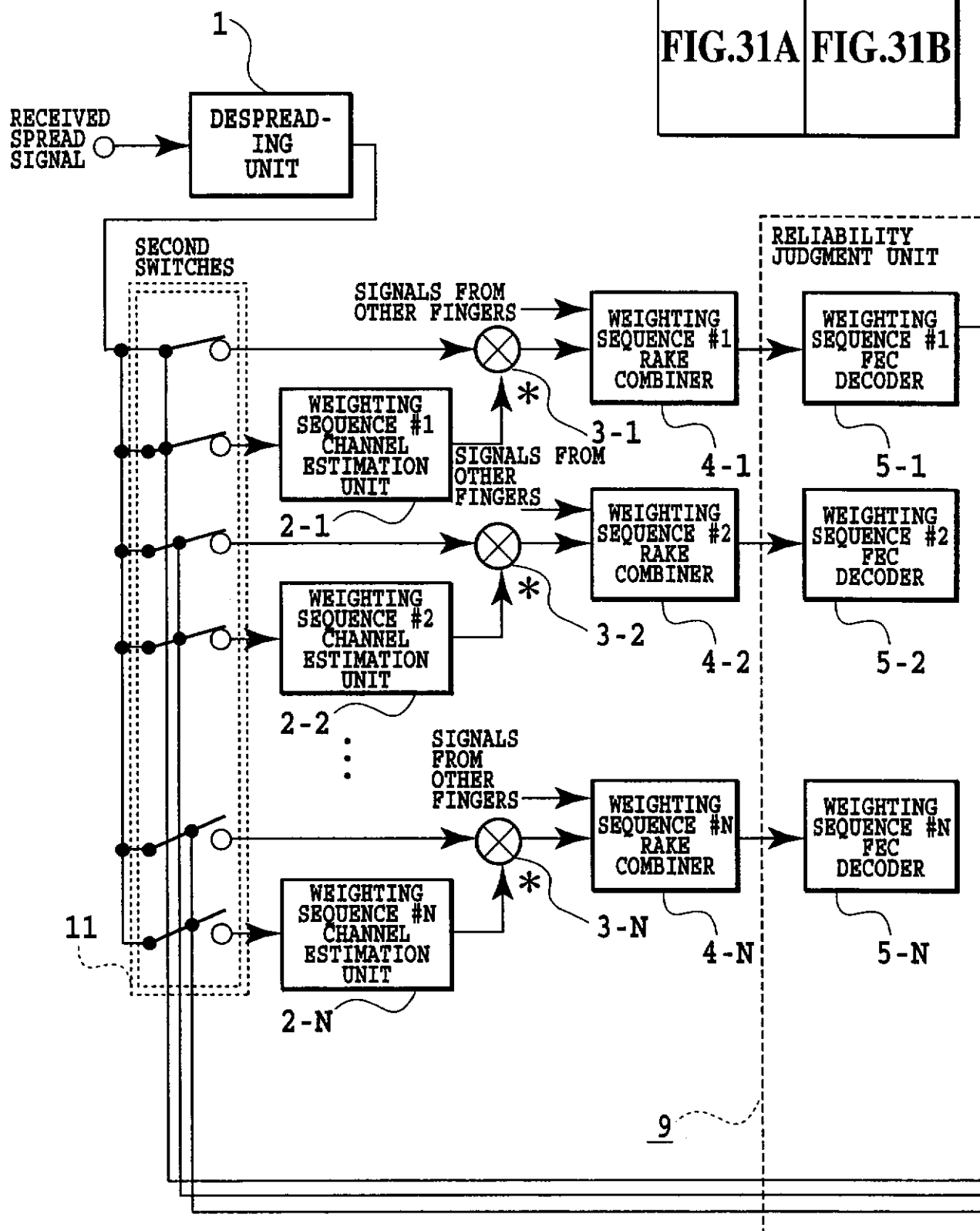


FIG.31A

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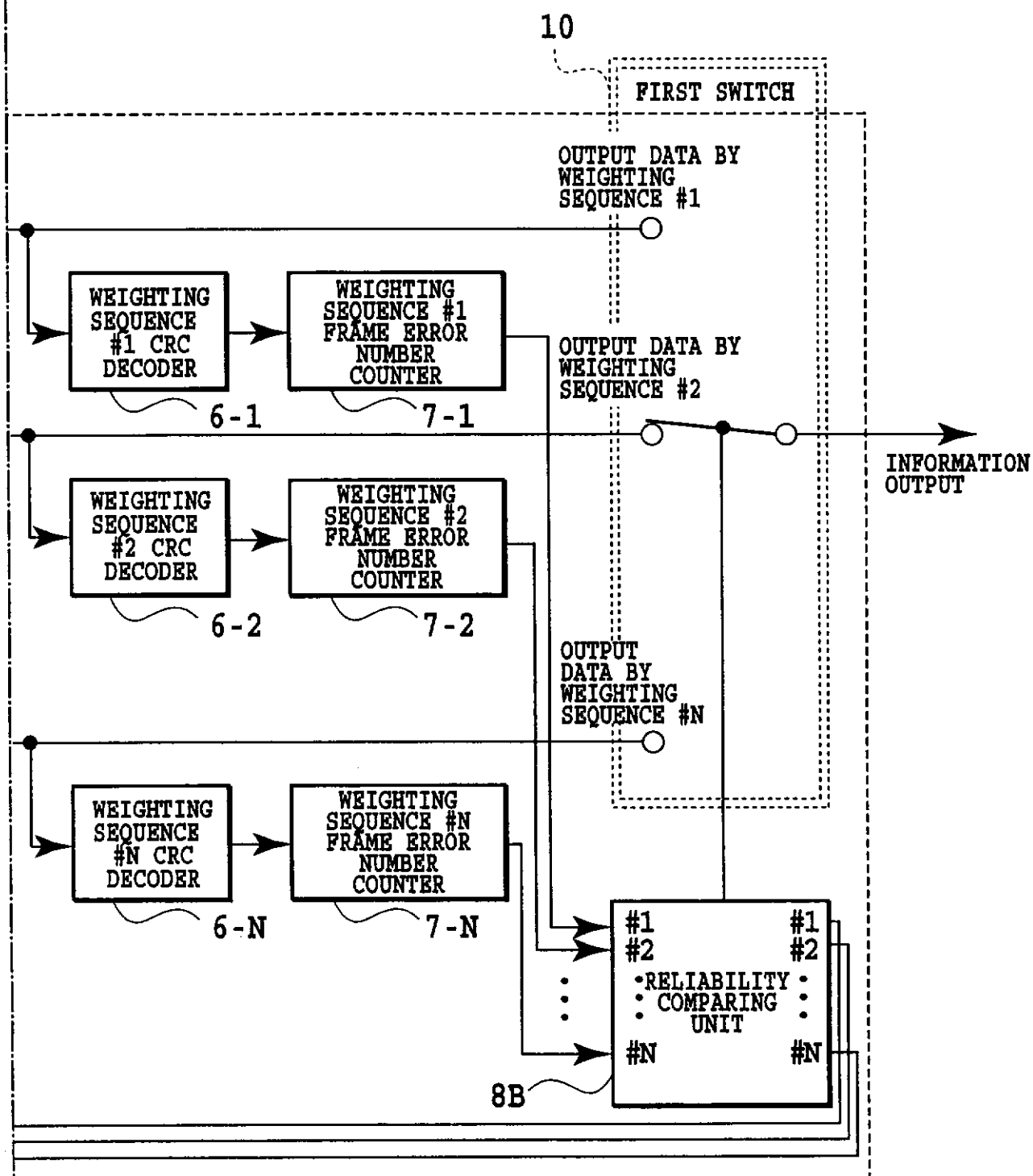


FIG.31B

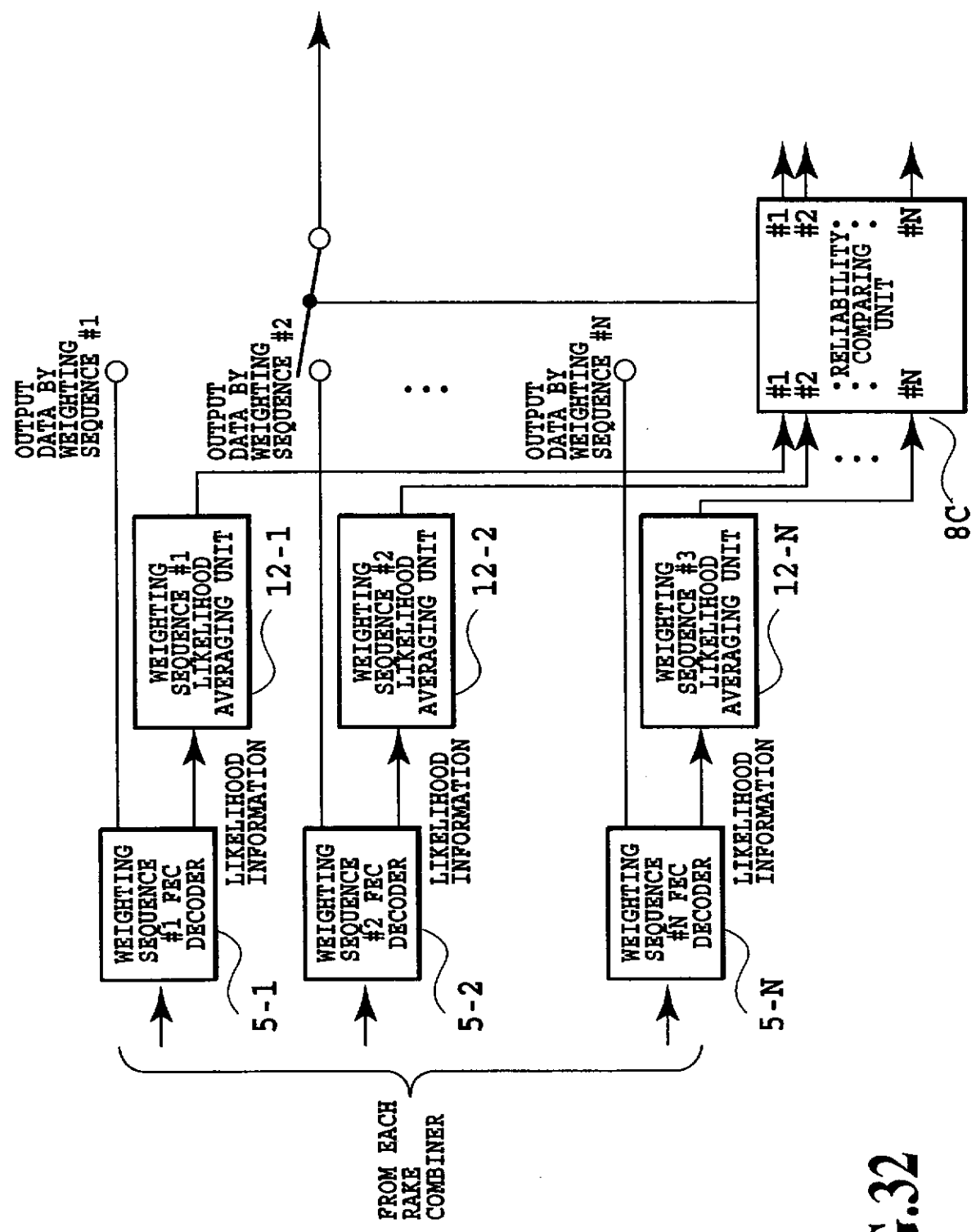


FIG.32

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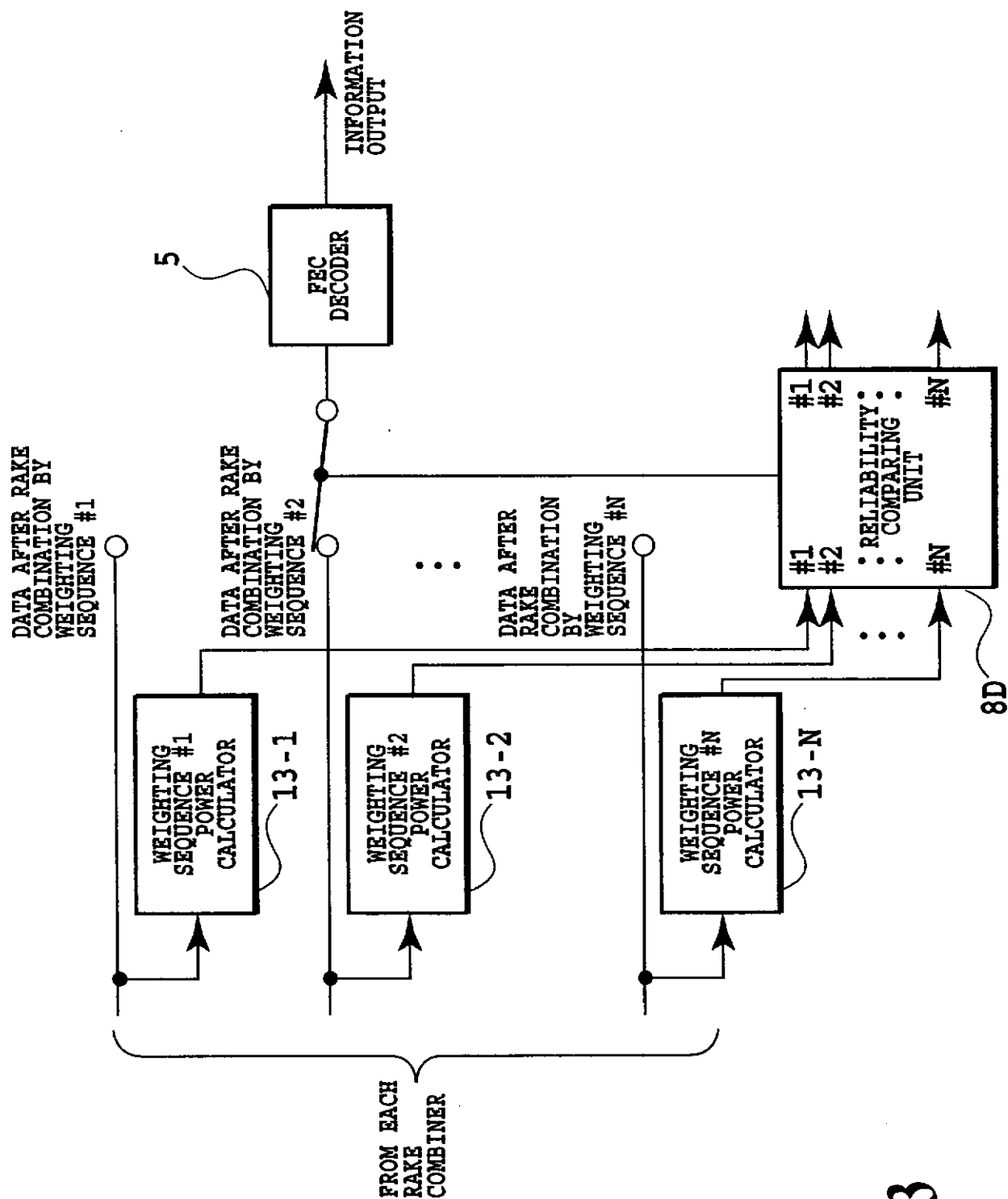


FIG.33

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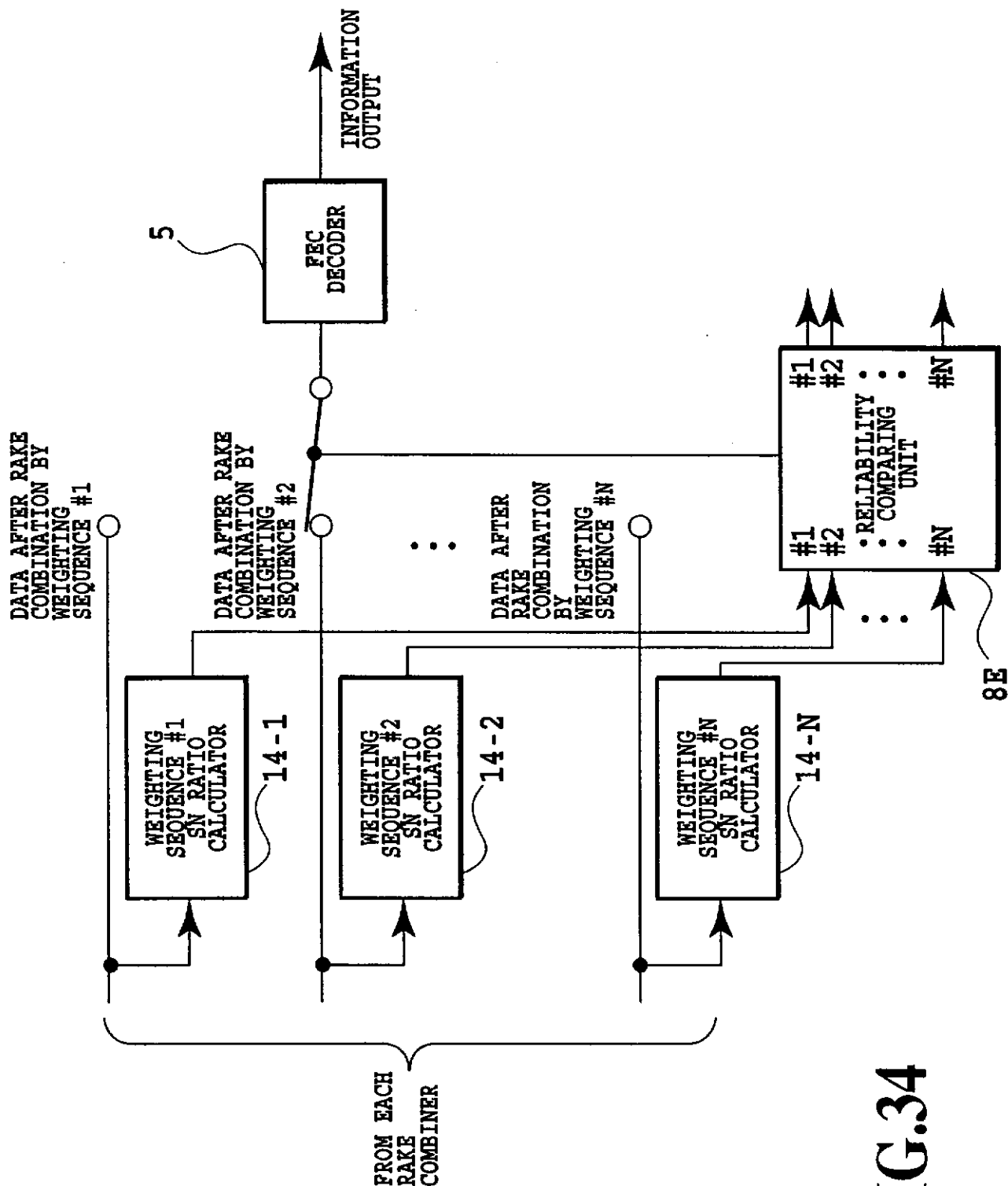


FIG.34

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FIG.35

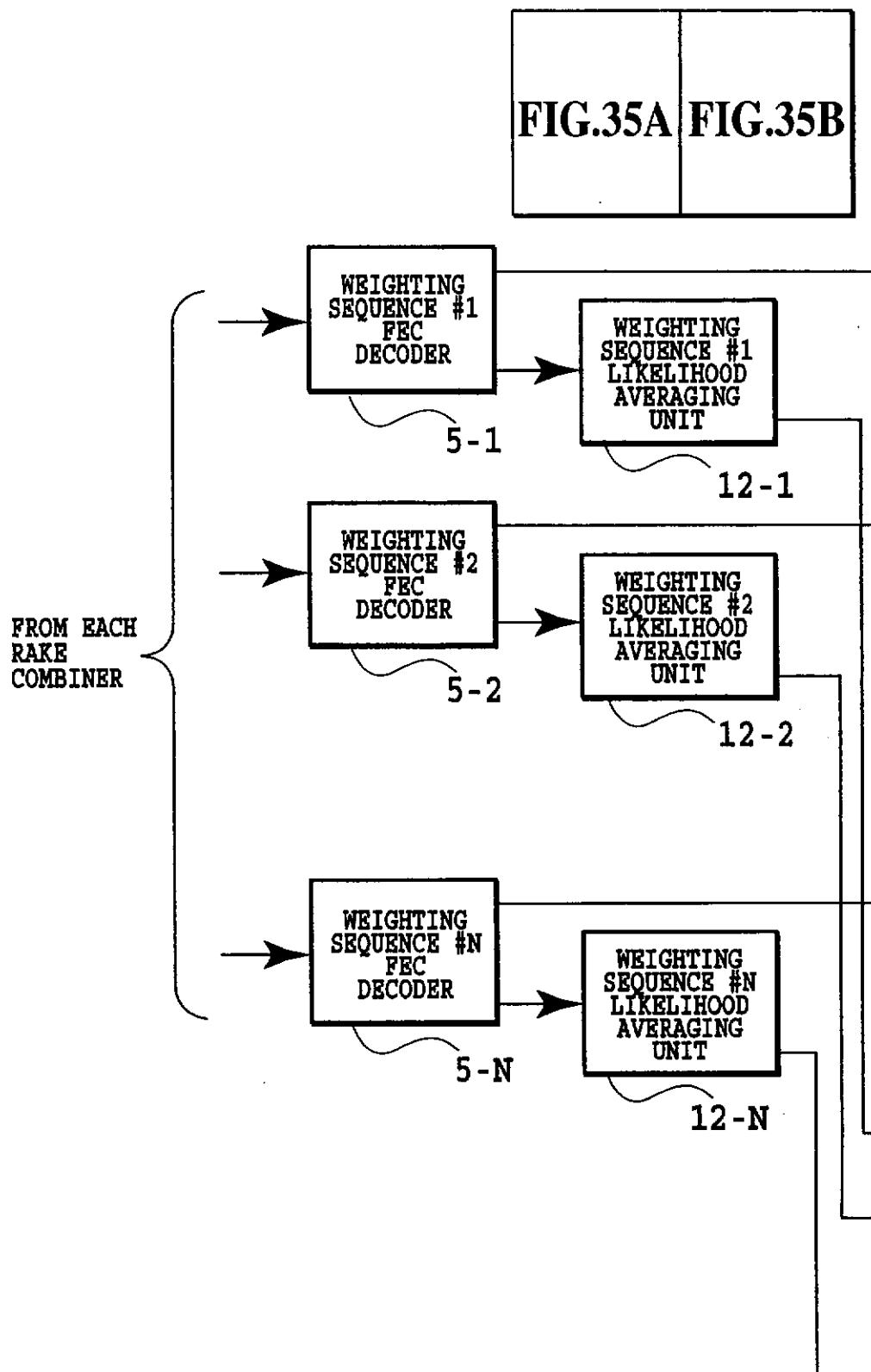


FIG.35A

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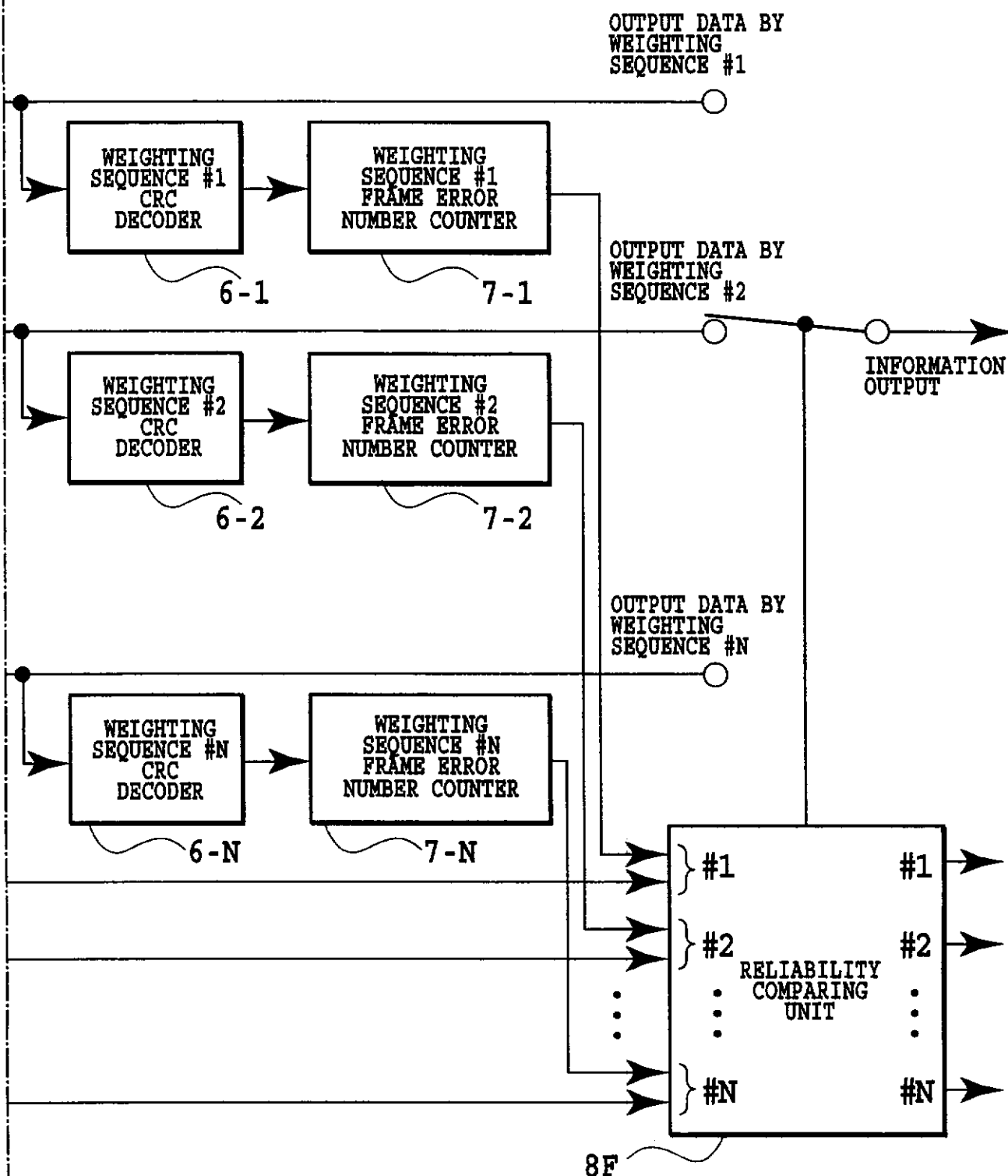


FIG.35B

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FIG.36

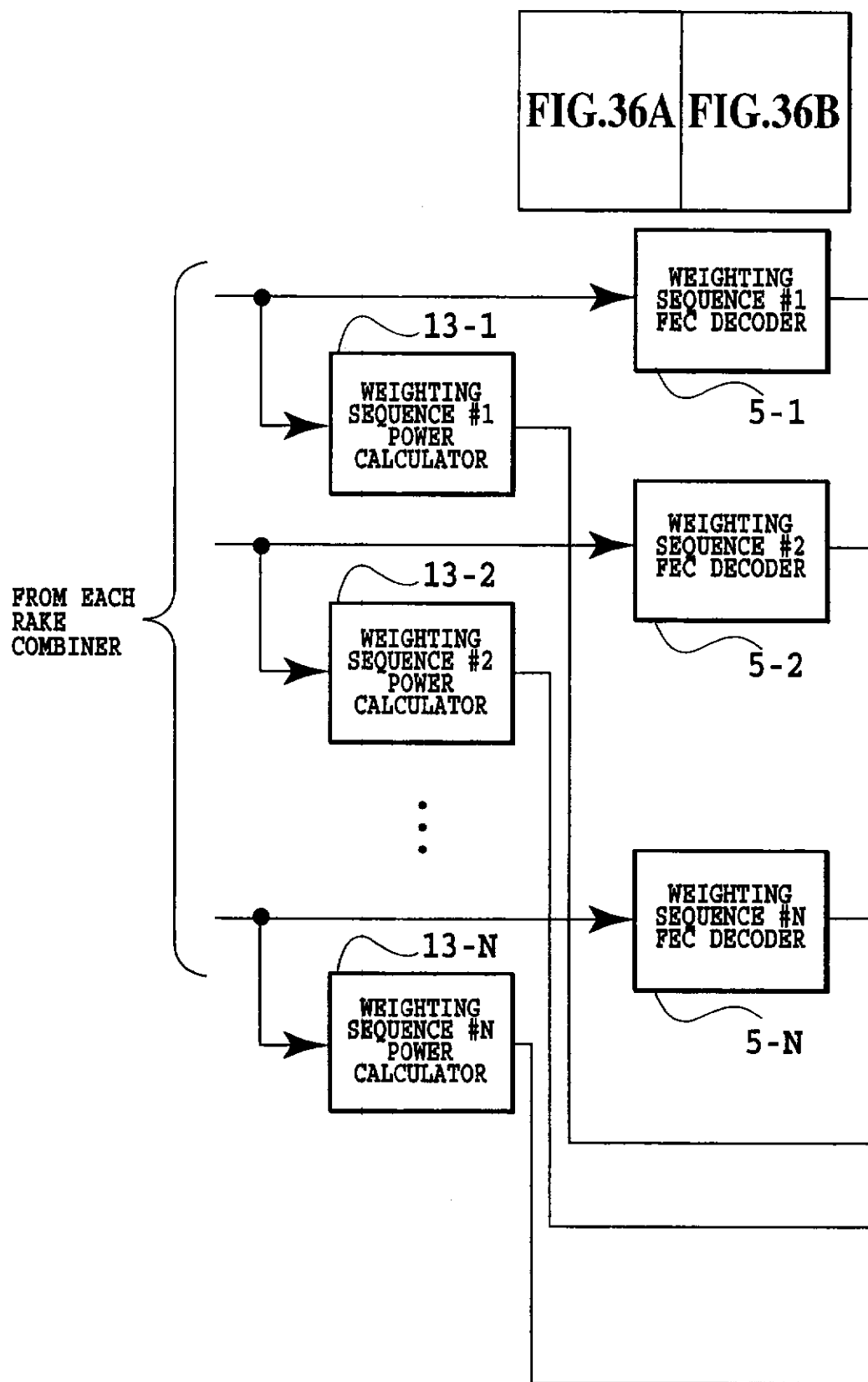


FIG.36A

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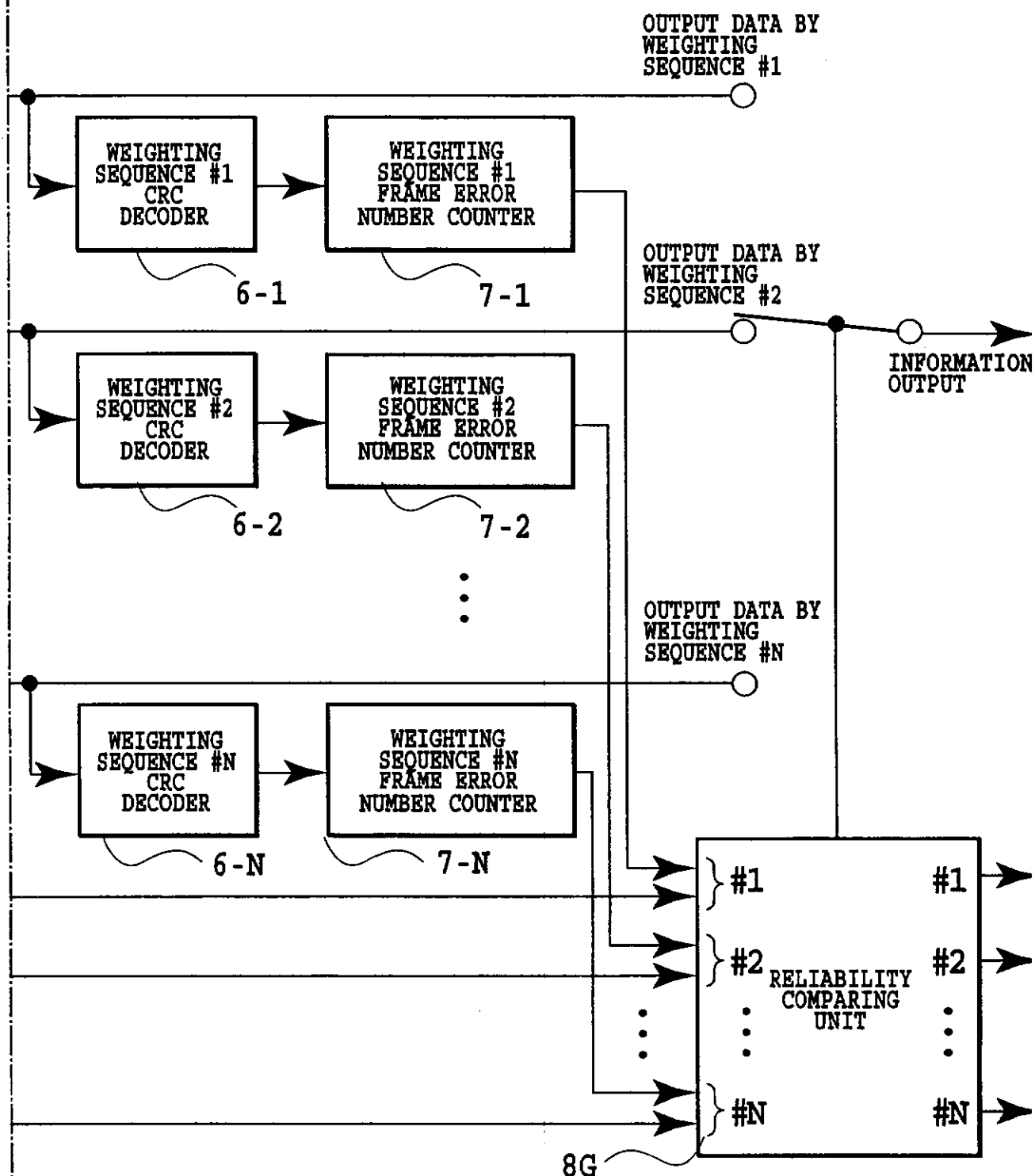


FIG.36B

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FIG.37

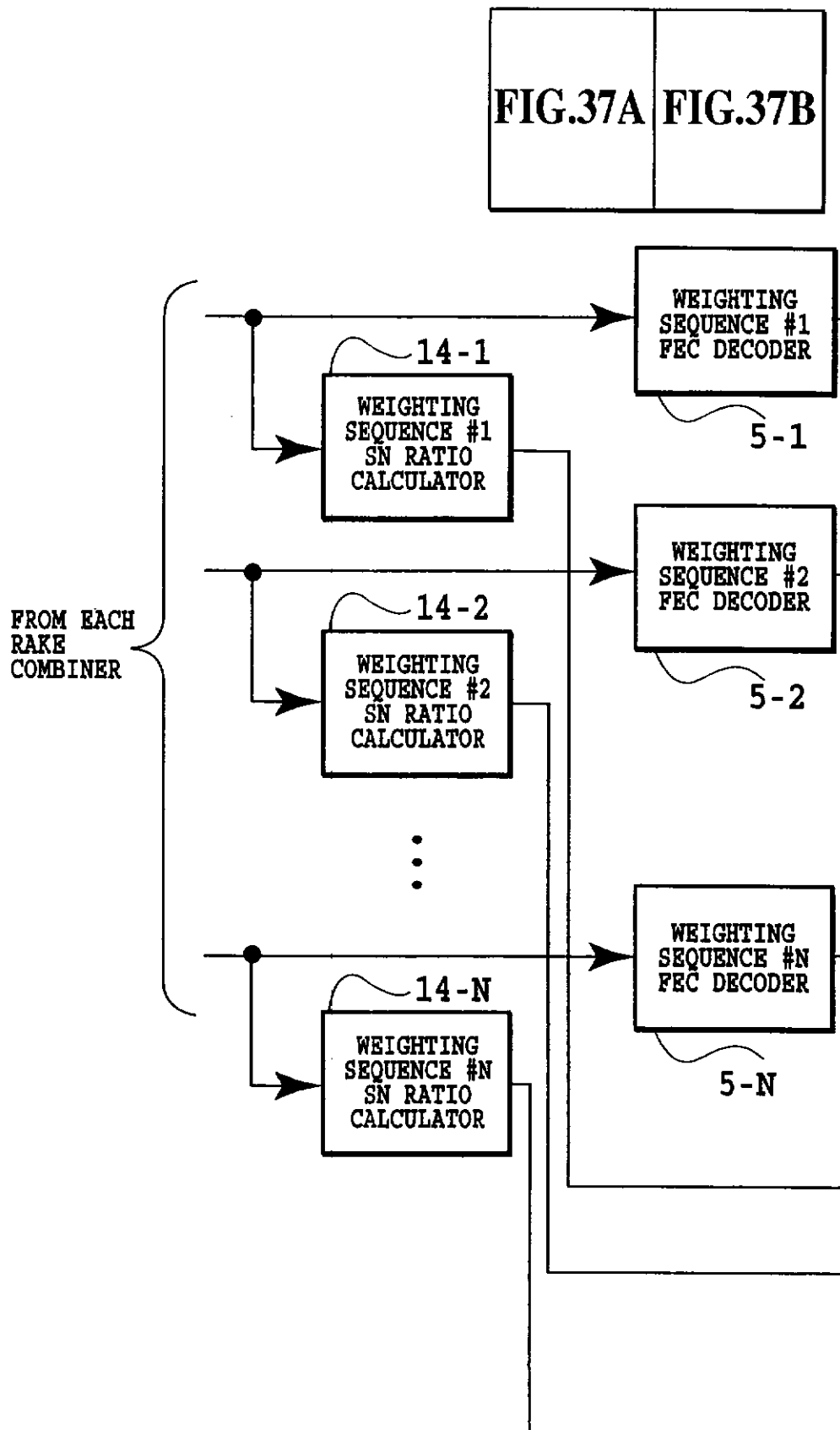


FIG.37A

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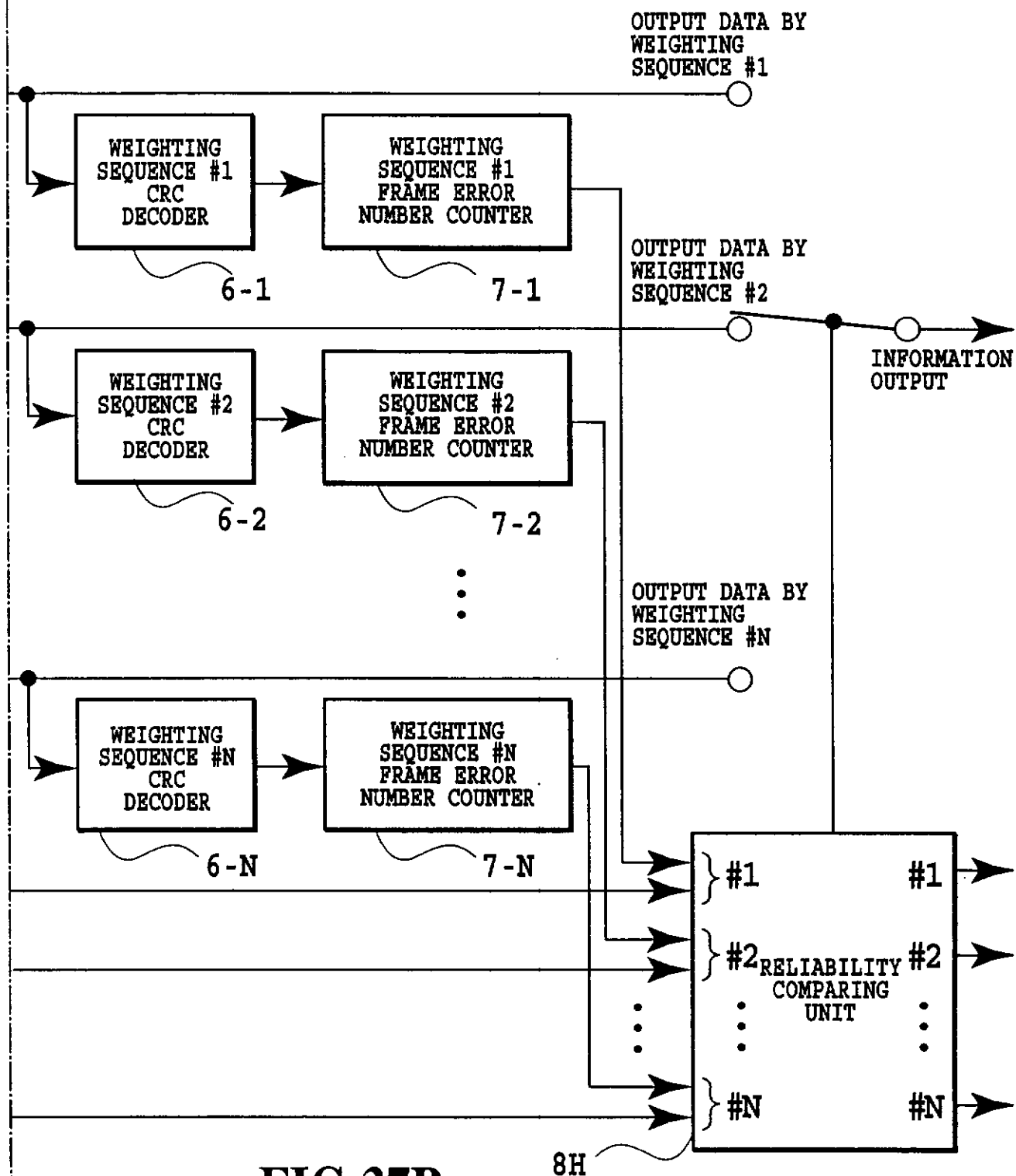


FIG.37B

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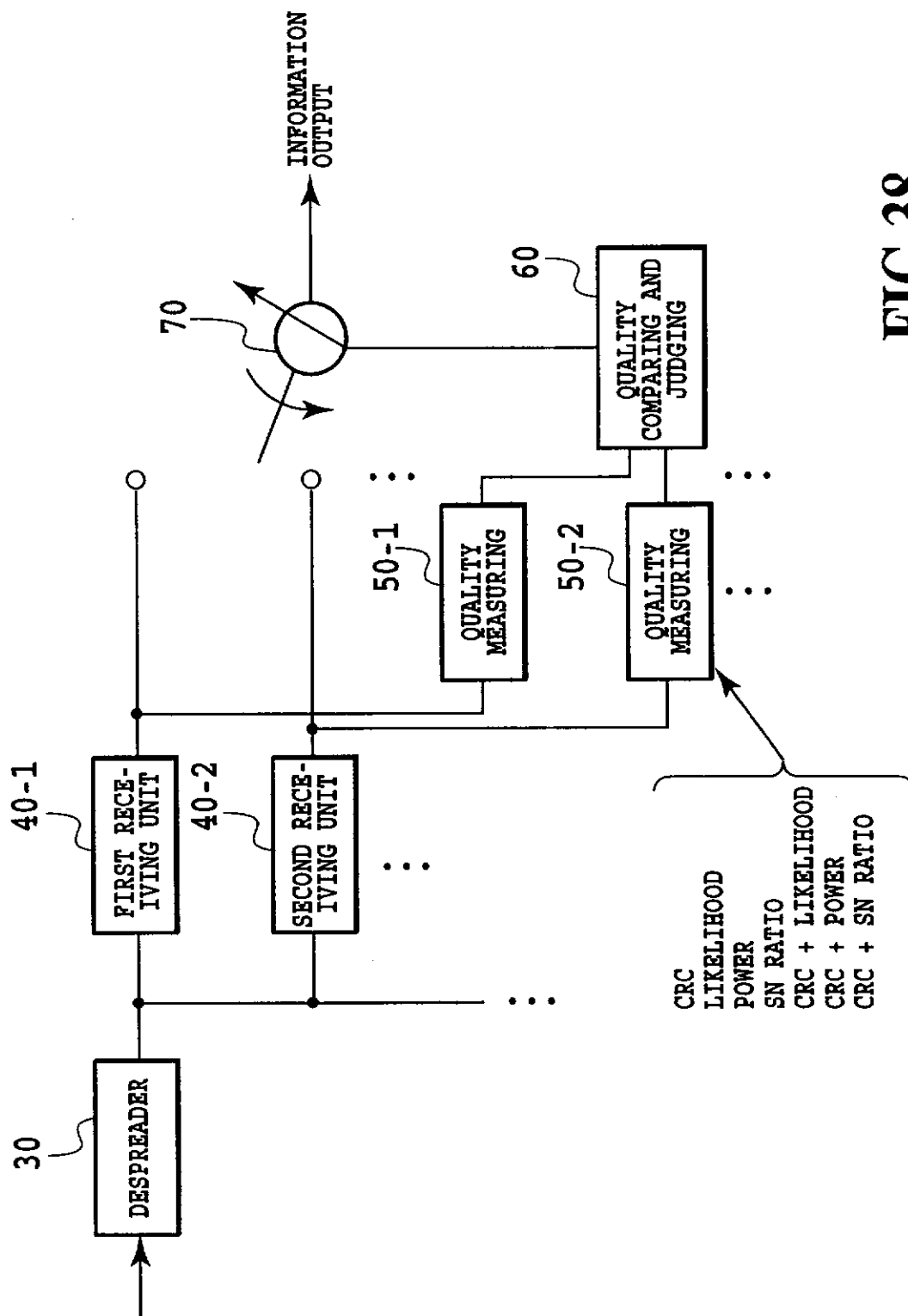


FIG. 38