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- (71) Applicant (for all designated States except US): **MI-CRON TECHNOLOGY, INC.** [US/US]; 8000 S. Federal Way, P.O. Box 6, Boise, ID 83707-0006 (US).
- (72) Inventor; and
- (75) Inventor/Applicant (for US only): **SCHUBERT, Martin, F.** [US/US]; 3914 S. Globe Theatre Avenue, Boise, ID 83716 (US).
- (74) Agents: **SPORER, Nicole, C.** et al.; Perkins Coie LLP, P.O. Box 1247, Seattle, WA 98111-1247 (US).
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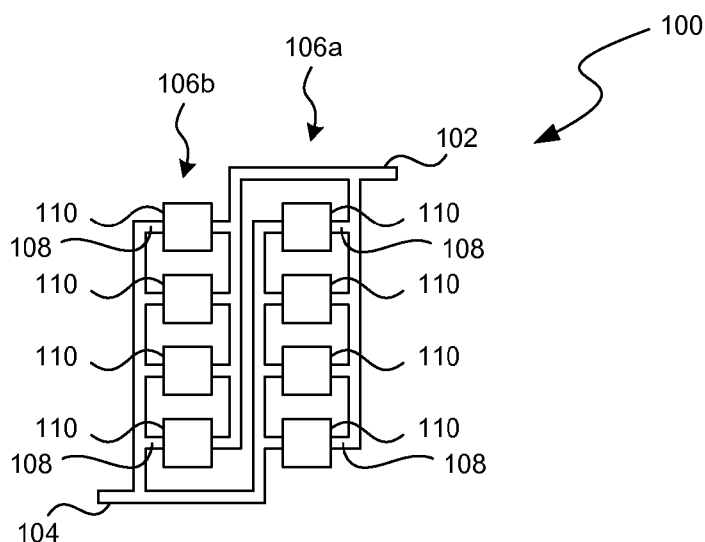


FIG. 1A

(57) Abstract: Various embodiments of solid state lighting ("SSL") assemblies with high voltage SSL dies and methods of manufacturing are described herein. In one embodiment, an array assembly of SSL dies includes a first terminal and a second terminal configured to receive an input voltage (V_o). The array assembly also includes a plurality of SSL dies coupled between the first terminal and the second terminal, at least some of which are high voltage SSL dies coupled in parallel.