

A D/A Converter with a Gamma Compensation and
a Liquid Crystal Display Device

Abstract

- 5 A DAC (100) comprises a decoder (1) generating an output corresponding to the value of the digital data on plurality of output lines, a constant current source (9), a plurality of driver transistors each connected to one of the plurality of output lines of the decoder (1) and to the constant current source, and conducting a current from the constant current source (9) when the connected output line is activated, and a plurality of current
- 10 limiting transistors (Q) each connected to each one of the plurality of driver transistors, the plurality of output lines of the decoder (1) being grouped correspondingly to each of the ranges of the gamma compensation curve, and a common reference voltage for each group being applied to the control terminal of the current limiting transistors associated with the output lines in each group.

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Figure 1

This Sheet Has Been Replaced

