

ABSTRACT

The present application relates to a memory check pin processing method and apparatus, a device, a storage medium, and a computer program product. The method can be applied to an application scene of a cloud technology and a vehicle-mounted terminal, and comprises: receiving first data signals and a sampling pulse signal returned by check pins in a target memory on the basis of a first data reading instruction; respectively performing time shift on the first data signals by means of a time delay circuit, so that target level values in the first data signals after the time shift are aligned with the sampling pulse signal to obtain a first time delay parameter; when second data signals returned by the check pins in the target memory are received, respectively performing time shift on target data signals in the second data signals by means of the time delay circuit, so that target level values in the second data signals after the time shift are aligned to obtain a second time delay parameter; and determining sampling time delay parameters of the check pins on the basis of the first time delay parameter and the second time delay parameter. By using the method, the correctness when the data signals are sampled on the basis of the sampling pulse signal can be ensured.