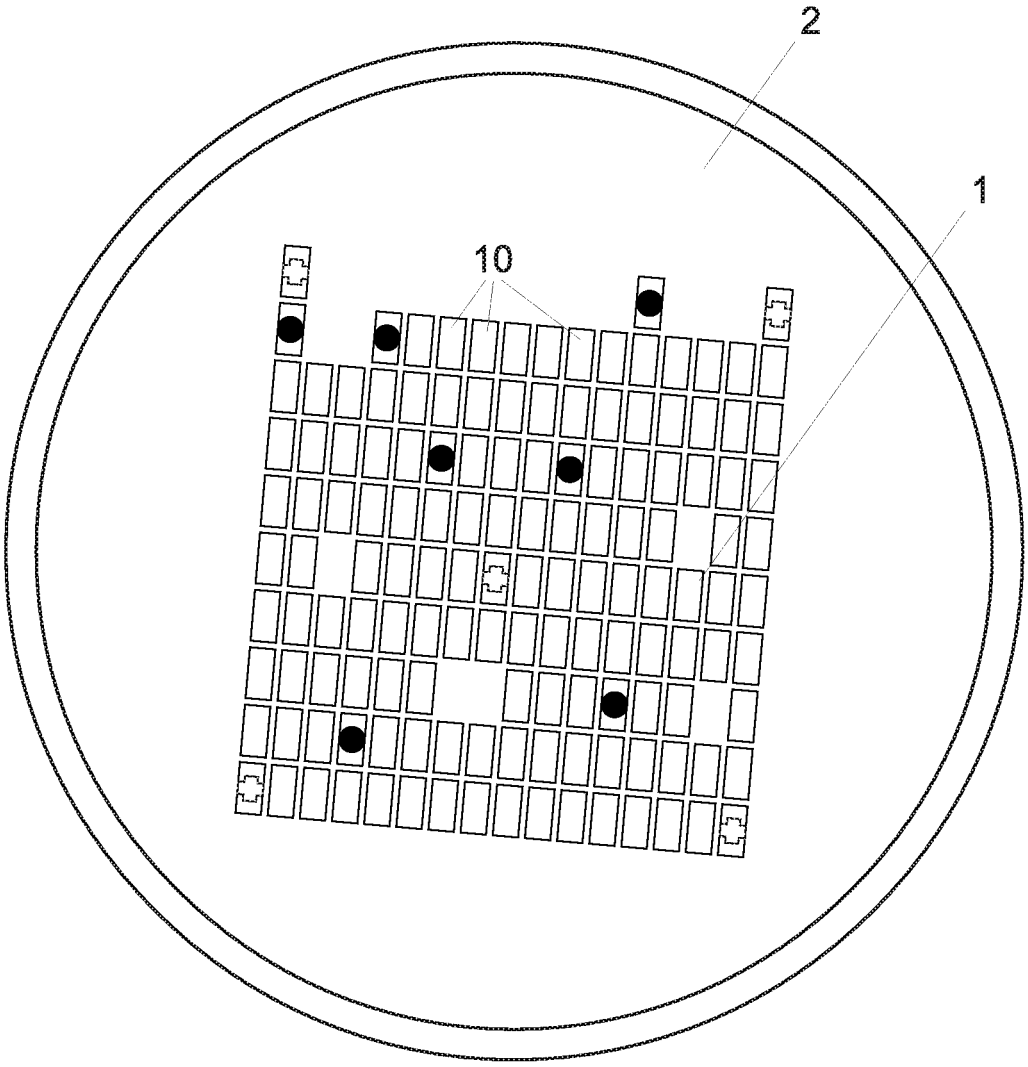


Fig. 1

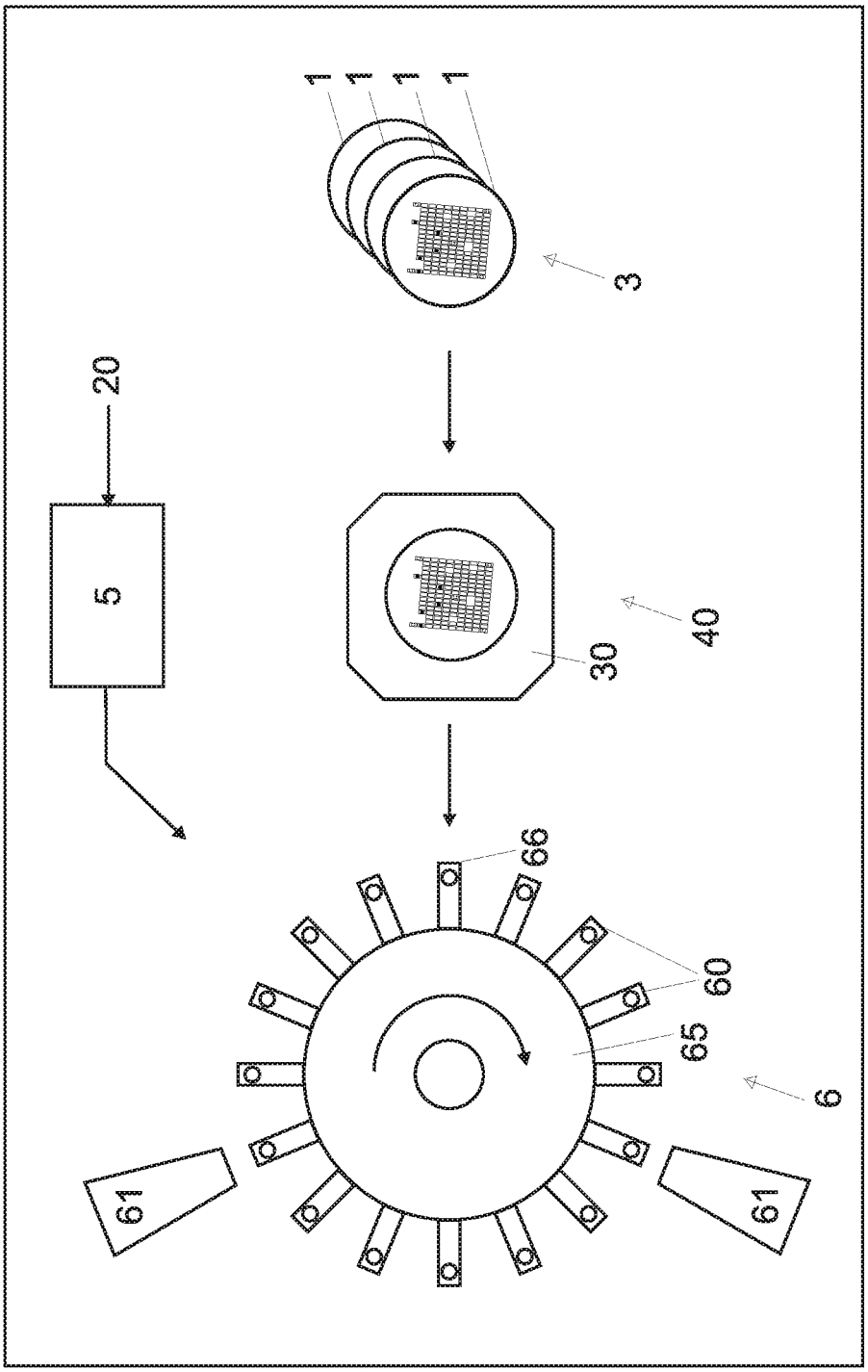


Fig. 2

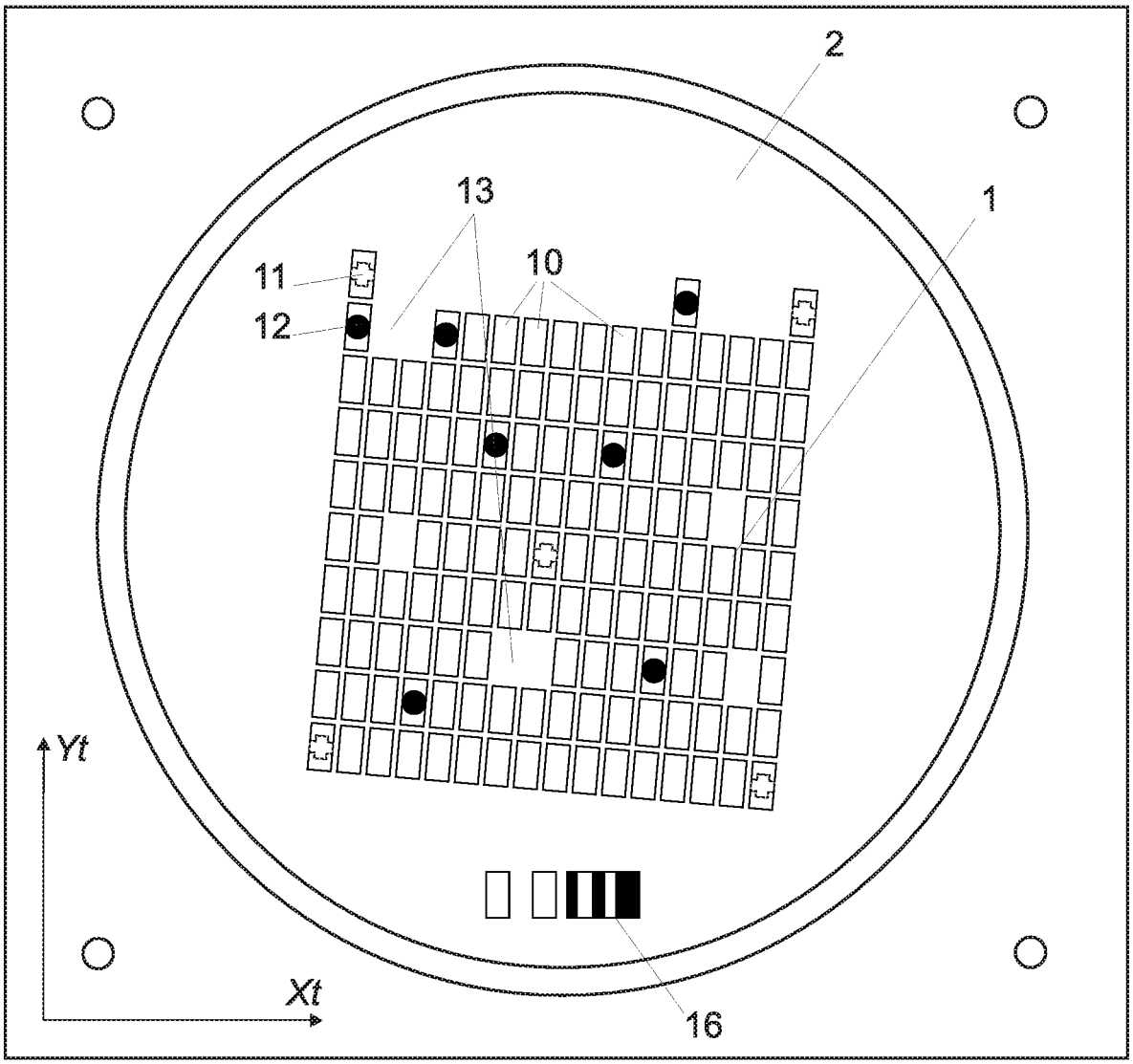


Fig. 3

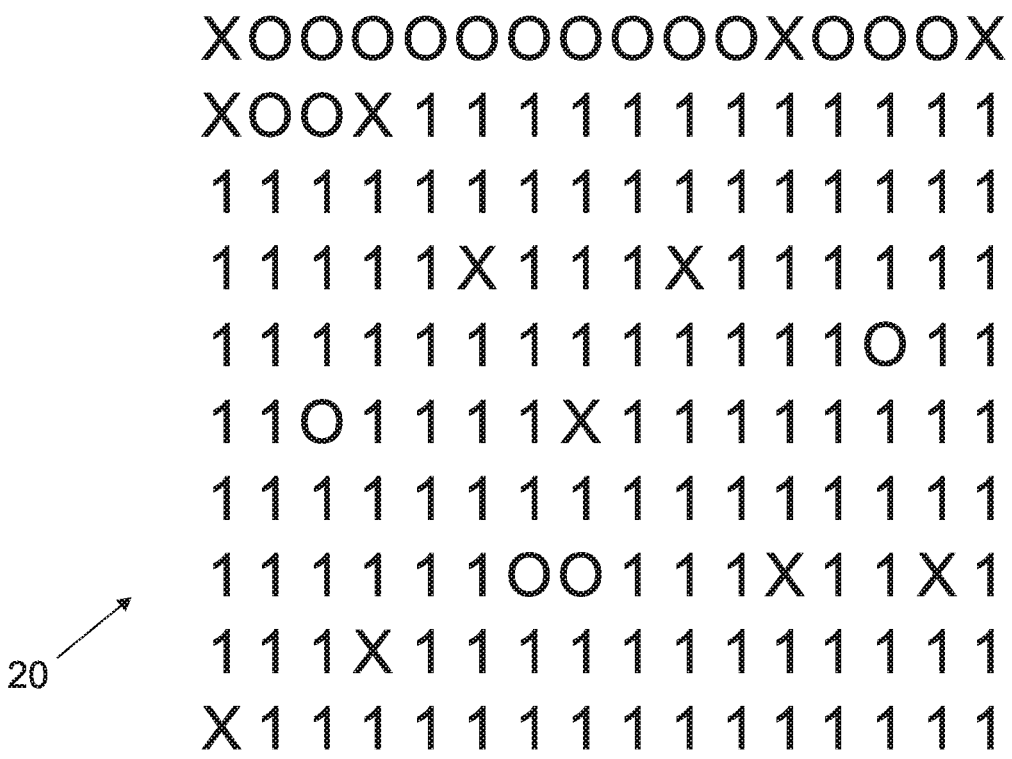

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Fig. 4

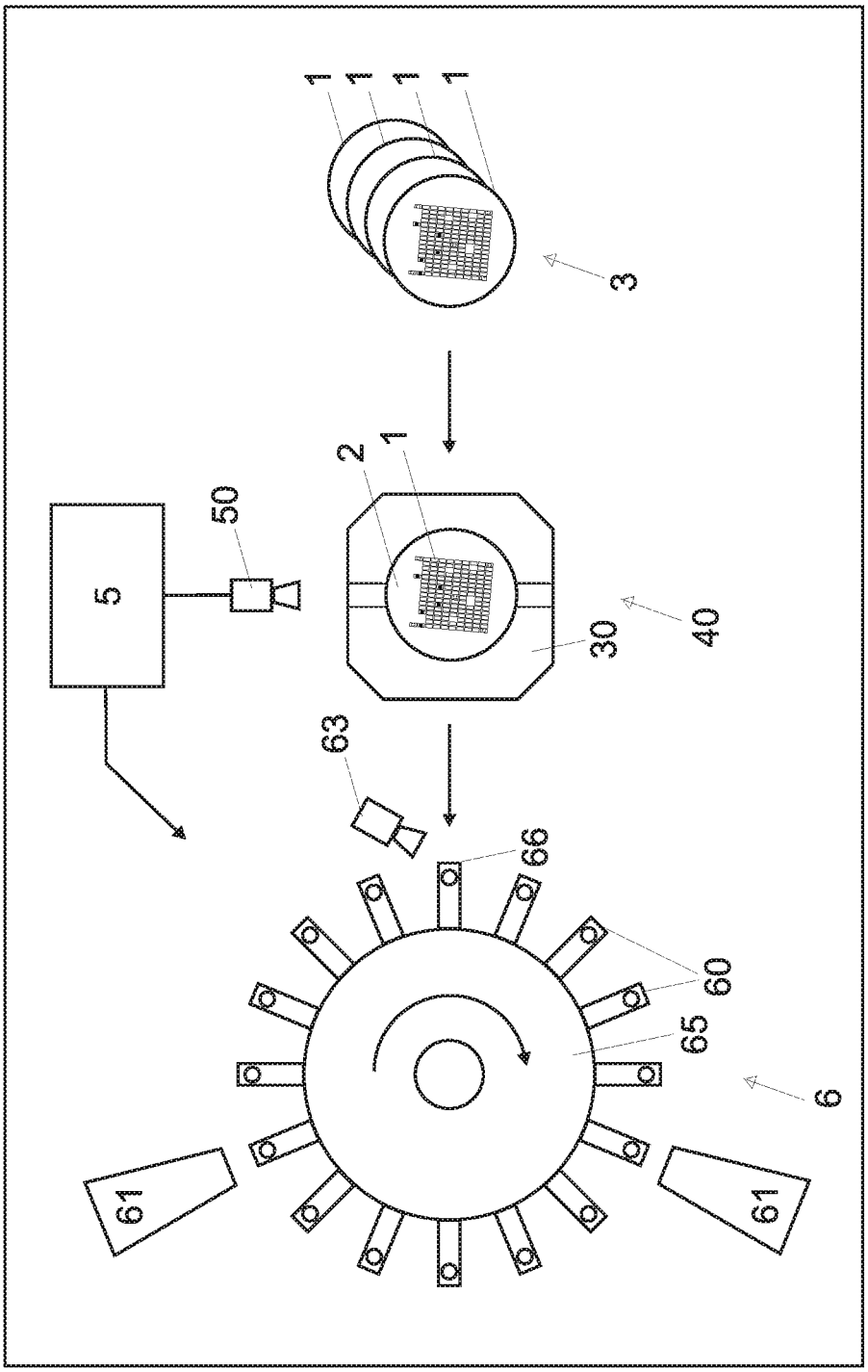


Fig. 5

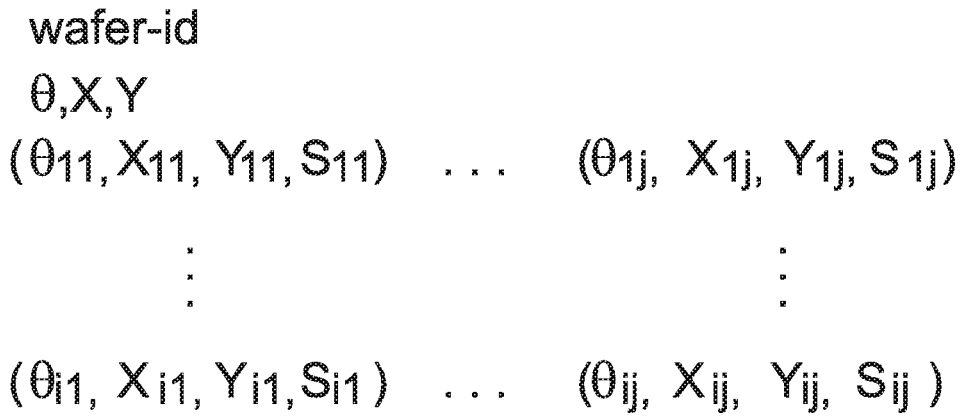

wafer-id
θ,X,Y
(θ11, X11, Y11, S11) . . . (θ1j, X1j, Y1j, S1j)
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Fig. 6

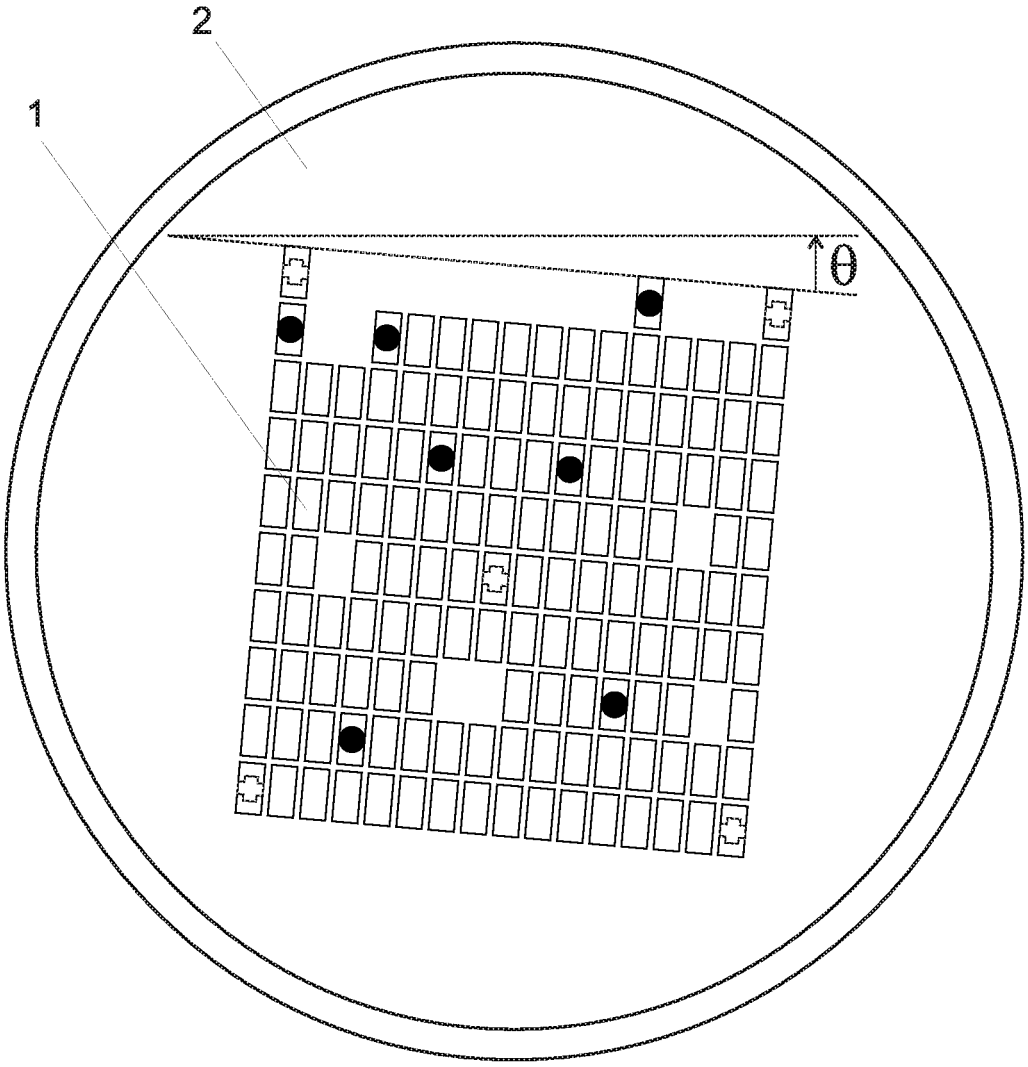


Fig. 7

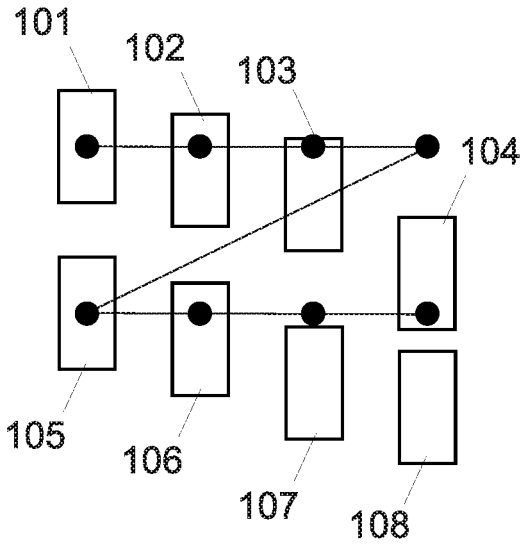


Fig. 8

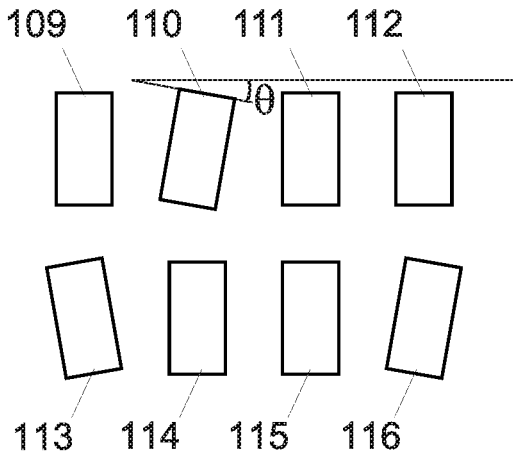


Fig. 9

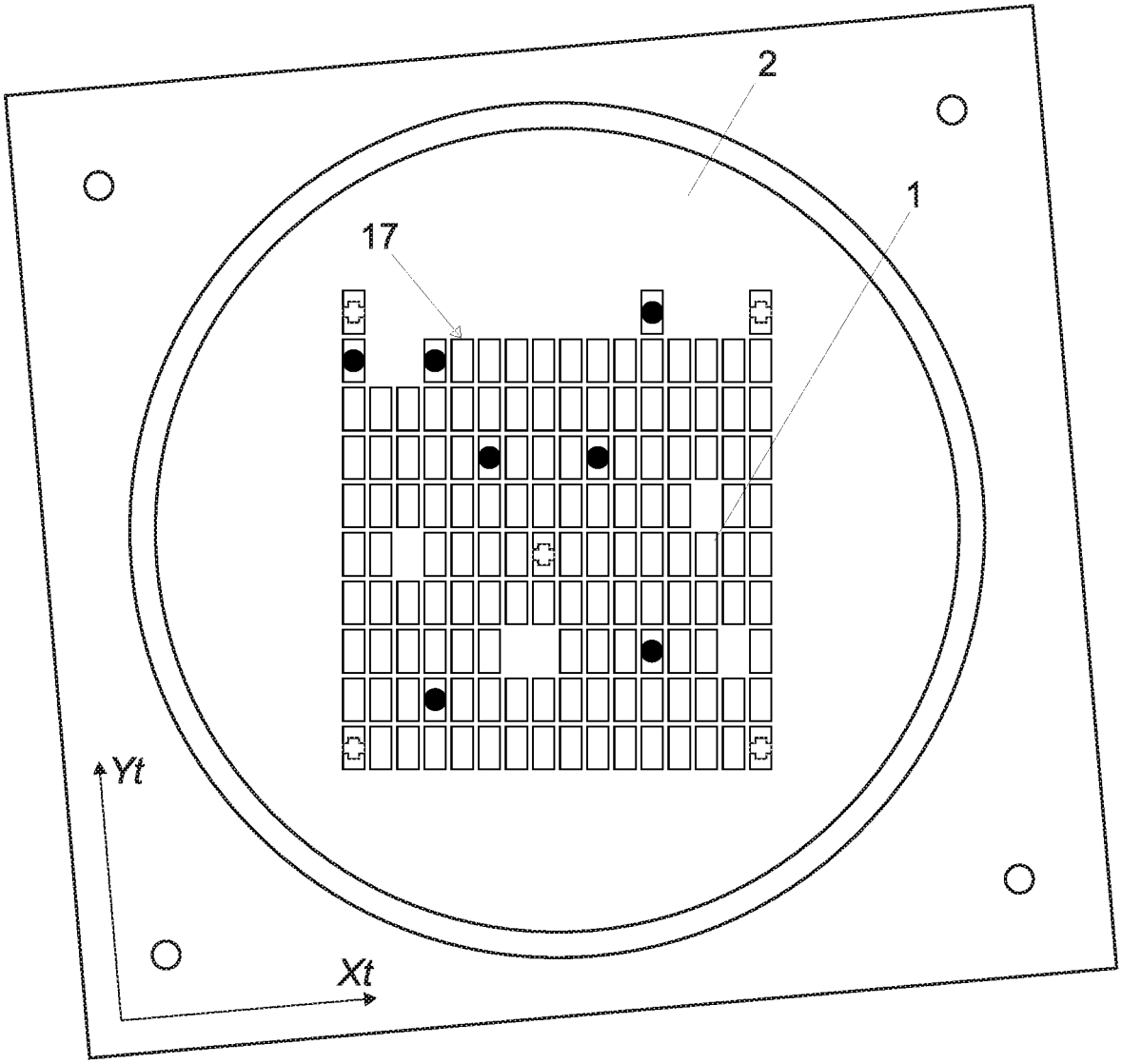


Fig. 10

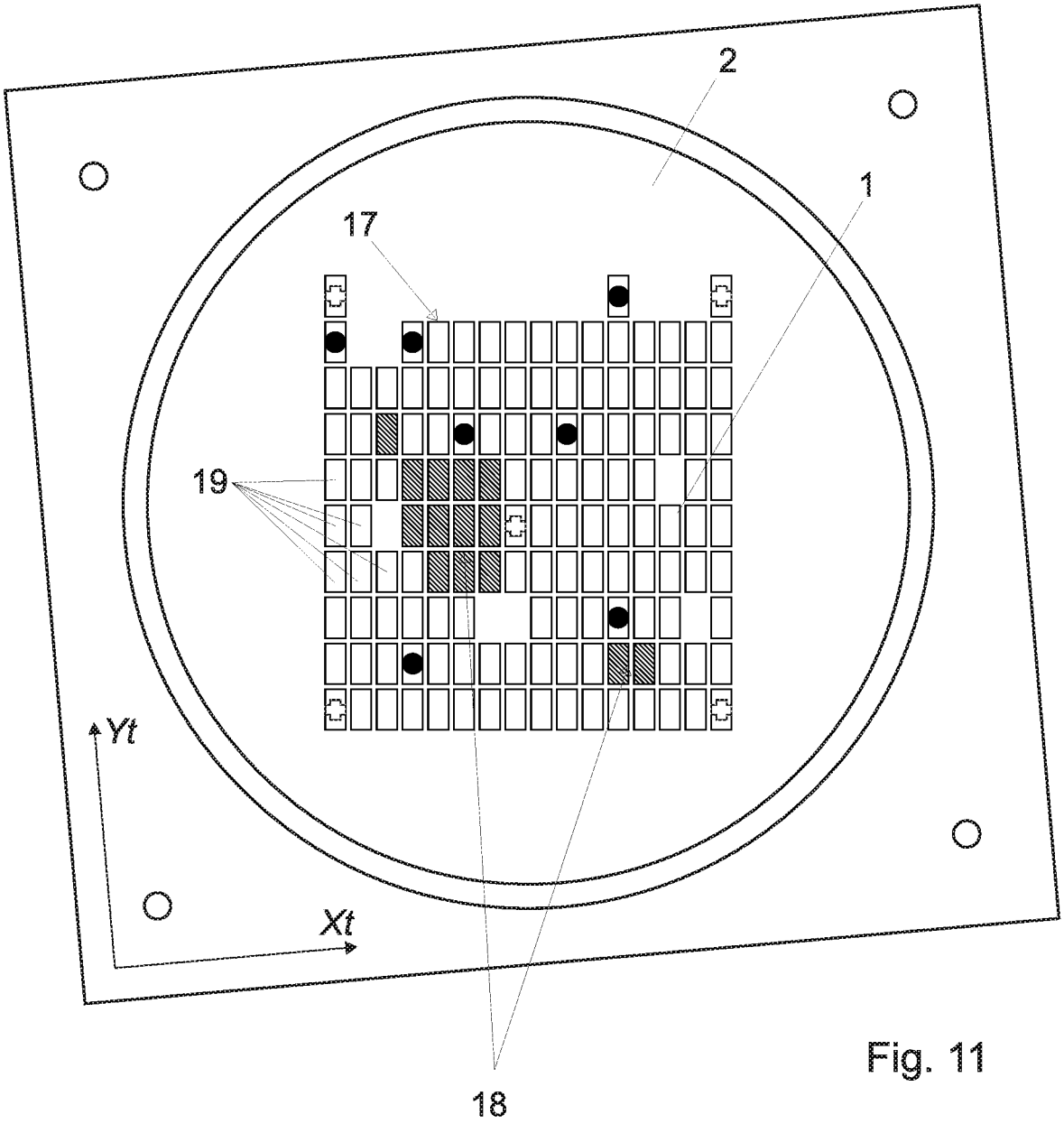


Fig. 11

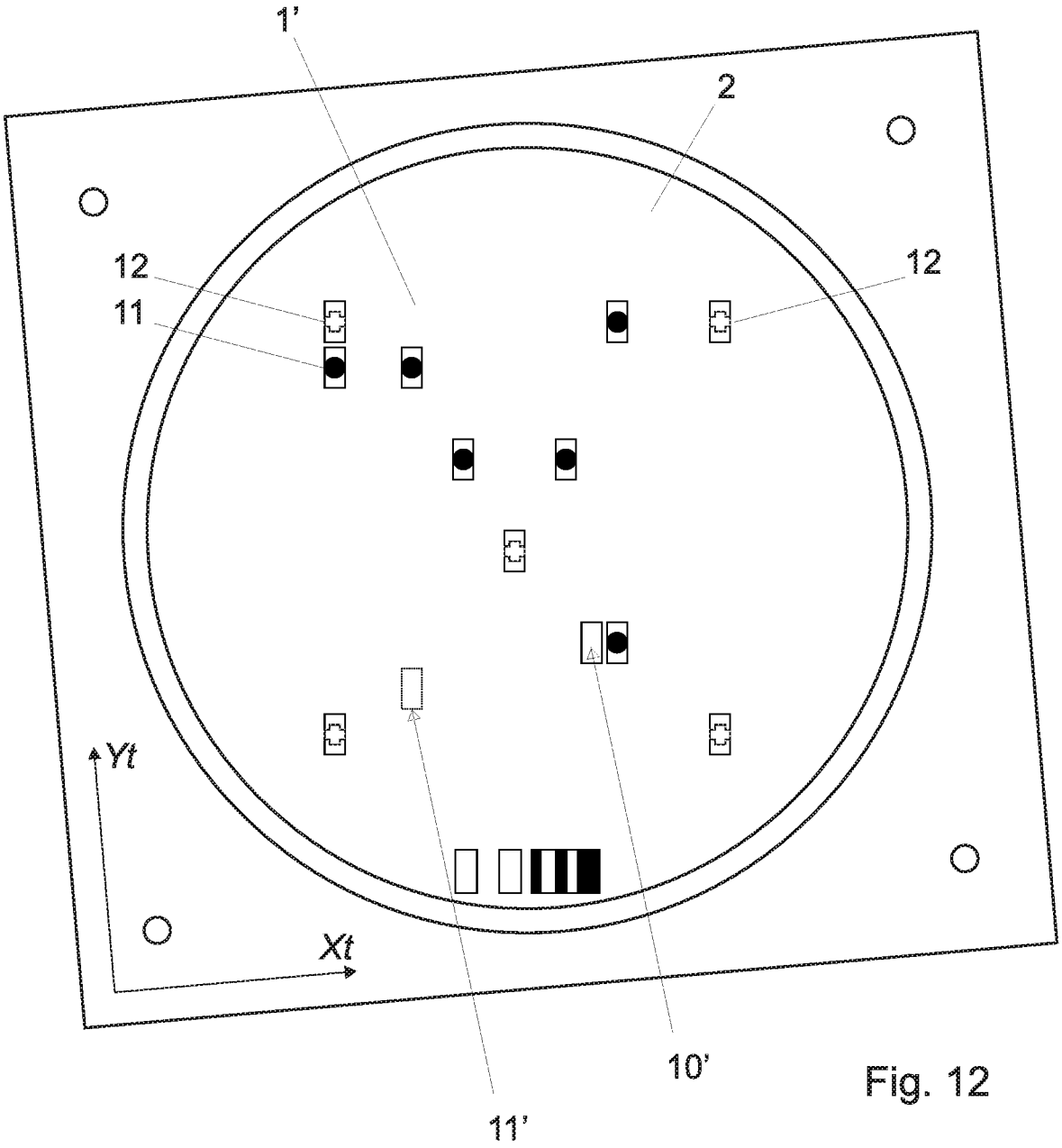


Fig. 12