

The diagram illustrates a non-destructive readout system for a magnetic memory device. It includes a magnetic core (10) with a loop (11) and a readout head (12). The system is divided into several functional blocks: a preamplifier (20) containing a differential amplifier (21) and a filter (22); a signal processor (30) with two parallel paths (31a, 31b) each containing a differential amplifier (32a, 32b) and a filter (33a, 33b); and a control unit (40) with a readout head (41) and a control circuit (42). The control unit is connected to the signal processor via a control line (43). A power supply (50) provides power to the system. A dashed box (70) indicates a connection to a bus (20, 30, 50).

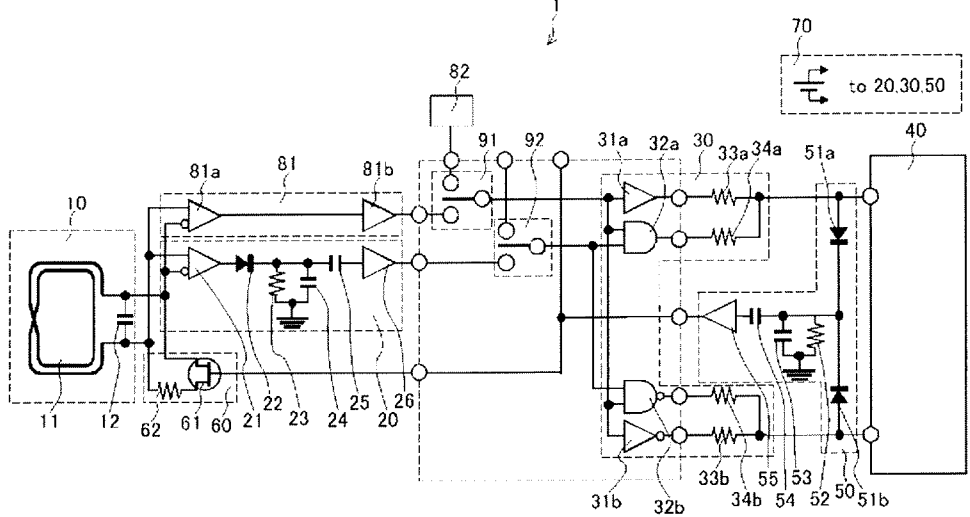


Fig. 2

