

WHAT IS CLAIMED IS:

1. A method of video decoding comprising:
receiving a coded video bitstream including a current picture;
determining a predetermined condition associated with signaling data included in the coded video bitstream;
determining, based on the predetermined condition, a size of an index included in the signaling data for a candidate list of vector predictors based on a number of merge mode candidates and a number of intra block copy (IBC) candidates;
constructing the candidate list with vector predictors;
retrieving a vector predictor from the candidate list in accordance with the index that has a value that does not exceed the determined size of the index; and
decoding the current block in accordance with the retrieved vector predictor.
2. The method of video decoding according to claim 1, wherein the predetermined condition includes determining whether the current block is encoded in the IBC mode.
3. The method of video decoding according to claim 2, wherein in response to determining that the current block is encoded in the IBC mode, setting the size of the index to a maximum number of IBC candidates, and in response to determining that the current block is not encoded in the IBC mode, setting the size of the index to a maximum number of merge mode candidates.
4. The method of video decoding according to claim 1, wherein the predetermined condition includes determining whether a maximum number of merge mode candidates is greater than a maximum number of IBC candidates.
5. The method of claim 4, wherein in response to determining that the maximum number of merge mode candidates is greater than the maximum number of IBC candidates, setting the size of the index to the maximum number of merge mode candidates, and in response to determining that the maximum number of merge mode candidates is less than the maximum

number of IBC candidates, setting the size of the index to the maximum number of IBC candidates.

6. A method of video decoding comprising:
receiving a coded video bitstream including a current picture;
retrieving signaling data from the coded video bitstream for a current block;
determining whether a maximum number of merge candidates is included in the retrieved signaling data for the current block; and
setting a maximum number of intra block copy (IBC) candidates based on the determination of whether the maximum number of merge candidates is included in the signaling data for the current block.

7. The method of video decoding according to claim 6, wherein in response to a determination that the maximum number of merge candidates is not included in the signaling data, the maximum number of IBC candidates is set to a value between 1 to 6.

8. The method of video decoding according to claim 7, wherein in response to a determination that the maximum number of merge candidates is included in the signaling data, the maximum number of IBC candidates is set to a value between 1 to the maximum number of merge candidates.

9. A video decoding apparatus for video decoding, comprising:
processing circuitry configured to:
receive a coded video bitstream including a current picture,
determine a predetermined condition associated with signaling data included in the coded video bitstream,
determine, based on the predetermined condition, a size of an index included in the signaling data for a candidate list of vector predictors based on a number of merge mode candidates and a number of intra block copy (IBC) candidates,
construct the candidate list with vector predictors,
retrieve a vector predictor from the candidate list in accordance with the index that has a value that does not exceed the determined size of the index, and

decode the current block in accordance with the retrieved vector predictor.

10. The video decoding apparatus according to claim 9, wherein the predetermined condition includes the processing circuitry configured to determine whether the current block is encoded in the IBC mode.

11. The video decoding apparatus according to claim 10, wherein in response to a determination that the current block is encoded in the IBC mode, the processing circuitry is configured to set the size of the index to a maximum number of IBC candidates, and in response to a determination that the current block is not encoded in the IBC mode, the processing circuitry is configured to set the size of the index to a maximum number of merge mode candidates.

12. The video decoding apparatus according to claim 9, wherein the predetermined condition includes a determination of whether a maximum number of merge mode candidates is greater than a maximum number of IBC candidates.

13. The video decoding apparatus of claim 12, wherein in response to a determination that the maximum number of merge mode candidates is greater than the maximum number of IBC candidates, the processing circuitry is further configured to set the size of the index to the maximum number of merge mode candidates, and in response to a determination that the maximum number of merge mode candidates is less than the maximum number of IBC candidates, the processing circuitry is further configured to set the size of the index to the maximum number of IBC candidates.

14. A video decoder apparatus for video decoding, comprising:
processing circuitry configured to:

- receive a coded video bitstream including a current picture,
- retrieve signaling data from the coded video bitstream for a current block,
- determine whether a maximum number of merge candidates is included in the retrieved signaling data for the current block, and

set a maximum number of intra block copy (IBC) candidates based on the determination of whether the maximum number of merge candidates is included in the signaling data for the current block.

15. The video decoder of claim 14, wherein in response to a determination that the maximum number of merge candidates is not included in the signaling data, the maximum number of IBC candidates is set to a value between 1 to 6.

16. The video decoder according to claim 15, wherein in response to a determination that the maximum number of merge candidates is included in the signaling data, the maximum number of IBC candidates is set to a value between 1 to the maximum number of merge candidates.

17. A non-transitory computer readable medium having instructions stored therein, which when executed by a processor in a video decoder causes the processor to execute a method comprising:

- receiving a coded video bitstream including a current picture;
- determining a predetermined condition associated with signaling data included in the coded video bitstream;
- determining, based on the predetermined condition, a size of an index included in the signaling data for a candidate list of vector predictors based on a number of merge mode candidates and a number of intra block copy (IBC) candidates;
- constructing the candidate list with vector predictors;
- retrieving a vector predictor from the candidate list in accordance with the index that has a value that does not exceed the determined size of the index; and
- decoding the current block in accordance with the retrieved vector predictor.

18. The non-transitory computer readable medium according to claim 17, wherein the predetermined condition includes determining whether the current block is encoded in the IBC mode.

19. A non-transitory computer readable medium having instructions stored therein, which when executed by a processor in a video decoder causes the processor to execute a method comprising:

- receiving a coded video bitstream including a current picture;
- retrieving signaling data from the coded video bitstream for a current block;
- determining whether a maximum number of merge candidates is included in the retrieved signaling data for the current block; and
- setting a maximum number of intra block copy (IBC) candidates based on the determination of whether the maximum number of merge candidates is included in the signaling data for the current block.

20. The non-transitory computer readable medium according to claim 19, wherein in response to a determination that the maximum number of merge candidates is not included in the signaling data, the maximum number of IBC candidates is set to a value between 1 to 6.