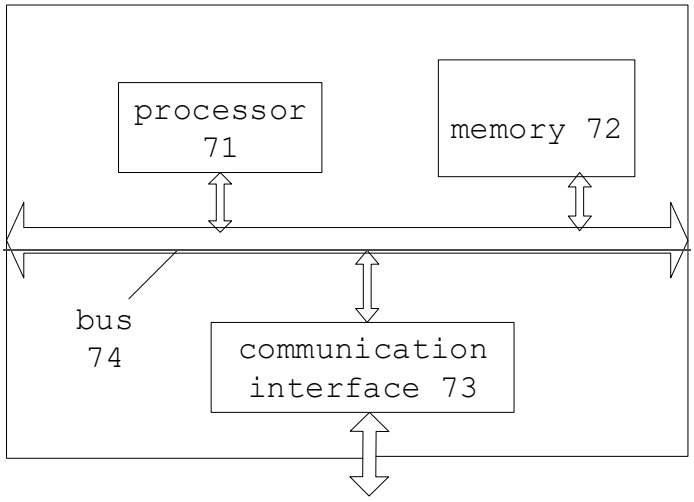

processor
71
memory 72
bus
74
communication
interface 73

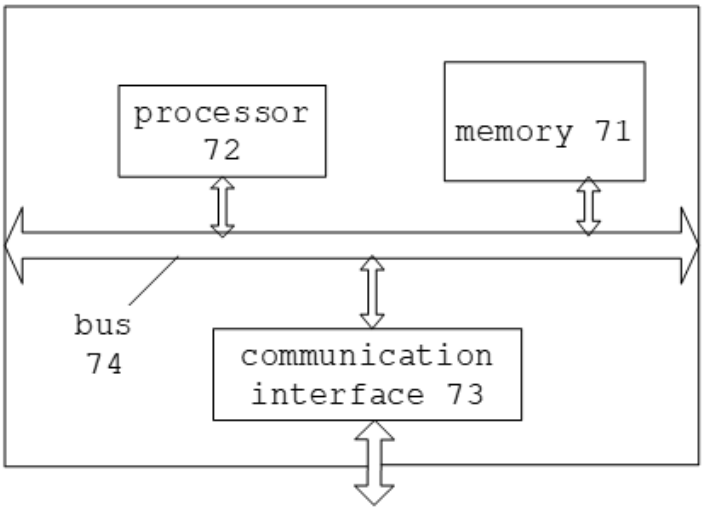

processor
72
memory 71
bus
74
communication
interface 73

Fig. 7