

Title Of Invention: APPARATUS AND METHODS FOR BIASING OF LOW NOISE AMPLIFIERS

ABSTRACT

Apparatus and methods for biasing of low noise amplifiers (LNAs) are provided herein. In certain embodiments, an LNA includes at least one transistor that amplifies a radio frequency (RF) input signal, and a bias circuit including a current bias circuit that generates a bias current based on a reference current and a voltage bias circuit that generates at least one input bias voltage for the at least one transistor based on the bias current. The current bias circuit includes a first bias transistor that receives the reference current, a second bias transistor that generates the bias current, and an amplifier that controls a first bias voltage of the first bias transistor to match a second bias voltage of the second bias transistor.

[Fig. 5A]