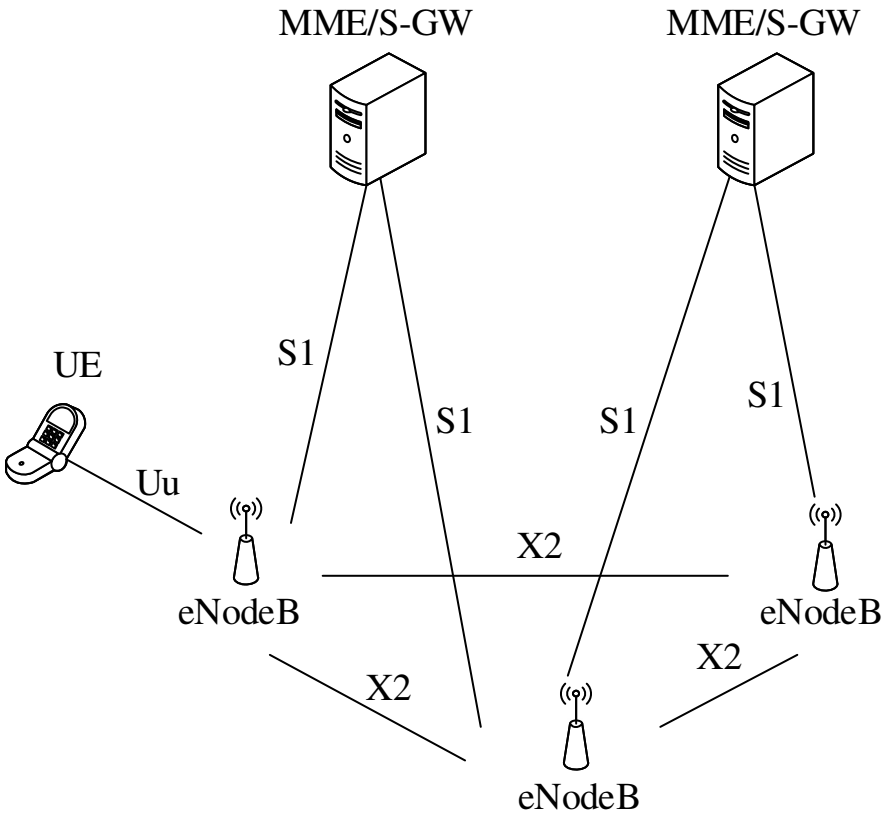

FIG. 1

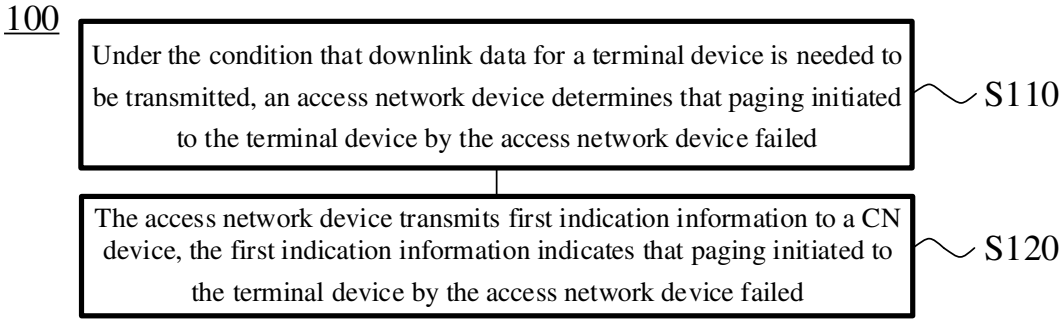

FIG. 2

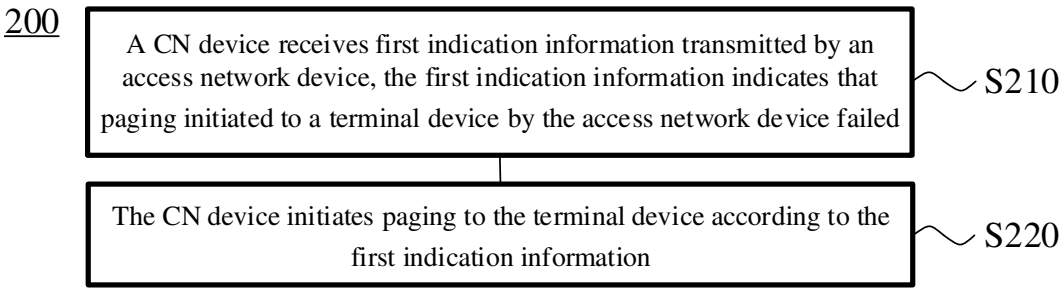

FIG. 3

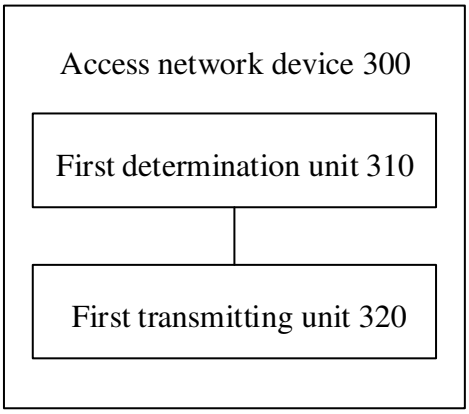

FIG. 4

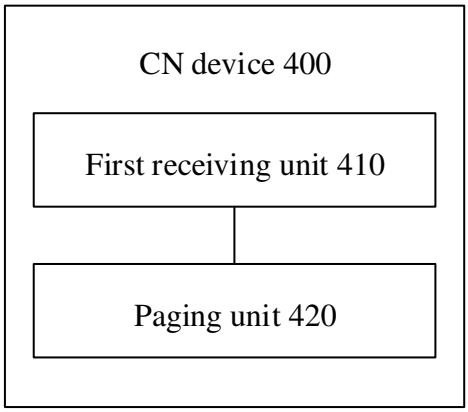

FIG. 5

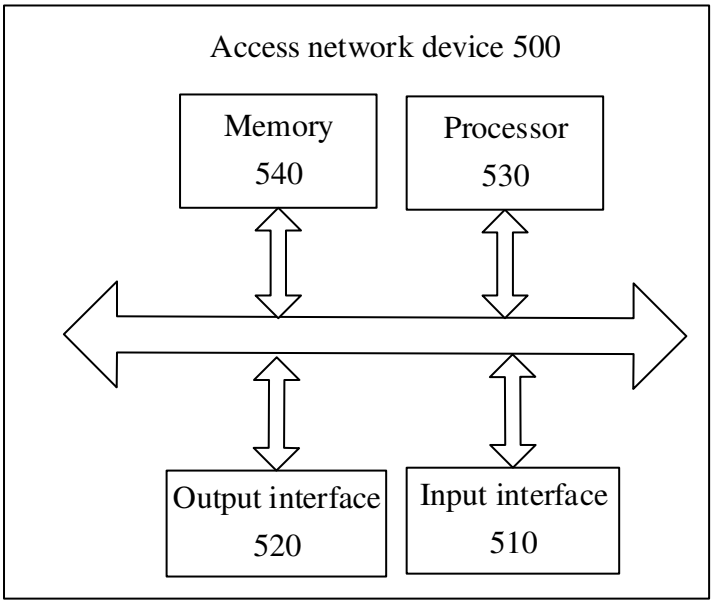

FIG. 6

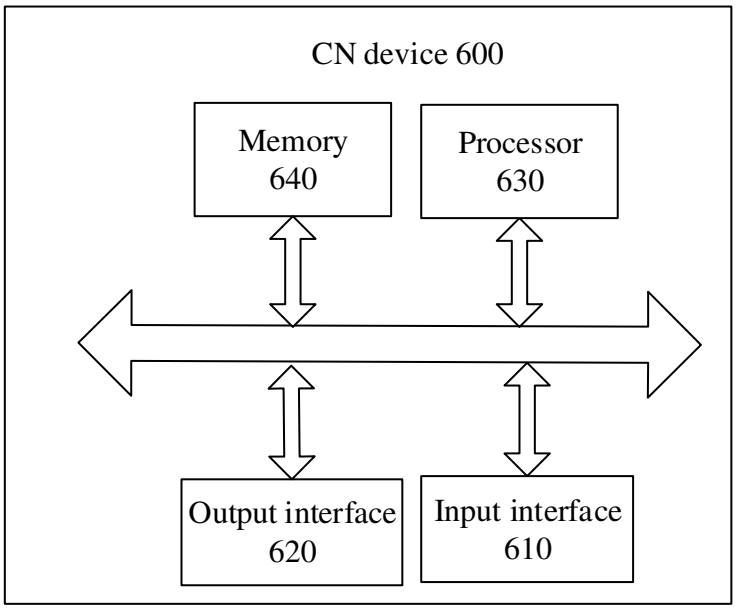
CN device 600
Memory
640
Processor
630
Output interface
620
Input interface
610

FIG. 7