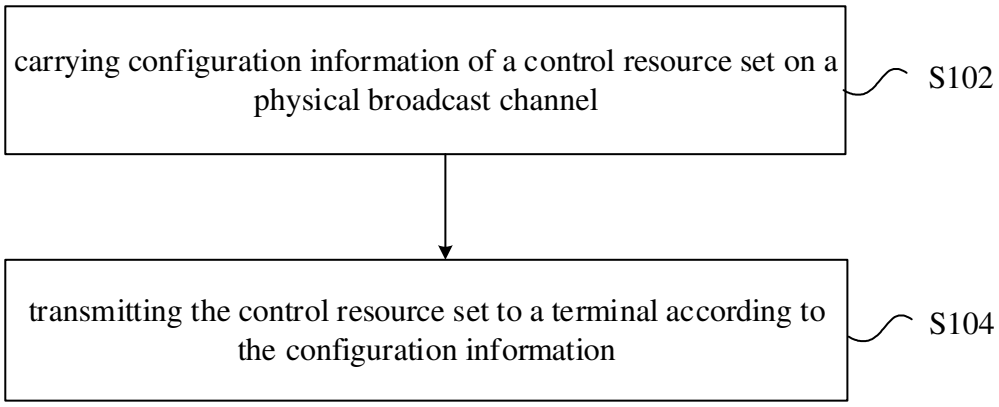


FIG. 1

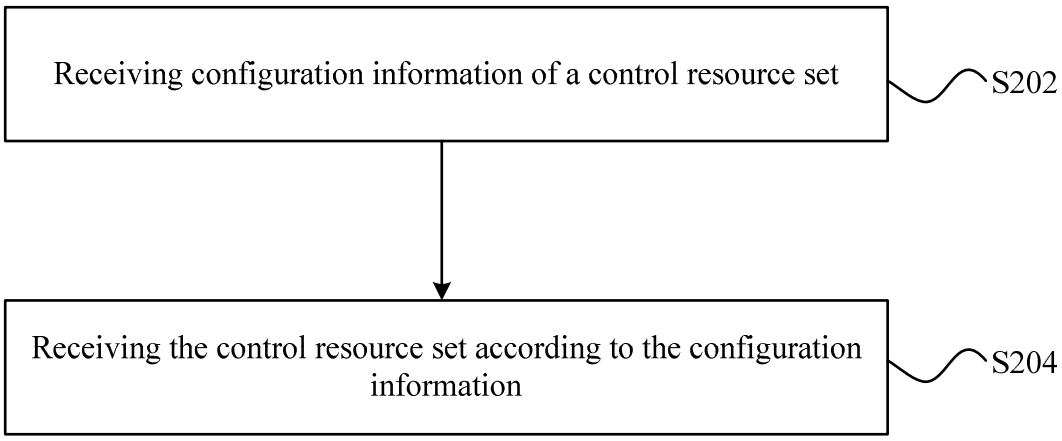


FIG. 2

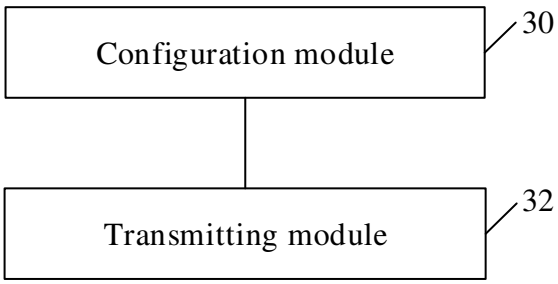


FIG. 3

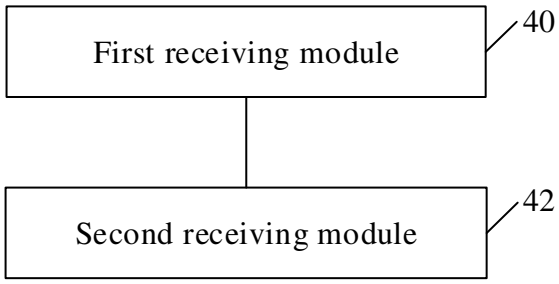


FIG. 4

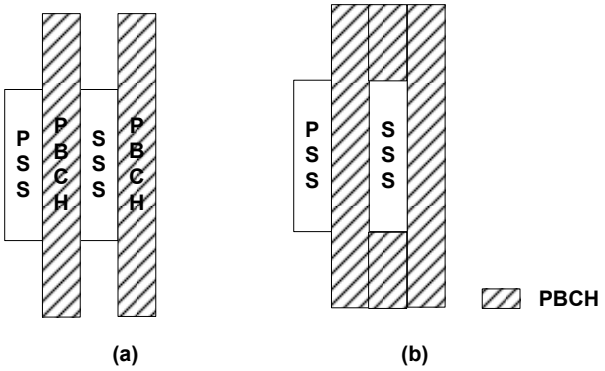


FIG. 5

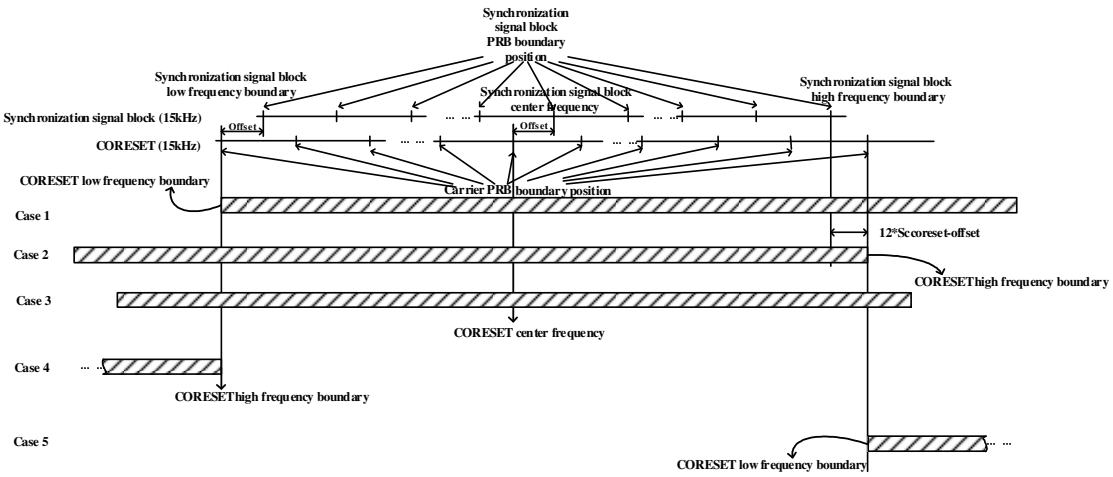


FIG. 6

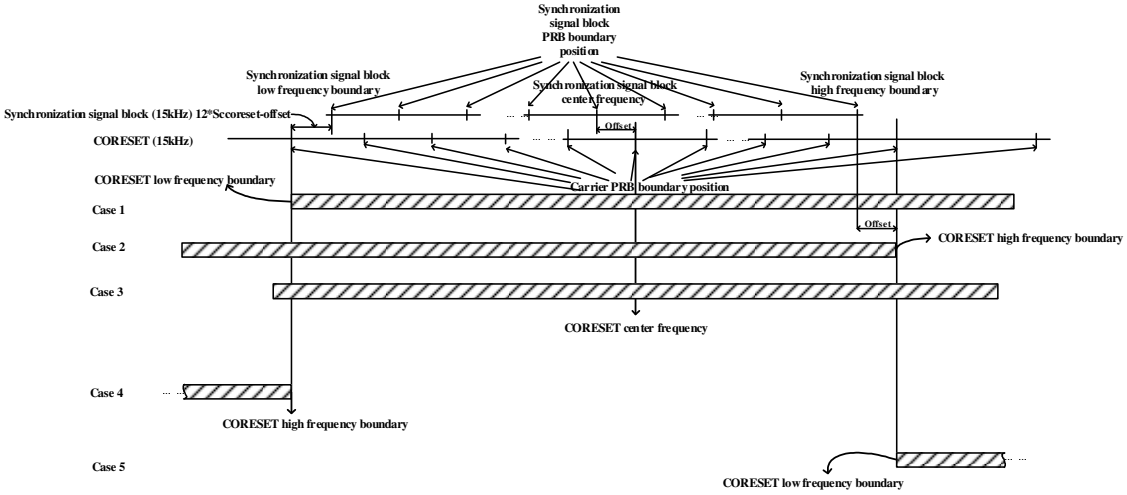

Synchronization signal block PRB boundary position
Synchronization signal block low frequency boundary
Synchronization signal block center frequency
Synchronization signal block high frequency boundary
Synchronization signal block (15kHz) 12*Sccoreset-offset
Offset
CORESET (15kHz)
CORESET low frequency boundary
Carrier PRB boundary position
Case 1
Offset
Case 2
CORESET high frequency boundary
Case 3
CORESET center frequency
Case 4
CORESET high frequency boundary
Case 5
CORESET low frequency boundary

FIG. 7

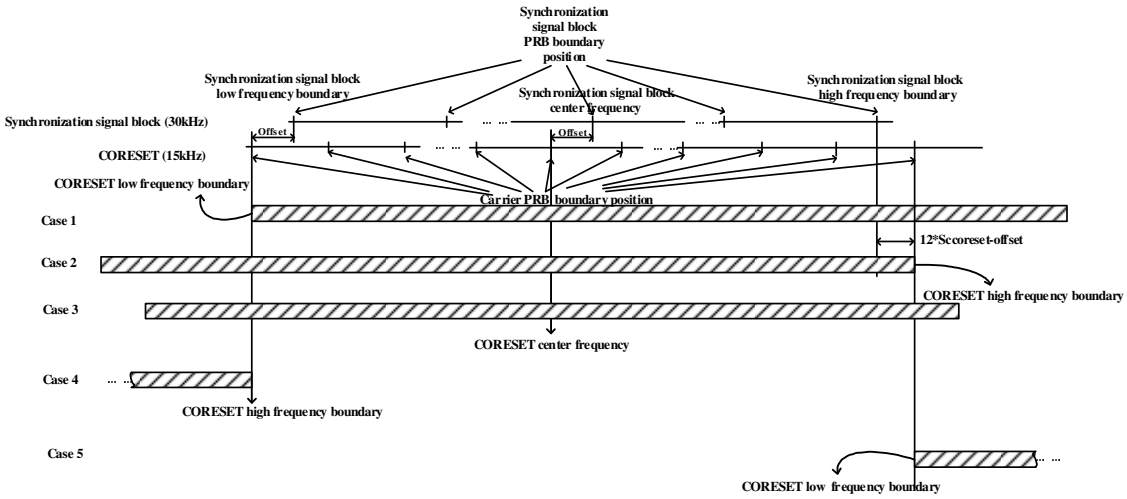

Synchronization signal block PRB boundary position
Synchronization signal block low frequency boundary
Synchronization signal block center frequency
Synchronization signal block high frequency boundary
Synchronization signal block (30kHz)
Offset
Offset
CORESET (15kHz)
CORESET low frequency boundary
Carrier PRB boundary position
Case 1
12*Sccoreset-offset
Case 2
CORESET high frequency boundary
Case 3
CORESET center frequency
Case 4
CORESET high frequency boundary
Case 5
CORESET low frequency boundary

FIG. 8

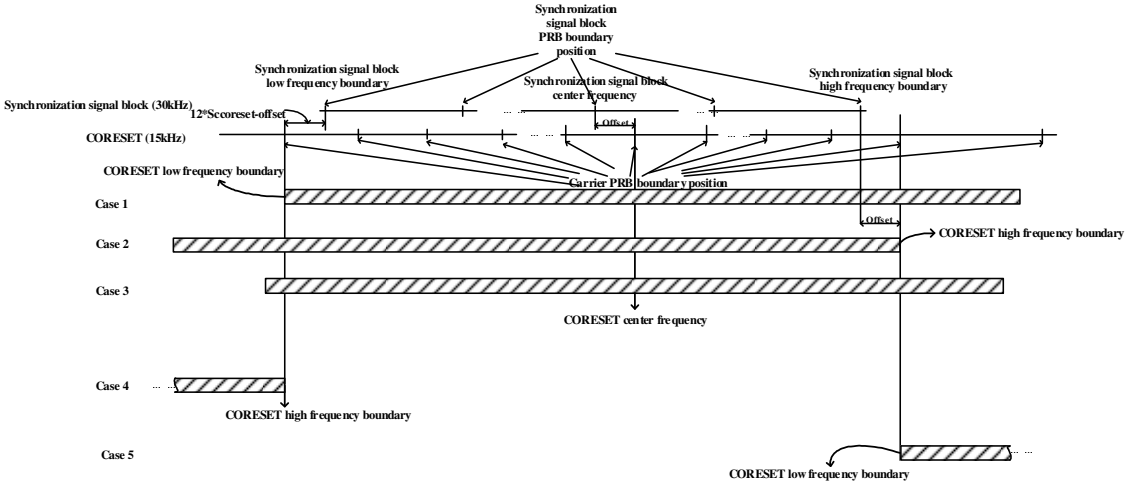

Synchronization signal block PRB boundary position
Synchronization signal block low frequency boundary
Synchronization signal block center frequency
Synchronization signal block high frequency boundary
Synchronization signal block (30kHz)
12*Sccoreset-offset
Offset
CORESET (15kHz)
CORESET low frequency boundary
Carrier PRB boundary position
Case 1
Offset
Case 2
CORESET high frequency boundary
Case 3
CORESET center frequency
Case 4
CORESET high frequency boundary
Case 5
CORESET low frequency boundary

FIG. 9

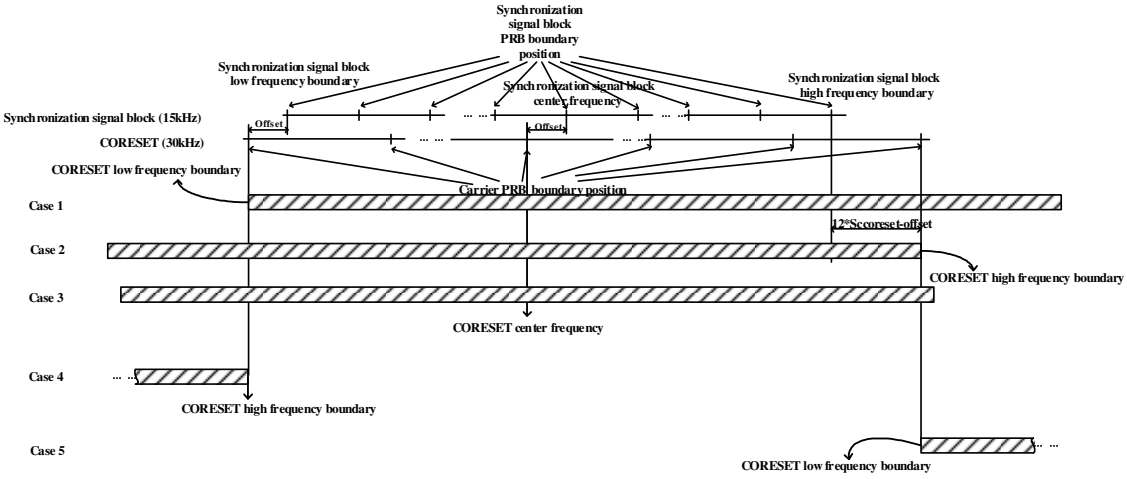

Synchronization signal block PRB boundary position
Synchronization signal block low frequency boundary
Synchronization signal block center frequency
Synchronization signal block high frequency boundary
Synchronization signal block (15kHz)
Offset
Offset
CORESET (30kHz)
CORESET low frequency boundary
Carrier PRB boundary position
Case 1
12*Sccoreset-offset
Case 2
CORESET high frequency boundary
Case 3
CORESET center frequency
Case 4
CORESET high frequency boundary
Case 5
CORESET low frequency boundary

FIG. 10

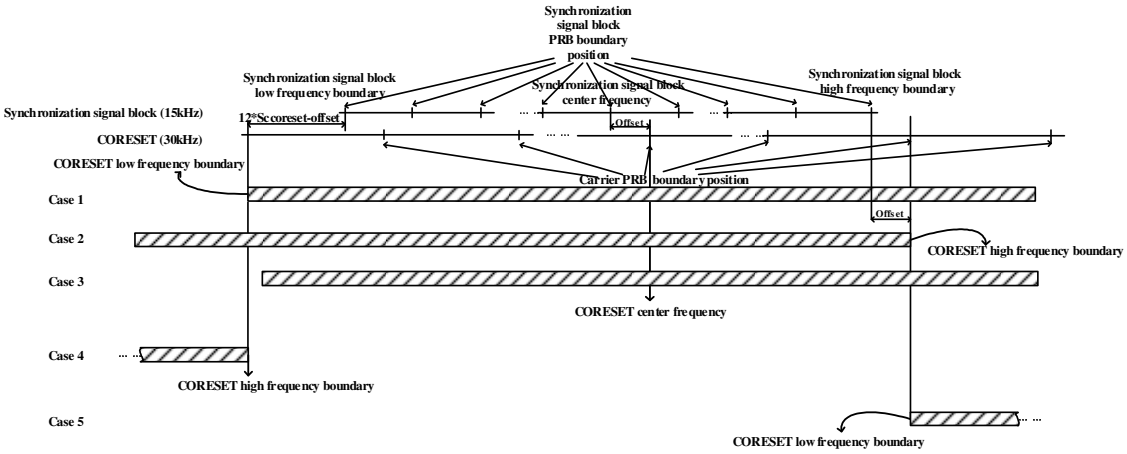

Synchronization signal block PRB boundary position
Synchronization signal block low frequency boundary
Synchronization signal block center frequency
Synchronization signal block high frequency boundary
Synchronization signal block (15kHz)
12*Sccoreset-offset
Offset
CORESET (30kHz)
CORESET low frequency boundary
Carrier PRB boundary position
Case 1
Offset
Case 2
CORESET high frequency boundary
Case 3
CORESET center frequency
Case 4
CORESET high frequency boundary
Case 5
CORESET low frequency boundary

FIG. 11

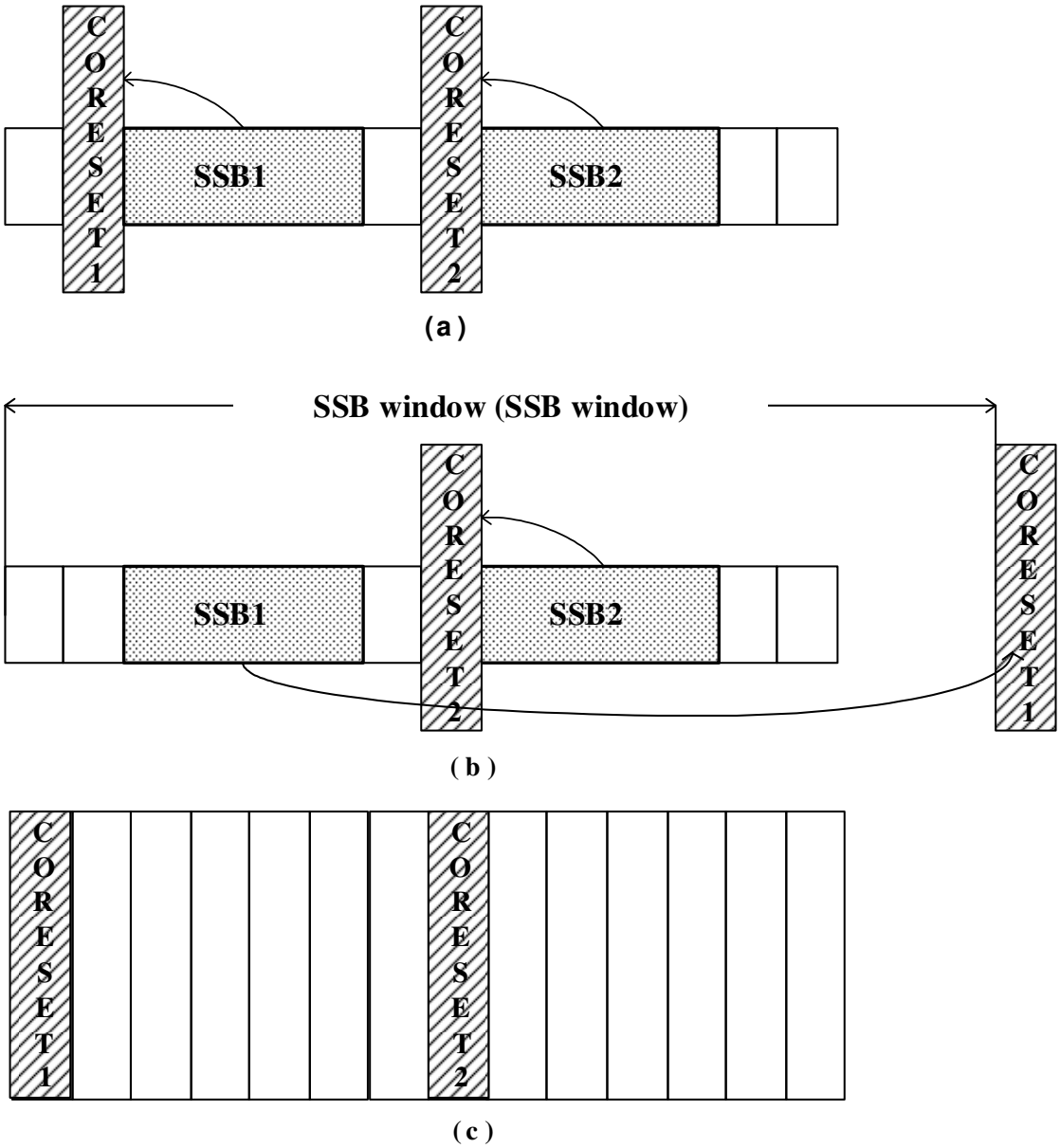

CORESET1
SSB1
CORESET2
SSB2
(a)
SSB window (SSB window)
CORESET2
SSB1
SSB2
CORESET1
(b)
CORESET1
CORESET2
(c)

FIG. 12

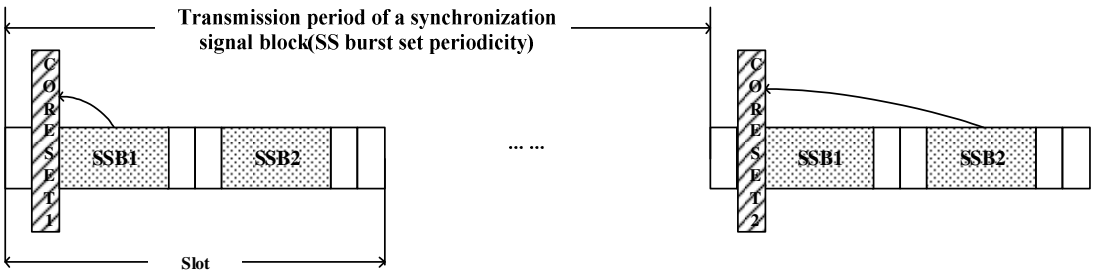

Transmission period of a synchronization signal block(SS burst set periodicity)
CORESET1
SSB1
SSB2
Slot
... ...
CORESET2
SSB1
SSB2

FIG. 13

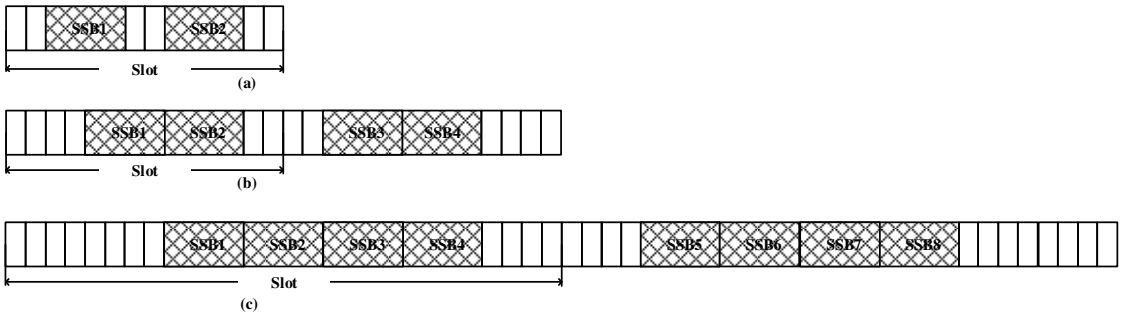

SSB1
SSB2
Slot
(a)
SSB1
SSB2
SSB3
SSB4
Slot
(b)
SSB1
SSB2
SSB3
SSB4
SSB5
SSB6
SSB7
SSB8
Slot
(c)

FIG. 14

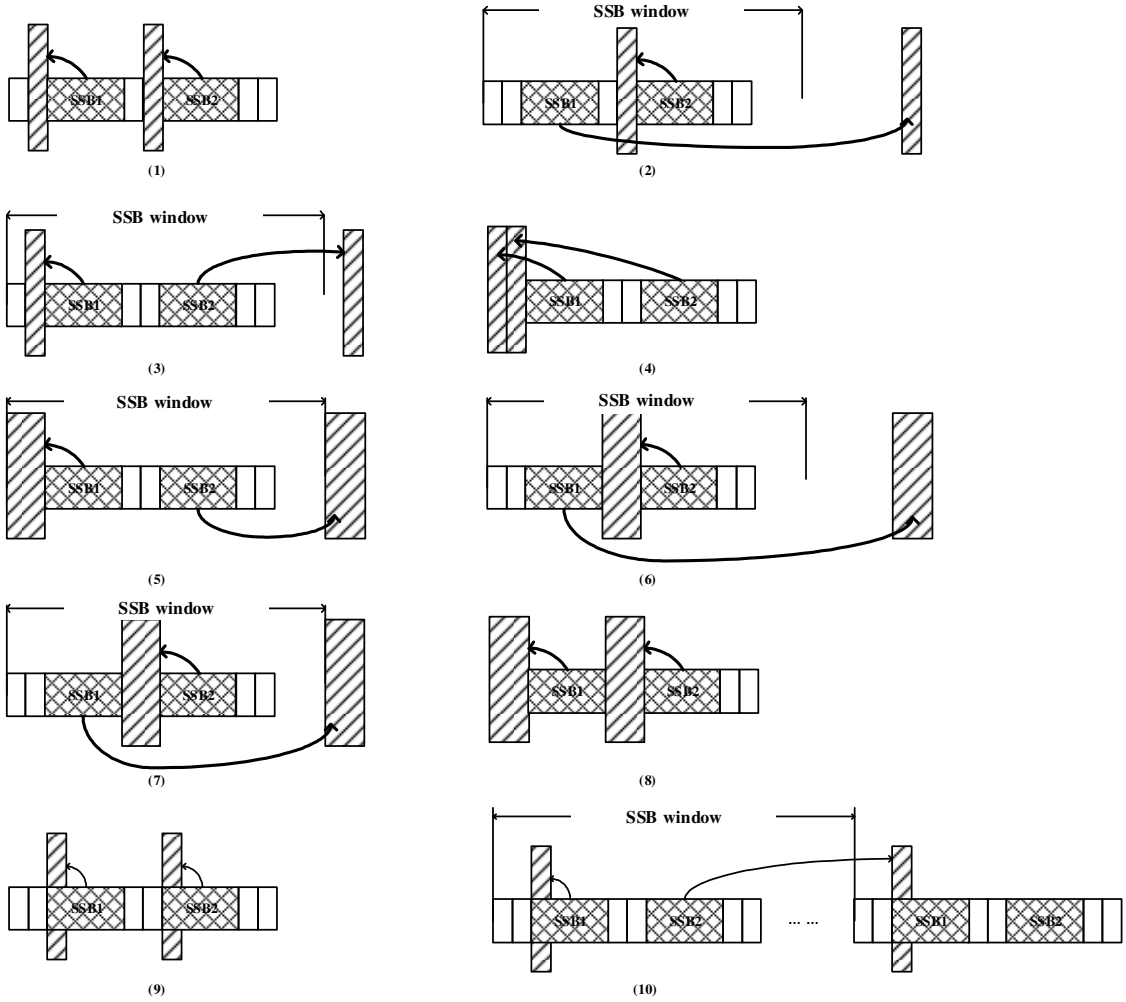


FIG. 15

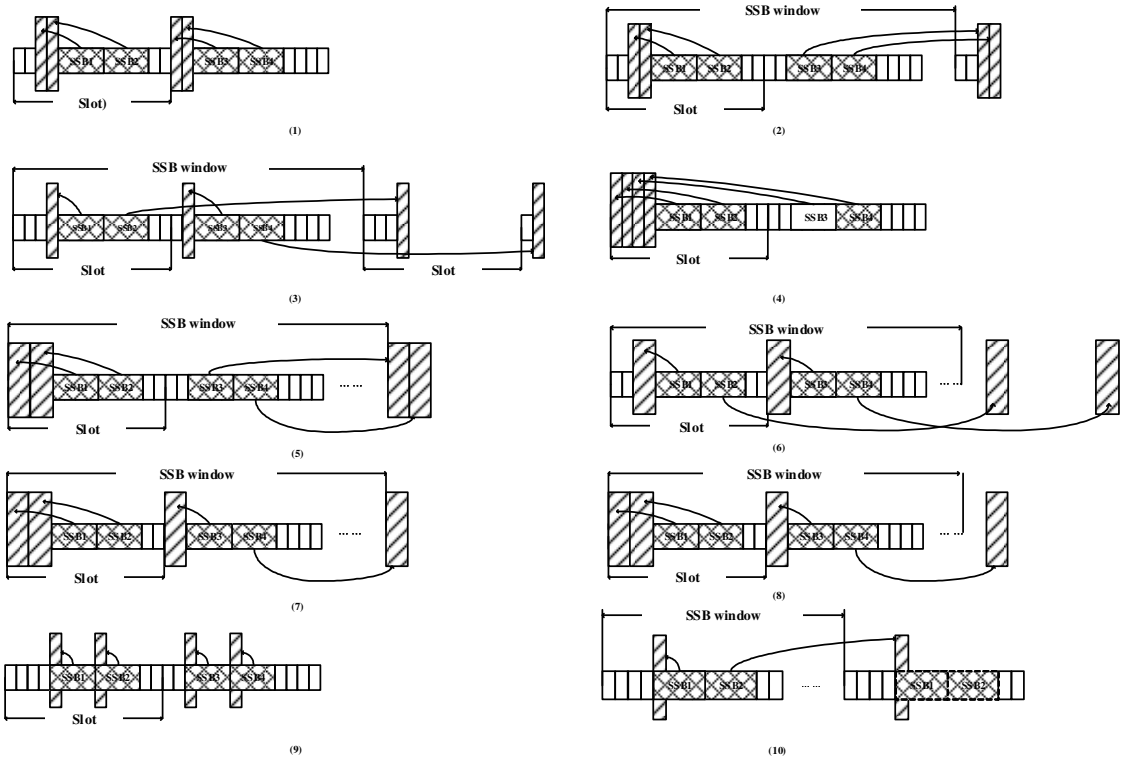

SSB1
SSB2
SSB3
SSB4
Slot)
(1)
SSB window
Slot
(2)
SSB window
Slot
Slot
(3)
Slot
(4)
SSB window
Slot
(5)
SSB window
Slot
(6)
SSB window
Slot
(7)
SSB window
Slot
(8)
Slot
(9)
SSB window
(10)

FIG. 16

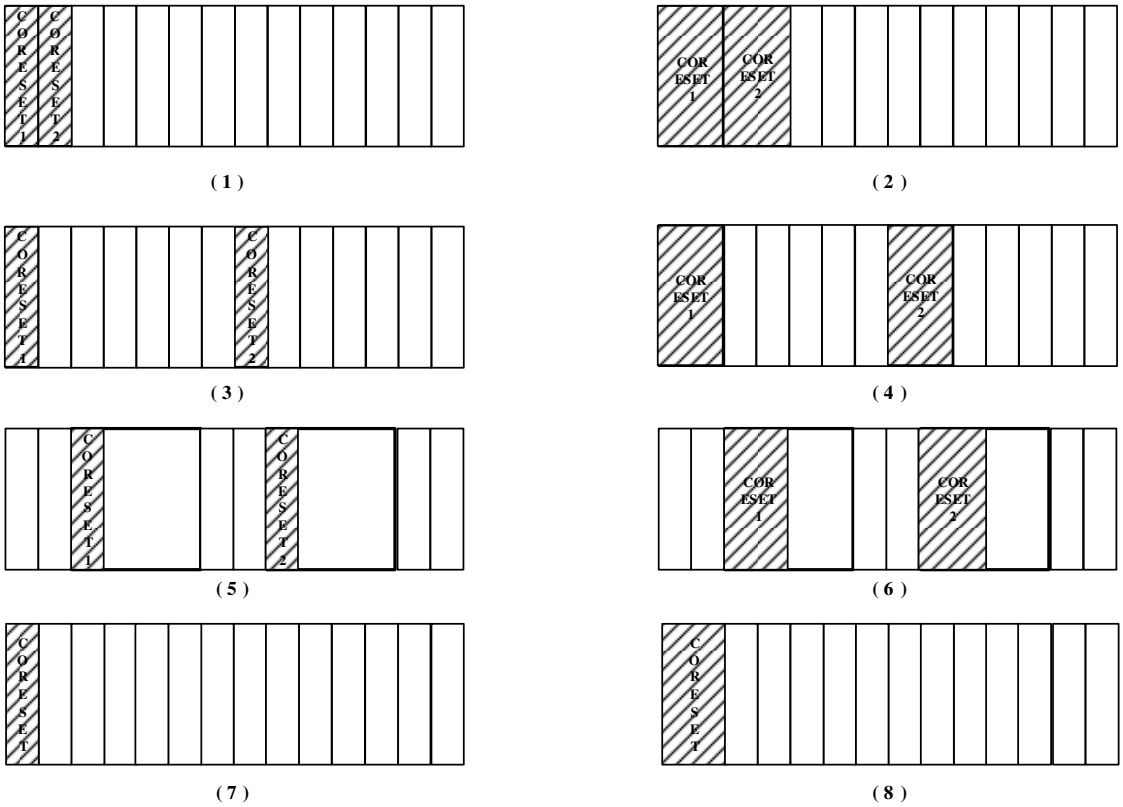
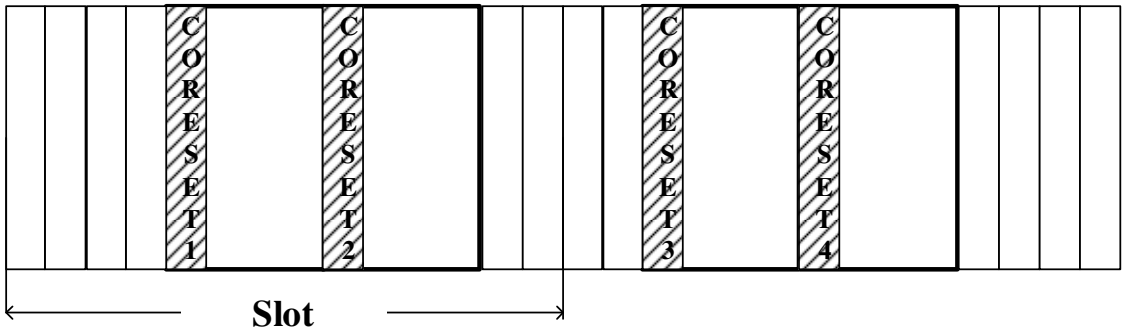
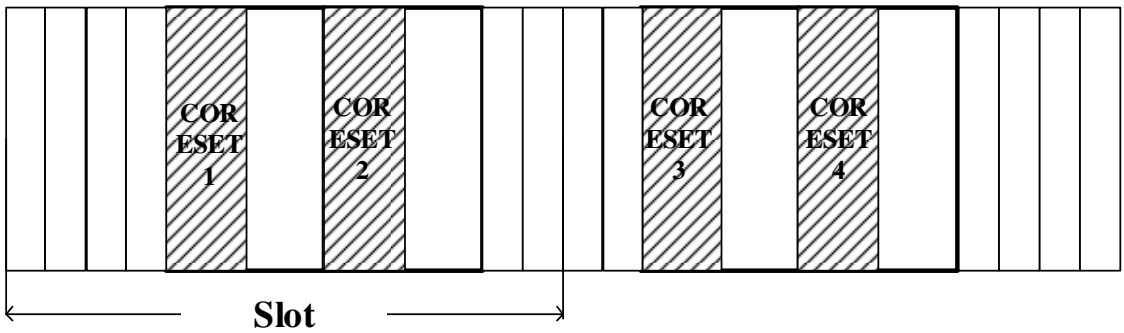

CORESET1
CORESET2
(1)
COR ESET 1
COR ESET 2
(2)
CORESET1
CORESET2
(3)
COR ESET 1
COR ESET 2
(4)
CORESET1
CORESET2
(5)
COR ESET 1
COR ESET 2
(6)
CORESET
(7)
CORESET
(8)

FIG. 17



(1)



(2)

FIG. 18

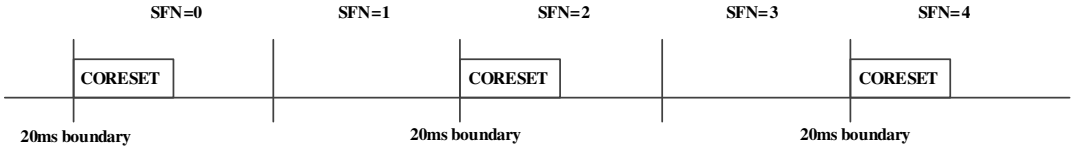


FIG. 19

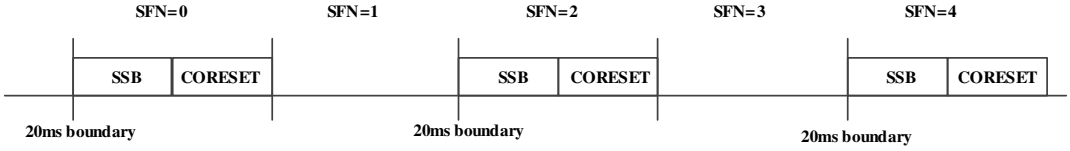


FIG. 20

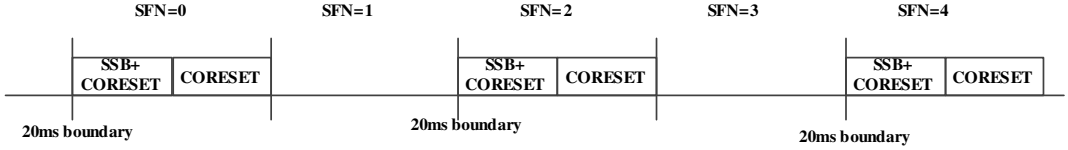


FIG. 21

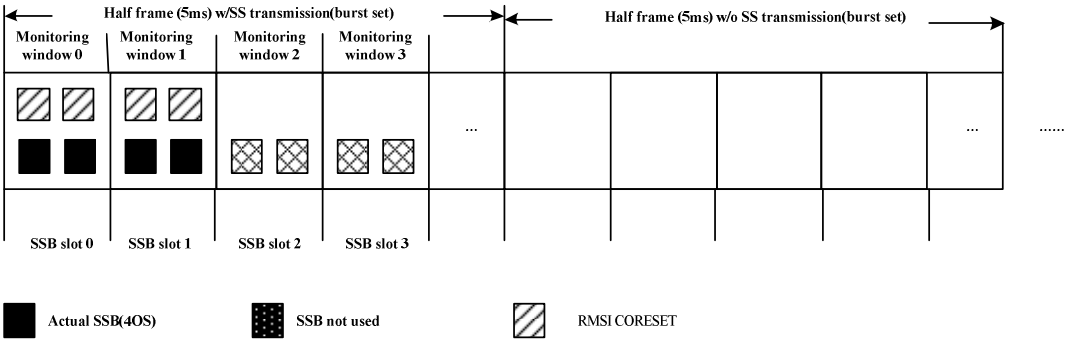


FIG. 22

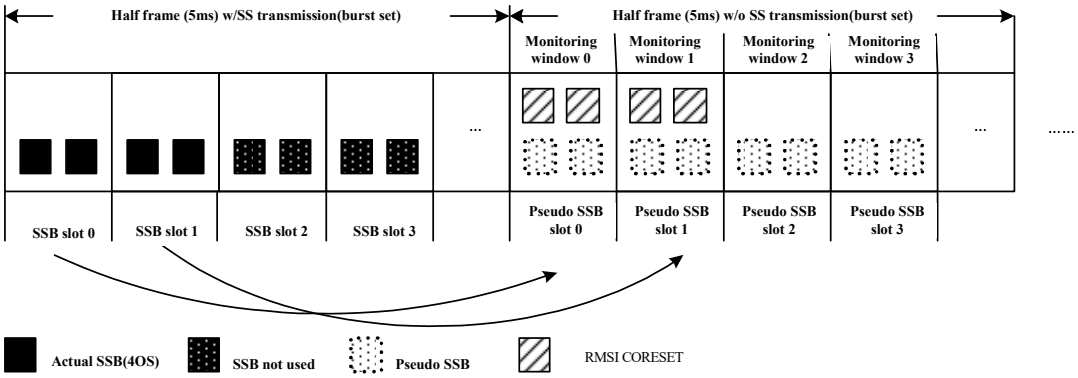


FIG. 23

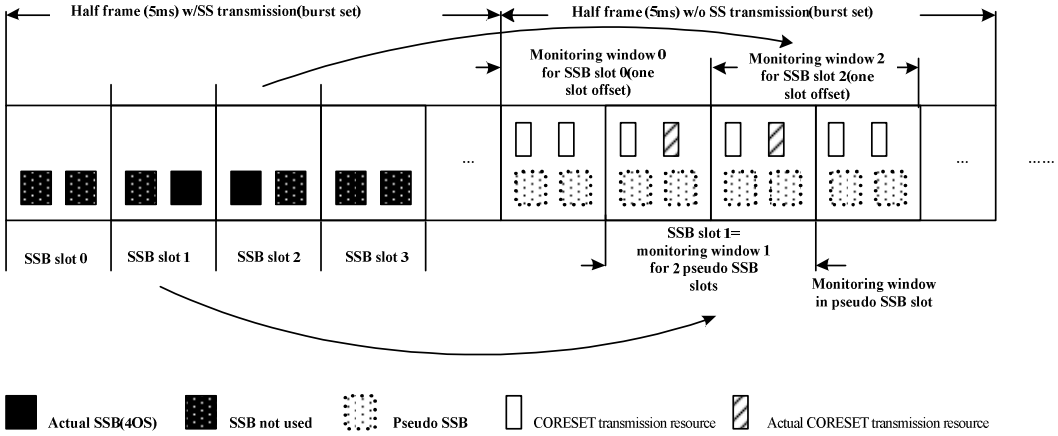

Half frame (5ms) w/SS transmission(burst set)
Half frame (5ms) w/o SS transmission(burst set)
Monitoring window 0 for SSB slot 0(one slot offset)
Monitoring window 2 for SSB slot 2(one slot offset)
SSB slot 0
SSB slot 1
SSB slot 2
SSB slot 3
SSB slot 1= monitoring window 1 for 2 pseudo SSB slots
Monitoring window in pseudo SSB slot
Actual SSB(4OS)
SSB not used
Pseudo SSB
CORESET transmission resource
Actual CORESET transmission resource

FIG. 24

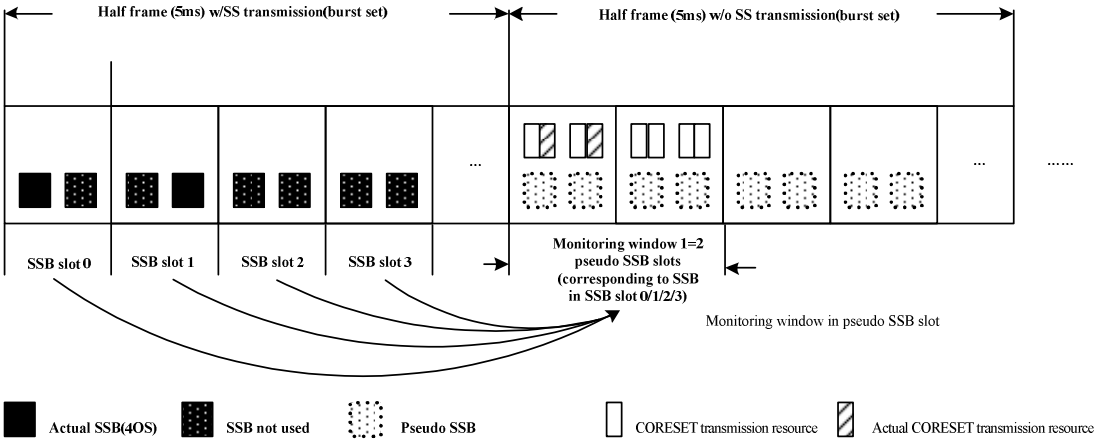

Half frame (5ms) w/SS transmission(burst set)
Half frame (5ms) w/o SS transmission(burst set)
SSB slot 0
SSB slot 1
SSB slot 2
SSB slot 3
Monitoring window 1=2 pseudo SSB slots (corresponding to SSB in SSB slot 0/1/2/3)
Monitoring window in pseudo SSB slot
Actual SSB(4OS)
SSB not used
Pseudo SSB
CORESET transmission resource
Actual CORESET transmission resource

FIG. 25

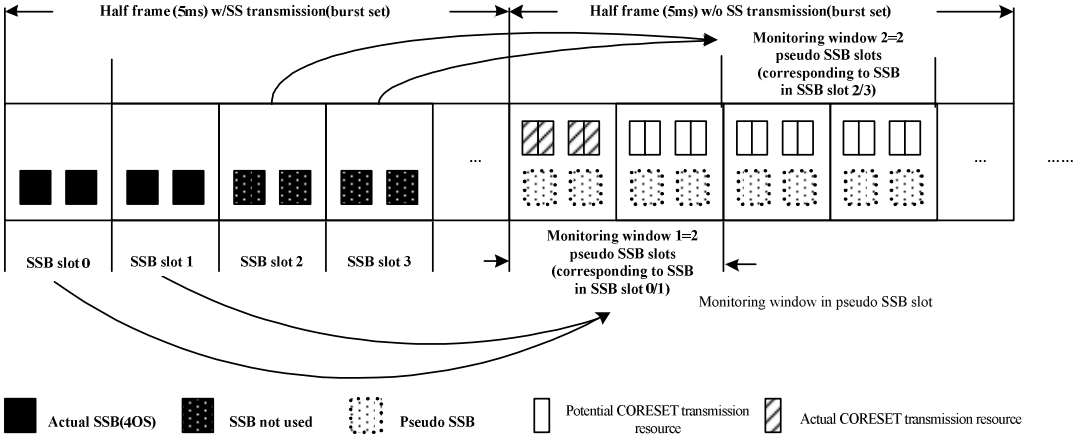

Half frame (5ms) w/SS transmission(burst set)
Half frame (5ms) w/o SS transmission(burst set)
Monitoring window 2=2 pseudo SSB slots (corresponding to SSB in SSB slot 2/3)
SSB slot 0
SSB slot 1
SSB slot 2
SSB slot 3
Monitoring window 1=2 pseudo SSB slots (corresponding to SSB in SSB slot 0/1)
Monitoring window in pseudo SSB slot
Actual SSB(4OS)
SSB not used
Pseudo SSB
Potential CORESET transmission resource
Actual CORESET transmission resource

FIG. 26