Abstract of the Disclosure:

When a frame is received from one of PON ports 12a to 12c of ONUs 1a to 1c, an OLT 3 compares VPIDs contained in Preamble/SFD regions A of the received Ethernet® frame with VPIDs assigned to LAN ports 31 and 32 of the OLT 3. Upon coincidence, a PON MAC process is started. When a frame is received from a PON port 33 of the OLT 3, each of the ONUs 1a to 1c compares a VPID contained in a Preamble/SFD region of the received Ethernet® frame with VPIDs assigned to the ONUs 1a to 1c. Upon coincidence, a PON MAC process is started.

(Fig. 1)