Abstract

In a liquid crystal display device (20), a liquid crystal panel (14) is divided into a display region (15a) including a plurality of pixels (10a) and a display region (15b) including a plurality of pixels (10b). A memory circuit (1) is provided to each pixel (10a) included in the display region (15a). The memory circuit (1) is capable of storing a data signal supplied from a signal line (3). As such, writing the data signal, which has been stored in the memory circuit (1), into a pixel electrode (2) allows an image to be displayed in accordance with the data signal. That is, in the display region (15a), it is possible to display an image without supplying image data from the outside via a scanning line (4) and the signal line (3). This allows a reduction in electric power consumption.