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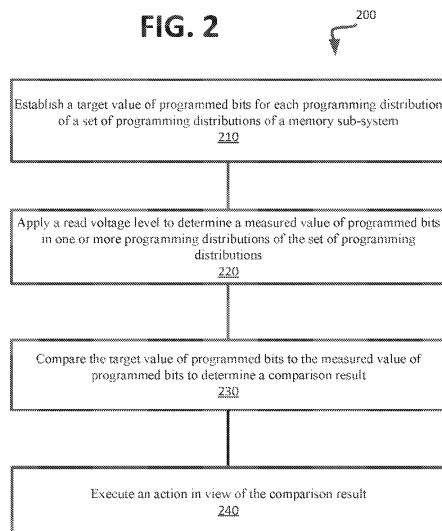
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- *as to applicant's entitlement to apply for and be granted a
patent (Rule 4.17(ii))*
- *as to the applicant's entitlement to claim the priority of the
earlier application (Rule 4.17(iii))*

(54) Title: ADJUSTING READ VOLTAGE LEVELS BASED ON A PROGRAMMED BIT COUNT IN A MEMORY SUB-SYSTEM

FIG. 2



(57) Abstract: A target value of programmed bits is established for each programming distribution of a set of programming distributions of a memory sub-system. A read voltage level is applied to determine a measured value of programmed bits in one or more programming distributions of the set of programming distributions. The target value of programmed bits is compared to the measured value of programmed bits to determine a comparison result and an action is executed in view of the comparison result.

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