



162162



G00001

DISC DATA REPRODUCING APPARATUS AND SIGNAL PROCESSING CIRCUIT

ABSTRACT

An apparatus, a signal processing circuit (7) and a method for reproducing data stored on a disc (1) to prevent a buffer memory (22) from experiencing overflow/underflow. Writing information data read from the disc (1) to the buffer memory (22) is performed in response to a reproduction stage clock (VCOCK), and reading from the buffer memory (22) is in response to a signal processing stage clock (XCK, PMCK). The signal processing stage clock (XCK, PMCK) for reading information data may be derived from the reproduction stage clock (VCOCK).

(Fig. 2)