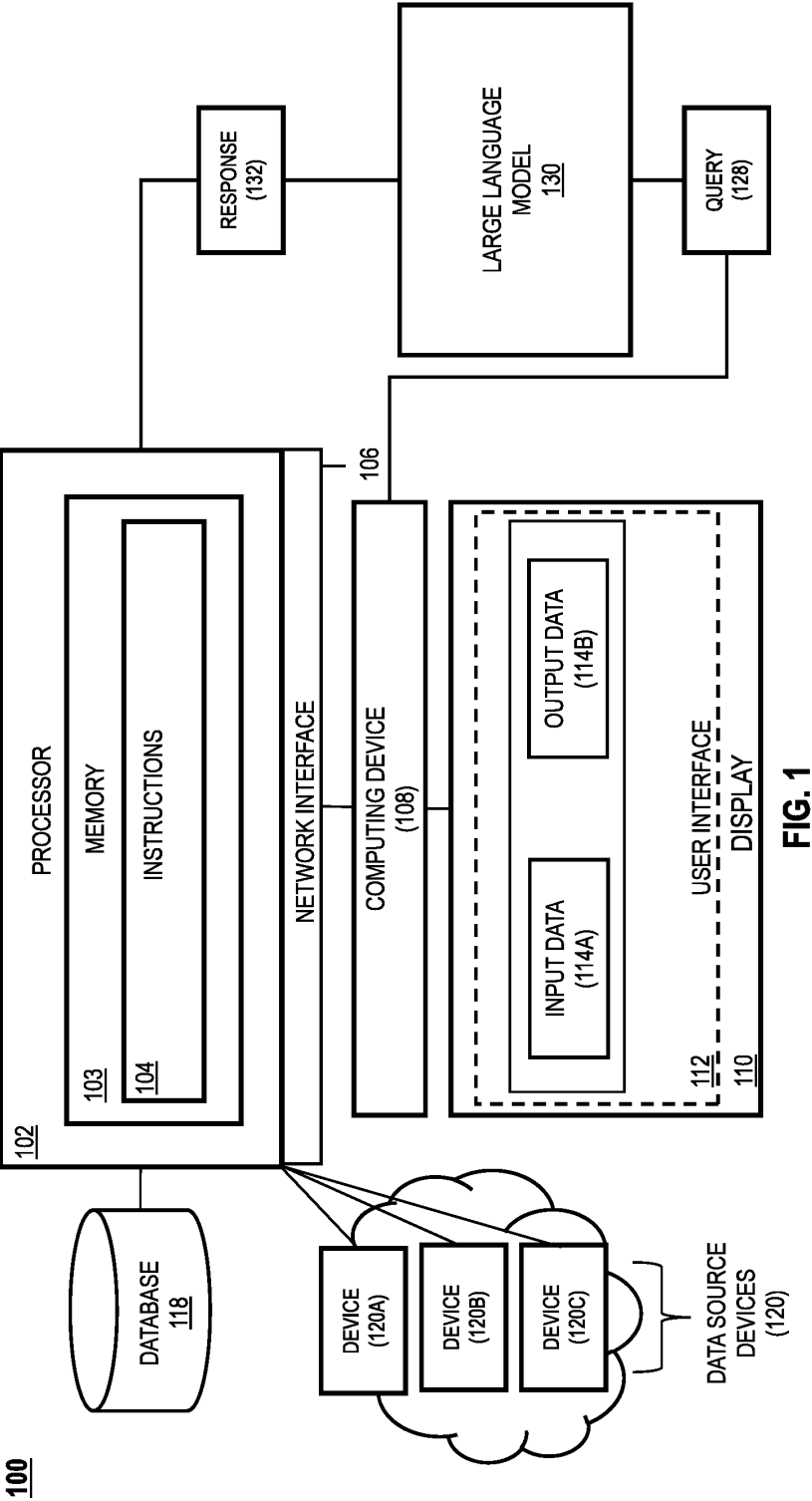




200

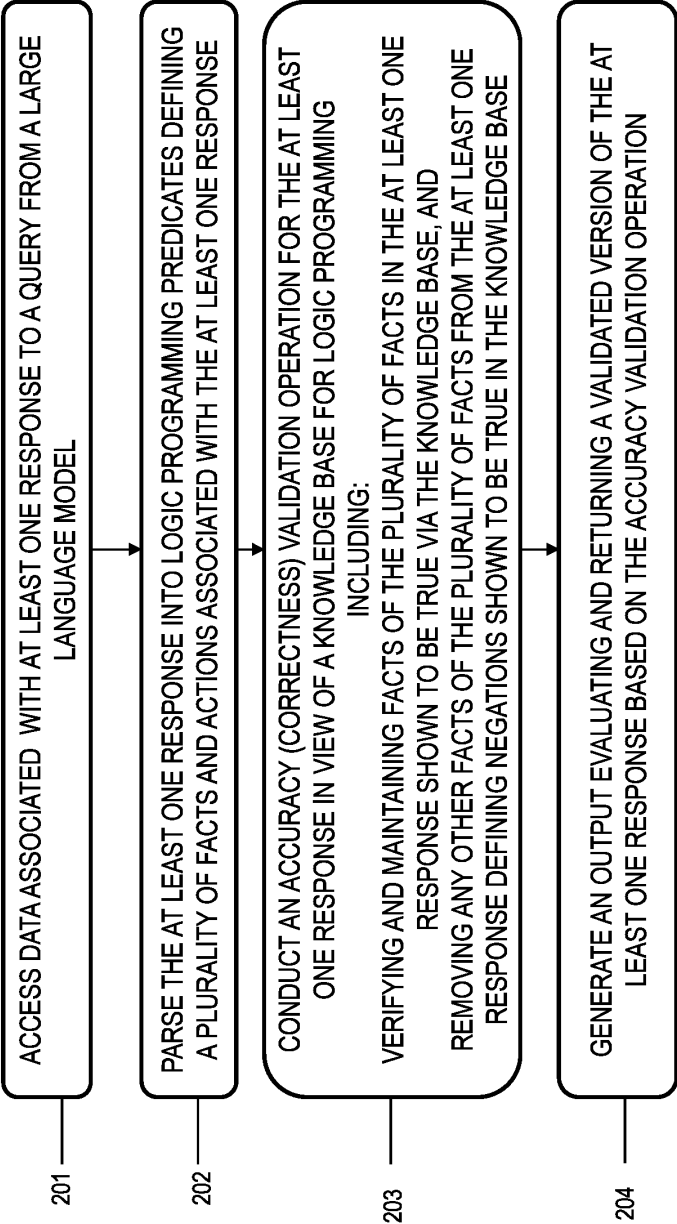


FIG. 2A

250

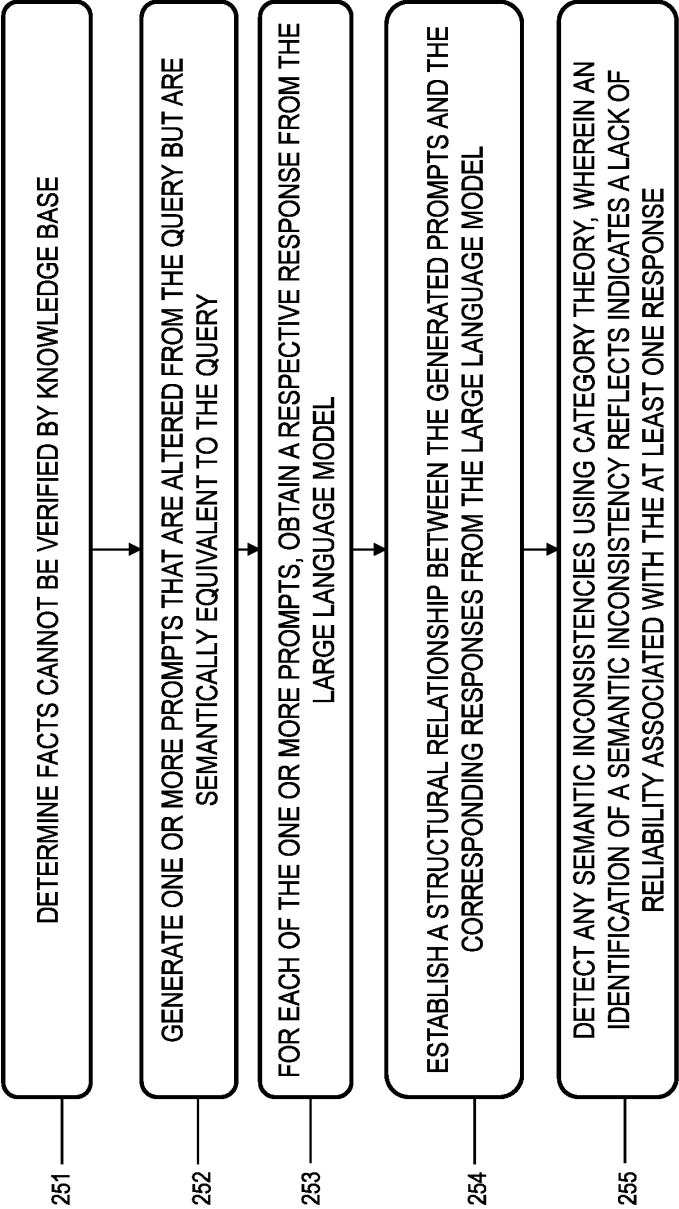
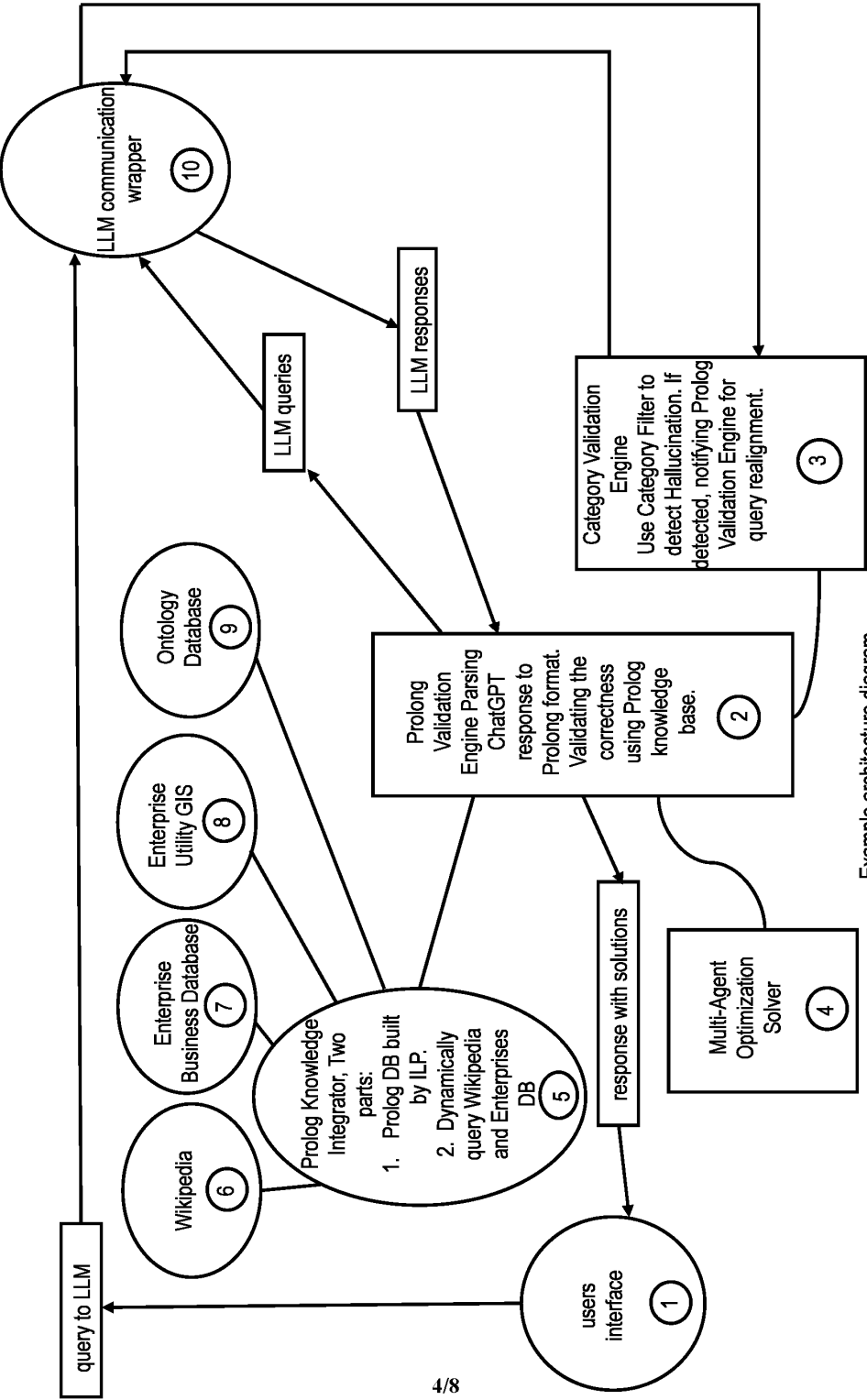
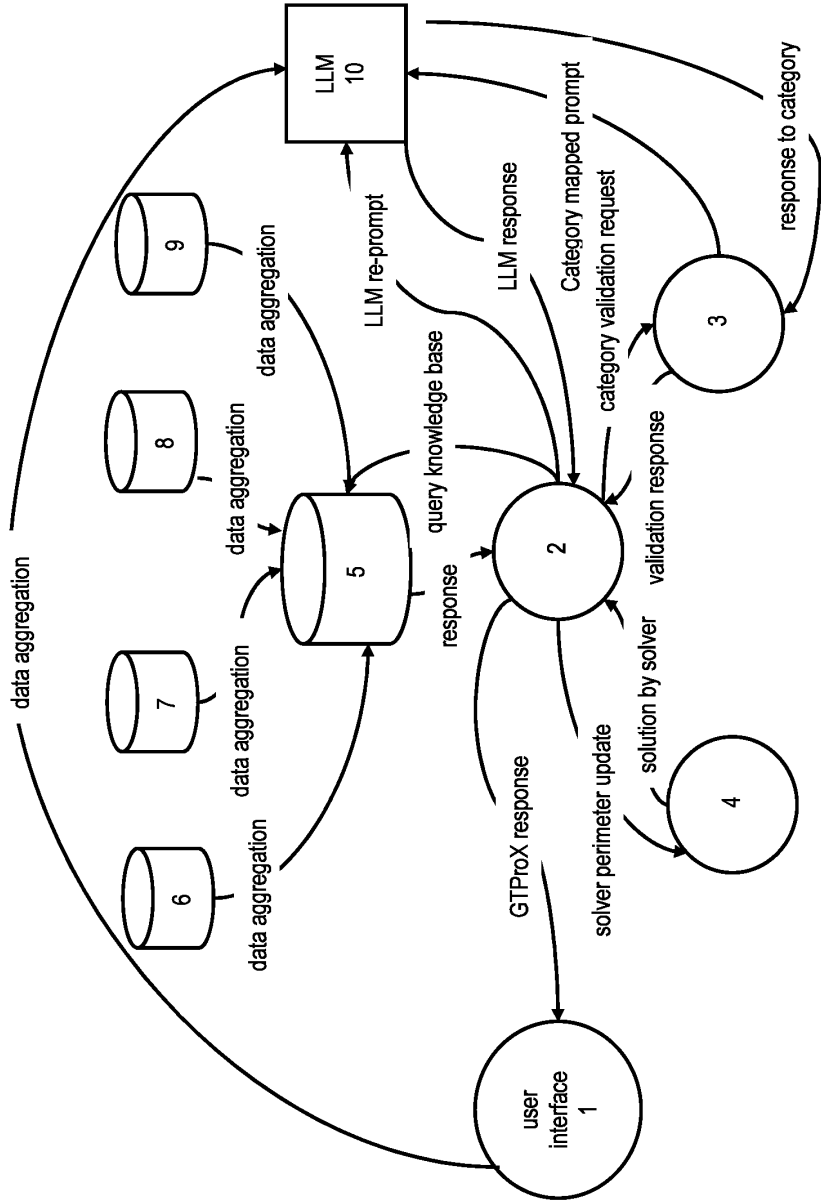


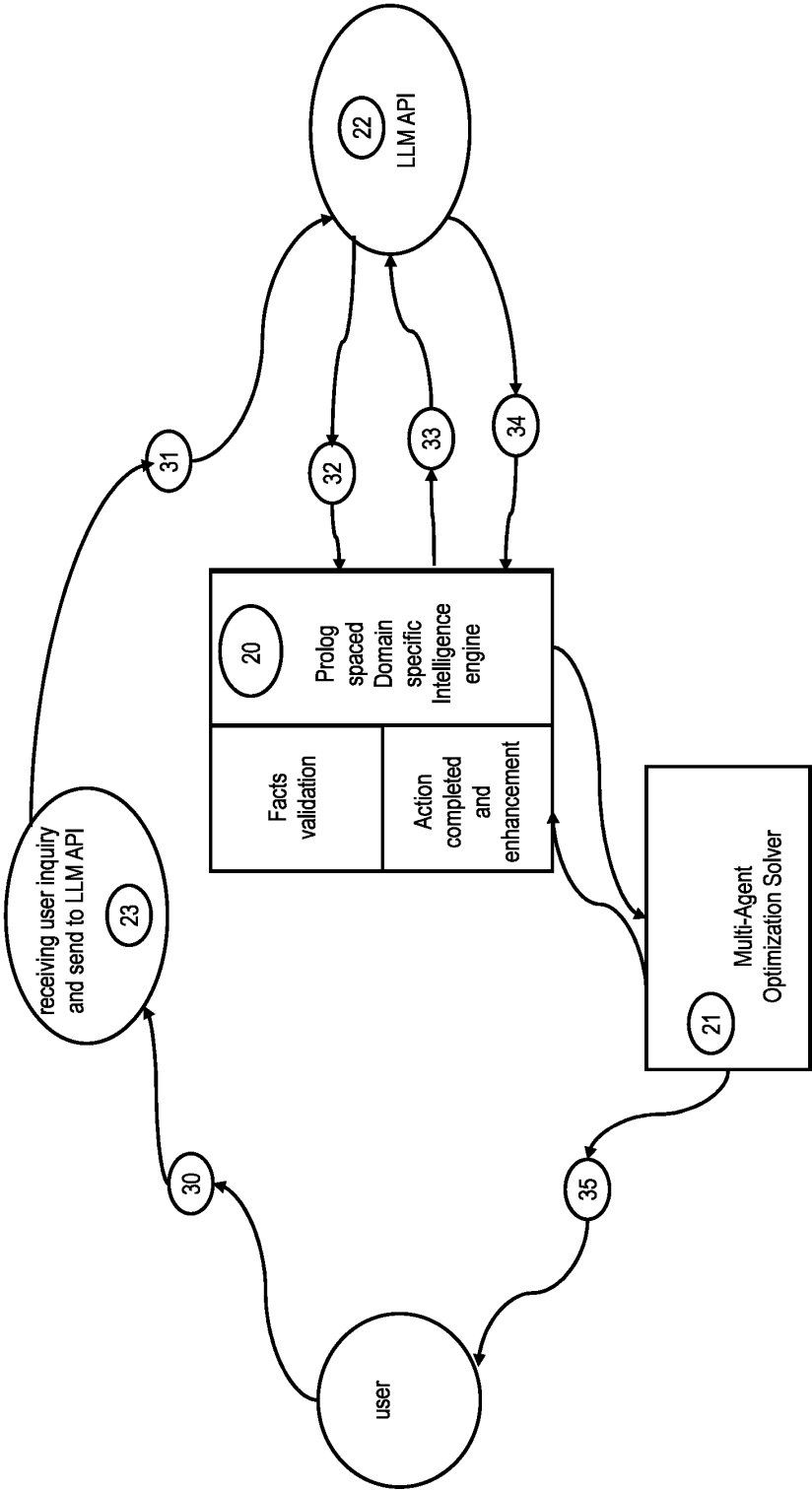
FIG. 2B



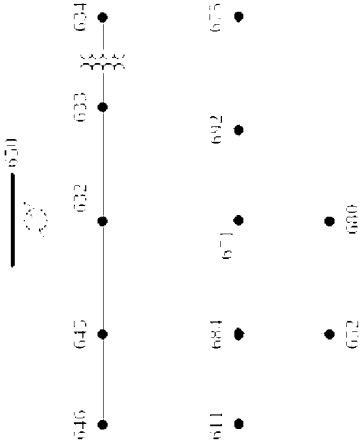
Example architecture diagram
FIG. 3



Data flow
FIG. 4



Validator and Multi-Agent Solver
FIG. 5



IEEE 13 Node Test Feeder

FIG. 6

1200

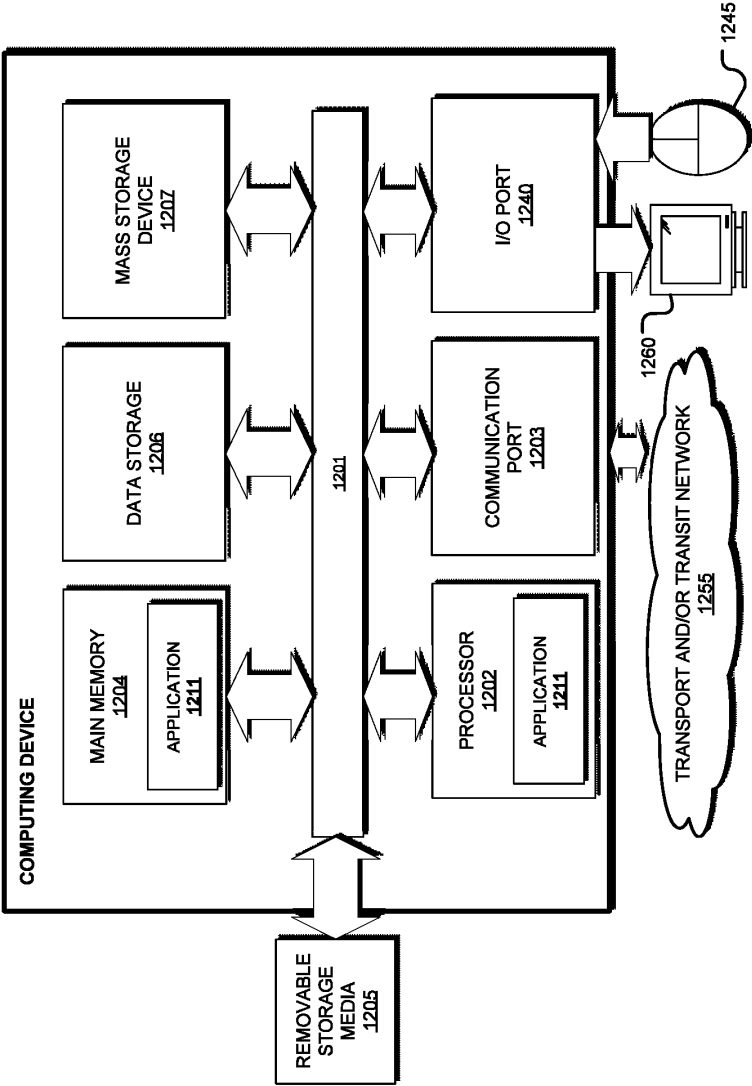


FIG. 7