

ABSTRACT:

Circuit arrangement for supplying an antenna signal.

A circuit arrangement for supplying an antenna signal to a receiver arrangement of a video recorder and/or to a television receiver, connected to said video recorder, which circuit arrangement comprises a switching device by which, in the blocked state, the antenna signal can be applied only to the receiver arrangement of the video recorder and from this video recorder to the television receiver, and by which, in the
5 conducting state, the antenna signal can be applied directly to the television receiver via this video recorder, as well as a switching device therefor.

To avoid unwanted low impedances at the input and the output, it is proposed that

- 10 - the switching device comprises a series arrangement of, successively, a first capacitive switching element having a constant capacitance, at least one pair of second capacitive switching elements and a third capacitive switching element having a constant capacitance, the second capacitive switching elements within the pairs being arranged in series, the first capacitive switching element being
15 connected to an input for applying the antenna signal to the switching device, and the third capacitive switching element being connected to an output for supplying the antenna signal from the switching device,
- for changing the switching device to the blocked state, a low potential can be supplied to the connections of the second switching elements within the pairs and a high potential can be supplied to the interconnections between the pairs or
20 to the first and the third capacitive switching element,
- in at least one of the pairs, at least one of the second capacitive switching elements has a constant capacitance and the other second capacitive switching elements are constituted as capacitance diodes having anodes which are
25 connected to the connections within the pairs and cathodes which are connected to the interconnections between the pairs,
- one of the second capacitive switching elements constituted as capacitance diode is connected to the first and the third capacitive switching element, respectively.

