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MEMORY DEFRAGMENTATION IN CHIPCARDS

ABSTRACT

There is provided two sets of descriptors having each at least three descriptors and each set is used in an alternating manner for defining the location of source and target of the copy operations which are to be performed during the defragmentation procedure. The defragmentation procedure is performed as sequence of copy operations on copy chunks, (10,12,13,14,16,18,20) i.e, a certain number of sequentially arranged bytes to be copied being part of a valid data block (8) to be copied. In each of said copy operations in said sequence the values which are assigned to said descriptors change. During the whole sequence of copy operations, one of the two sets of descriptors holds information which is usable for restoring the contents of a copy chunk in case of a power break during a copy operation. Thus, defragmenting is a safe procedure, and data integrity is assured.

FIG 1