ABSTRACT

A soft repair control circuit includes a lock control circuit, an undo control circuit, a soft repair address latch circuit, and a soft repair address matching circuit. The lock control circuit locks an output latch control signal into a first level when a lock enable signal is at an active level. The undo control circuit maintains an output unchanged when the latch control signal is locked into the first level and generates an undo flag signal at an active level according to an undo enable signal at an active level when the latch control signal is an inverted delayed signal of a soft repair pulse signal. The soft repair address latch circuit maintains an output unchanged when the latch control signal is locked into the first level. The soft repair address matching circuit locks an output soft repair matching signal into an inactive level when the undo flag signal is at an active level.

[FIG. 1]