FIG. 1

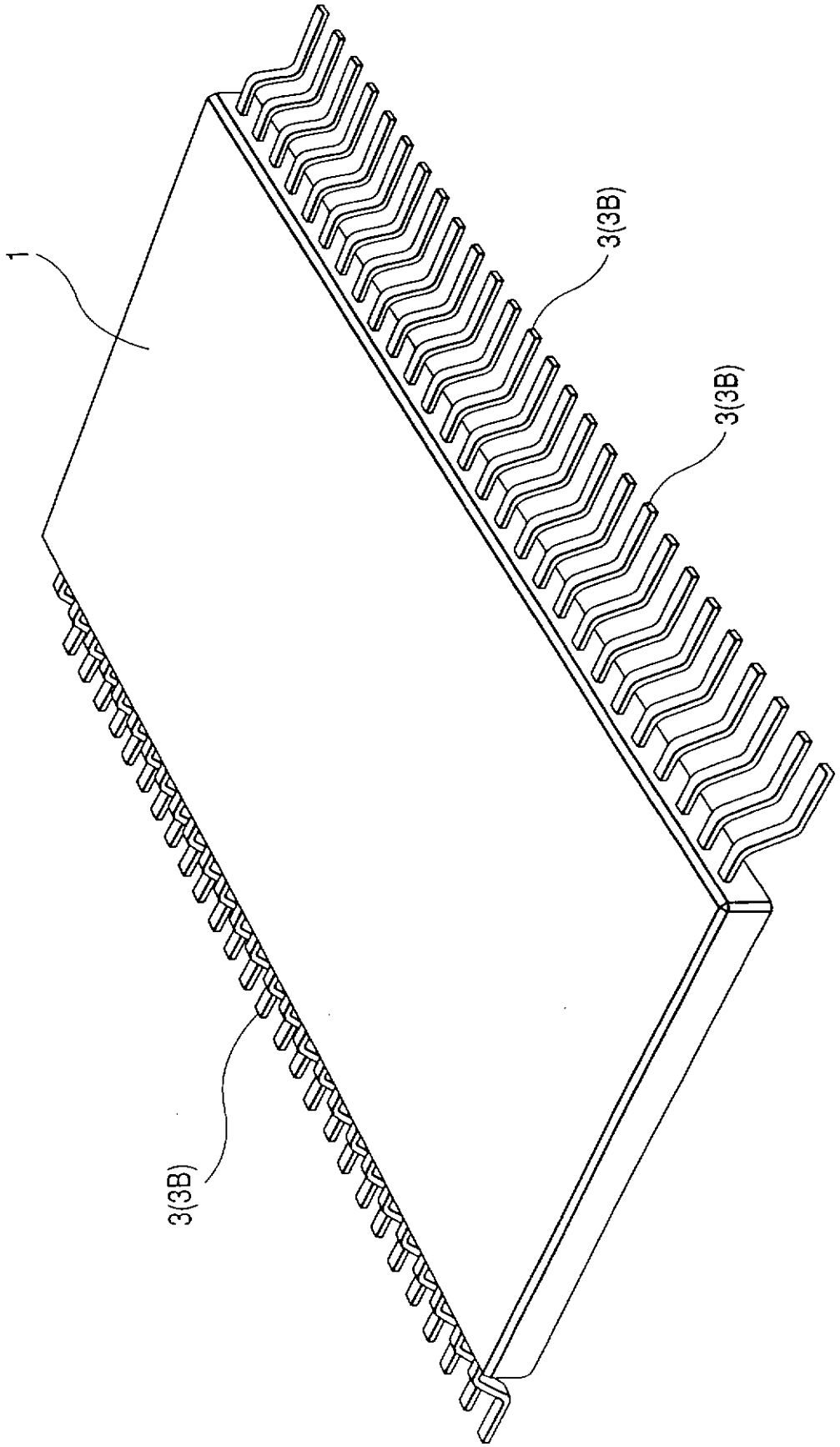


FIG. 2

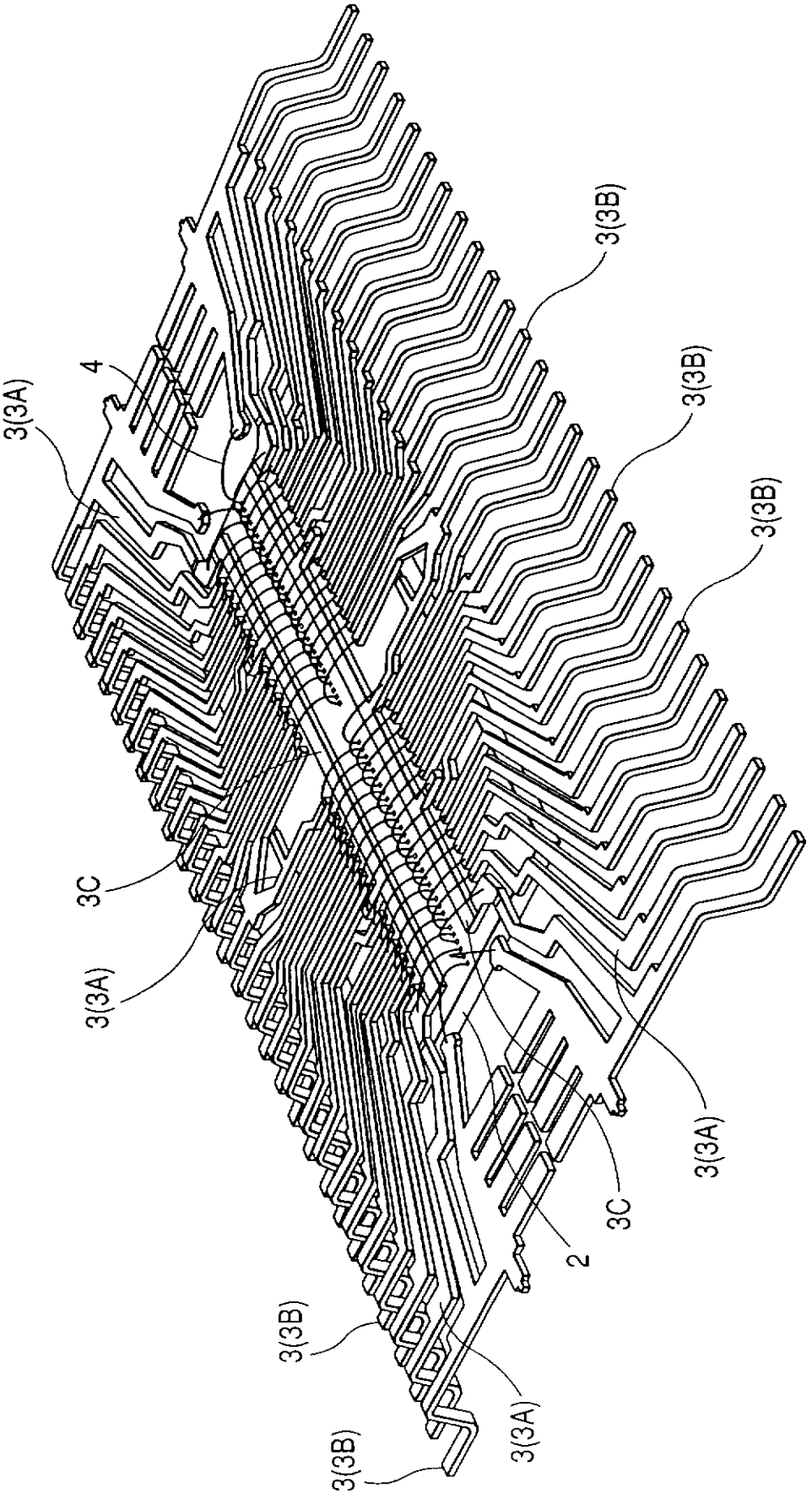


FIG. 3

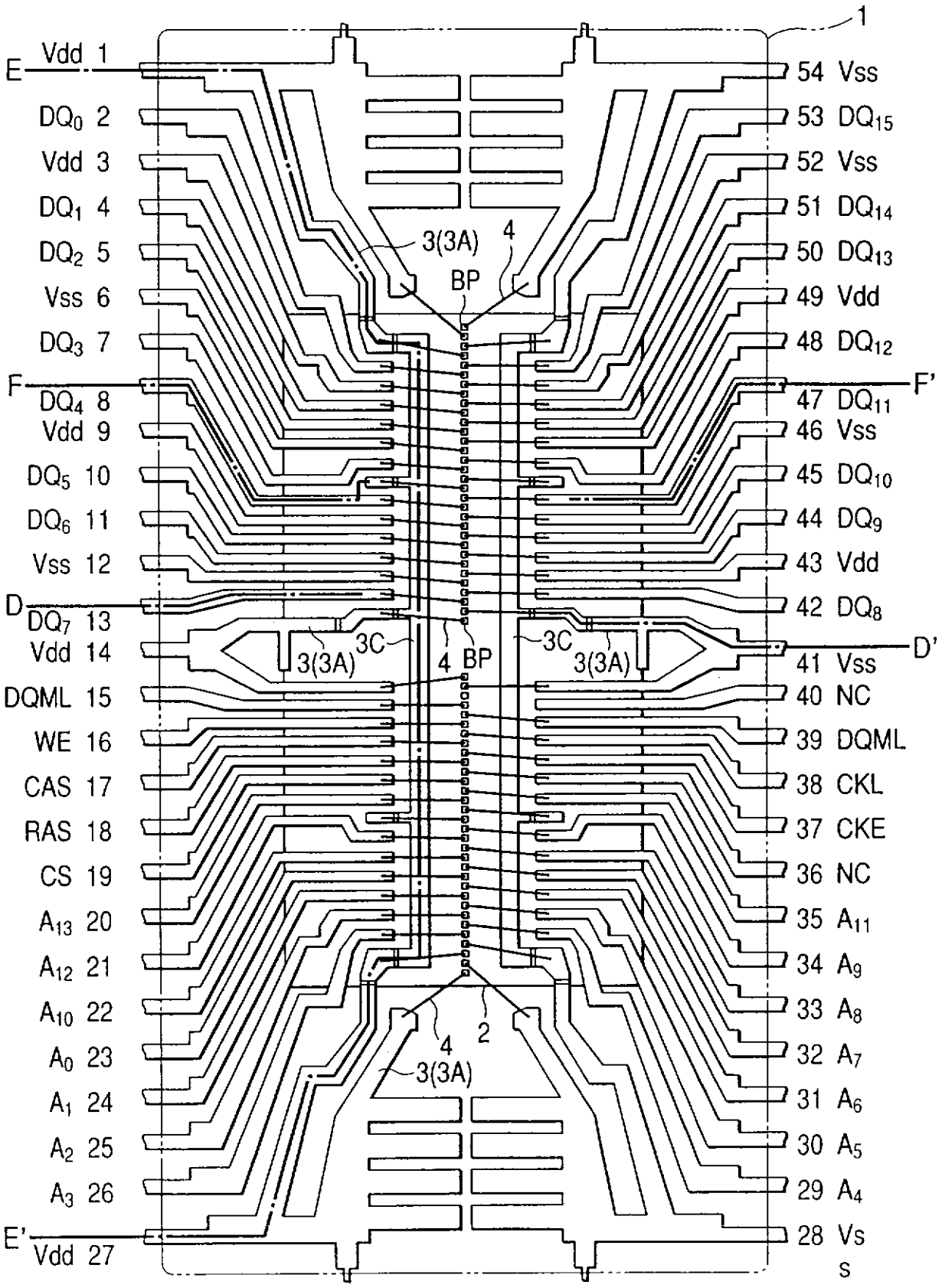

1
E
Vdd 1
DQ0 2
Vdd 3
DQ1 4
DQ2 5
Vss 6
DQ3 7
F
DQ4 8
Vdd 9
DQ5 10
DQ6 11
Vss 12
D
DQ7 13
Vdd 14
DQML 15
WE 16
CAS 17
RAS 18
CS 19
A13 20
A12 21
A10 22
A0 23
A1 24
A2 25
A3 26
E'
Vdd 27
54 Vss
53 DQ15
52 Vss
51 DQ14
50 DQ13
49 Vdd
48 DQ12
F'
47 DQ11
46 Vss
45 DQ10
44 DQ9
43 Vdd
42 DQ8
D'
41 Vss
40 NC
39 DQML
38 CKL
37 CKE
36 NC
35 A11
34 A9
33 A8
32 A7
31 A6
30 A5
29 A4
28 Vss
3(3A)
4
BP
3C
2

FIG. 4

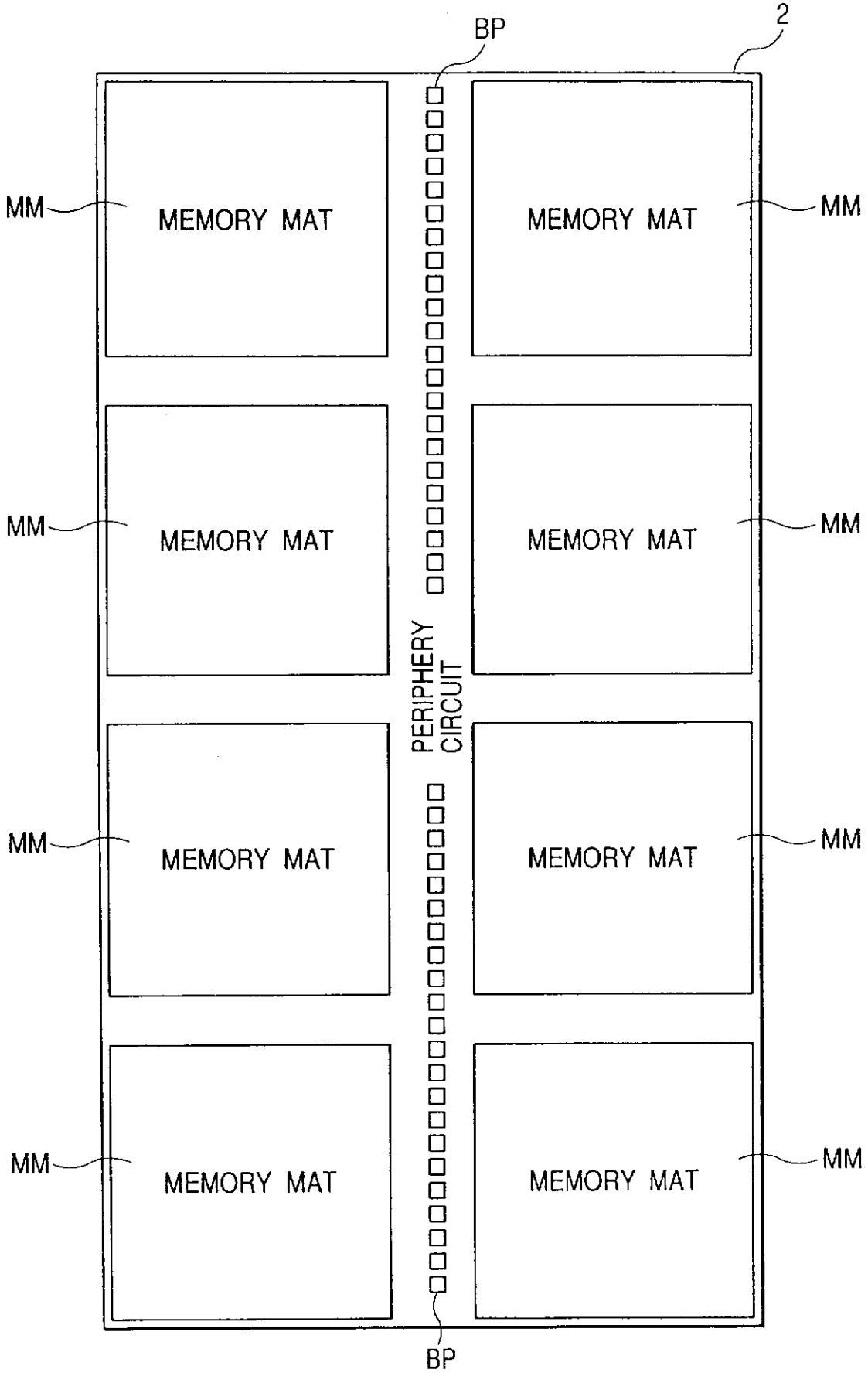

2
BP
MM
MEMORY MAT
MEMORY MAT
MM
MM
MEMORY MAT
MEMORY MAT
MM
MM
MEMORY MAT
MEMORY MAT
MM
MM
MEMORY MAT
MEMORY MAT
MM
PERIPHERY CIRCUIT
BP

FIG. 5(a)

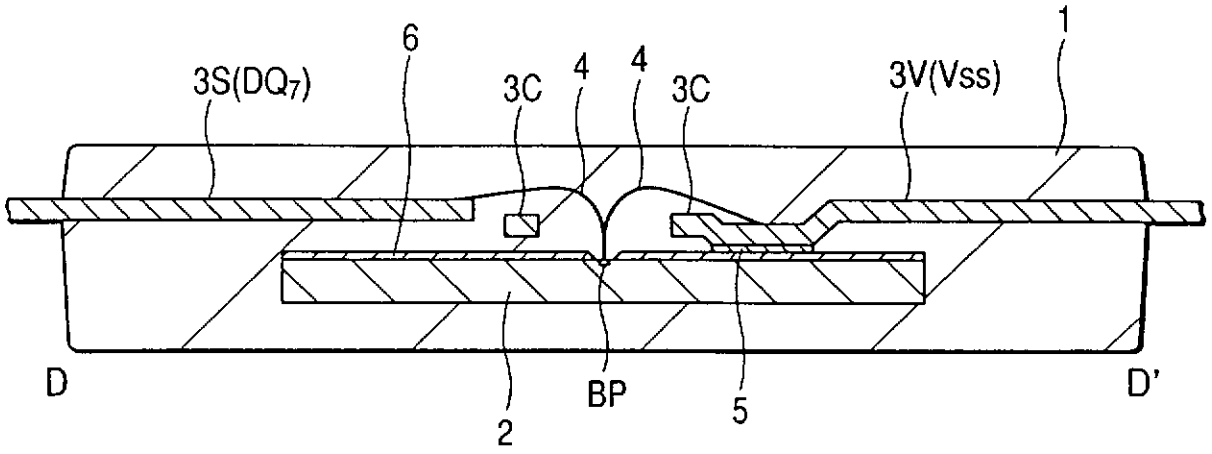


FIG. 5(b)

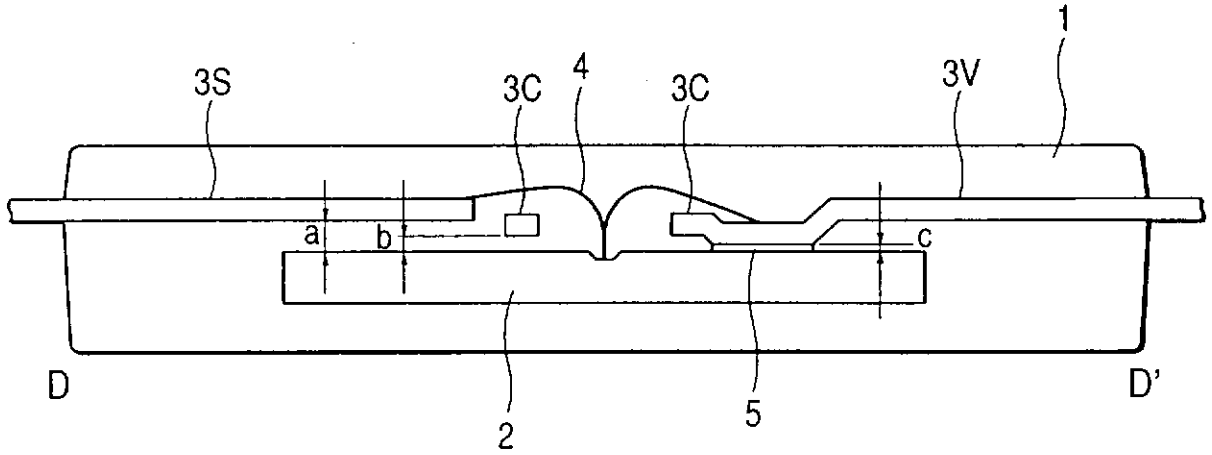


FIG. 6

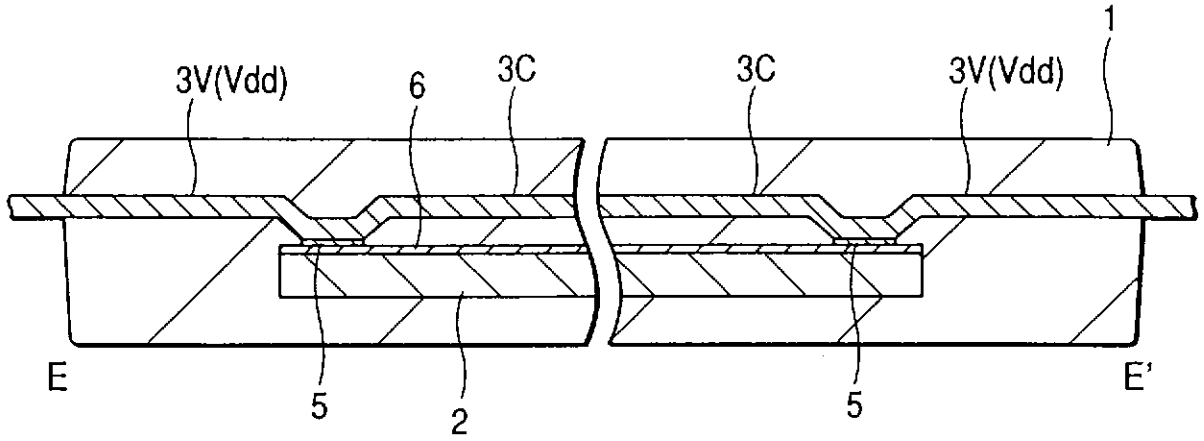


FIG. 7

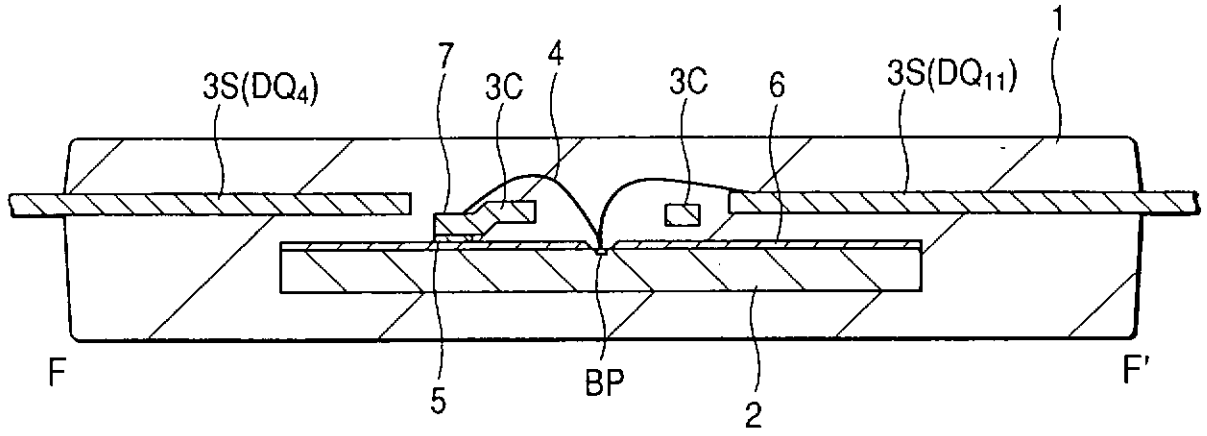


FIG. 8

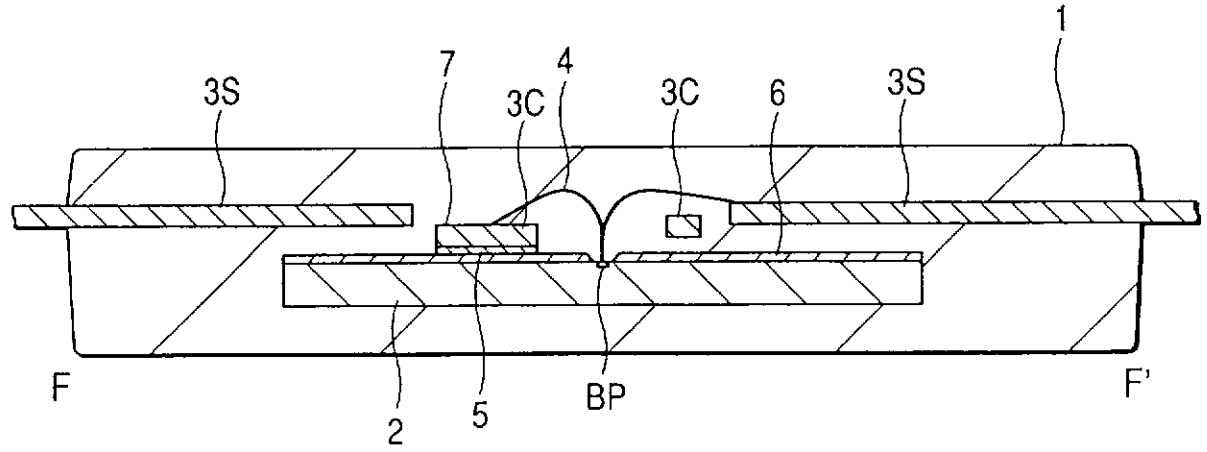


FIG. 9

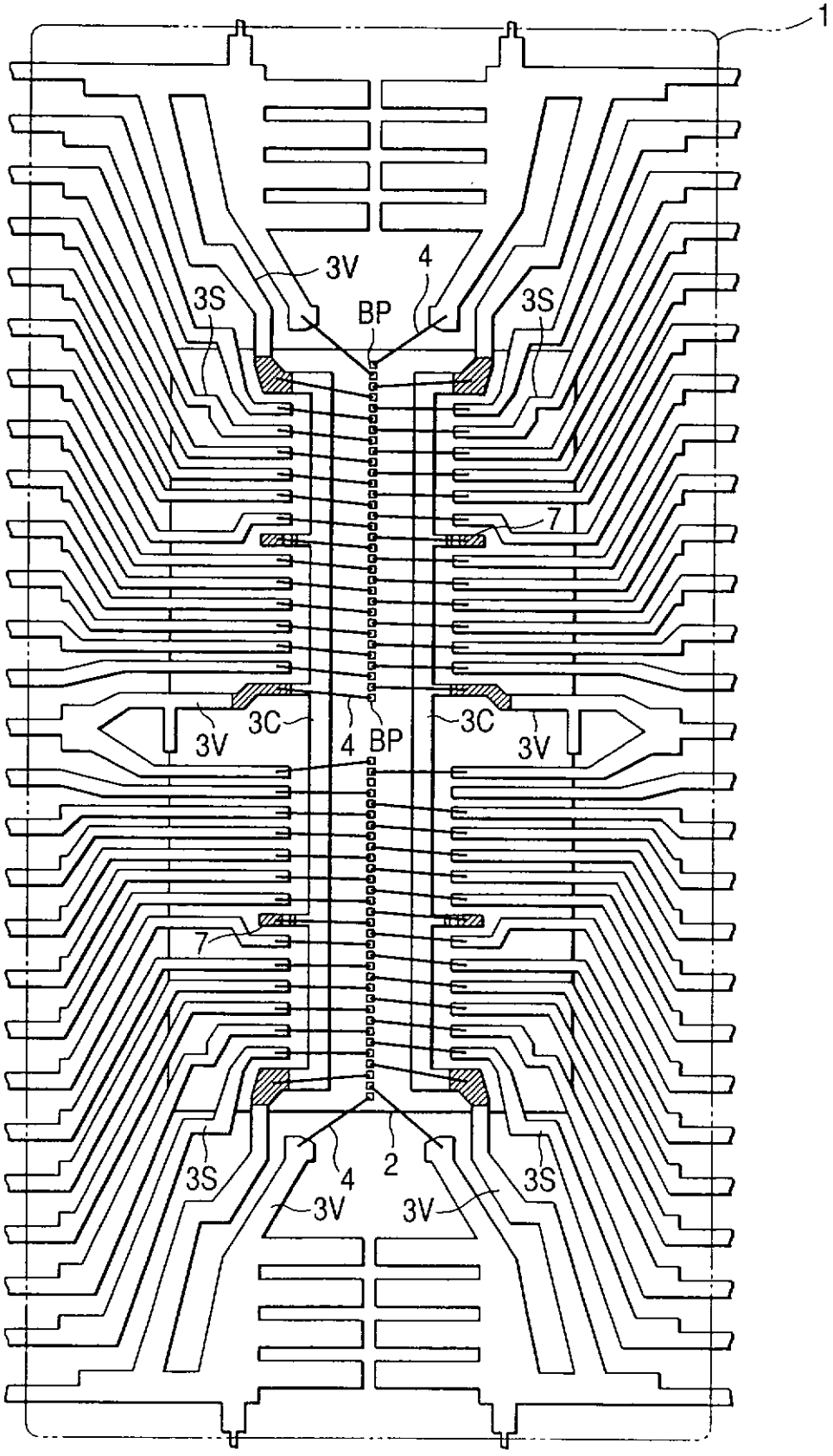


FIG. 10

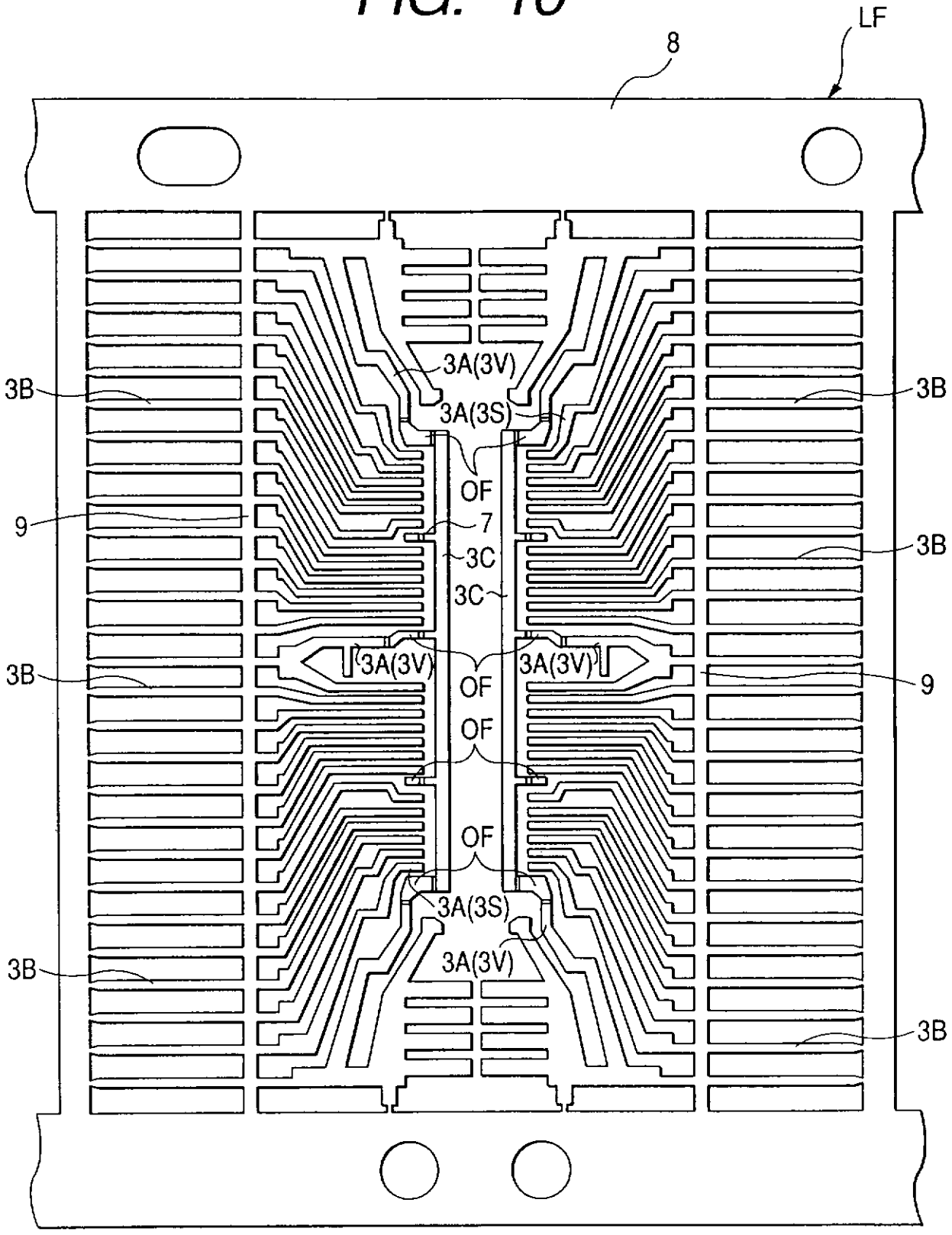

LF
8
3A(3V)
3A(3S)
OF
7
3C
3C
3A(3V)
3A(3V)
OF
OF
OF
3A(3S)
3A(3V)
3B
3B
3B
3B
3B
3B
9
9

FIG. 11

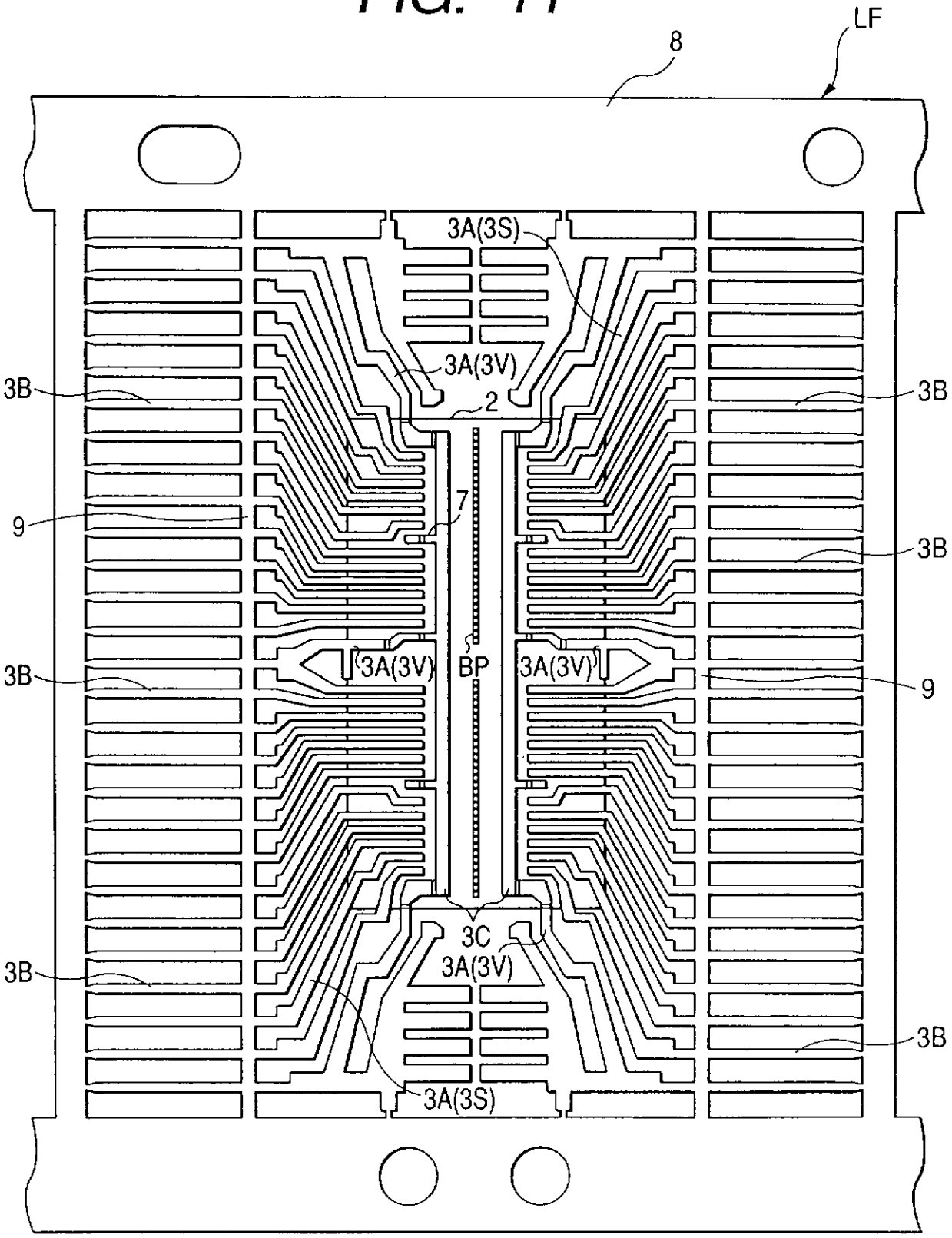


FIG. 12

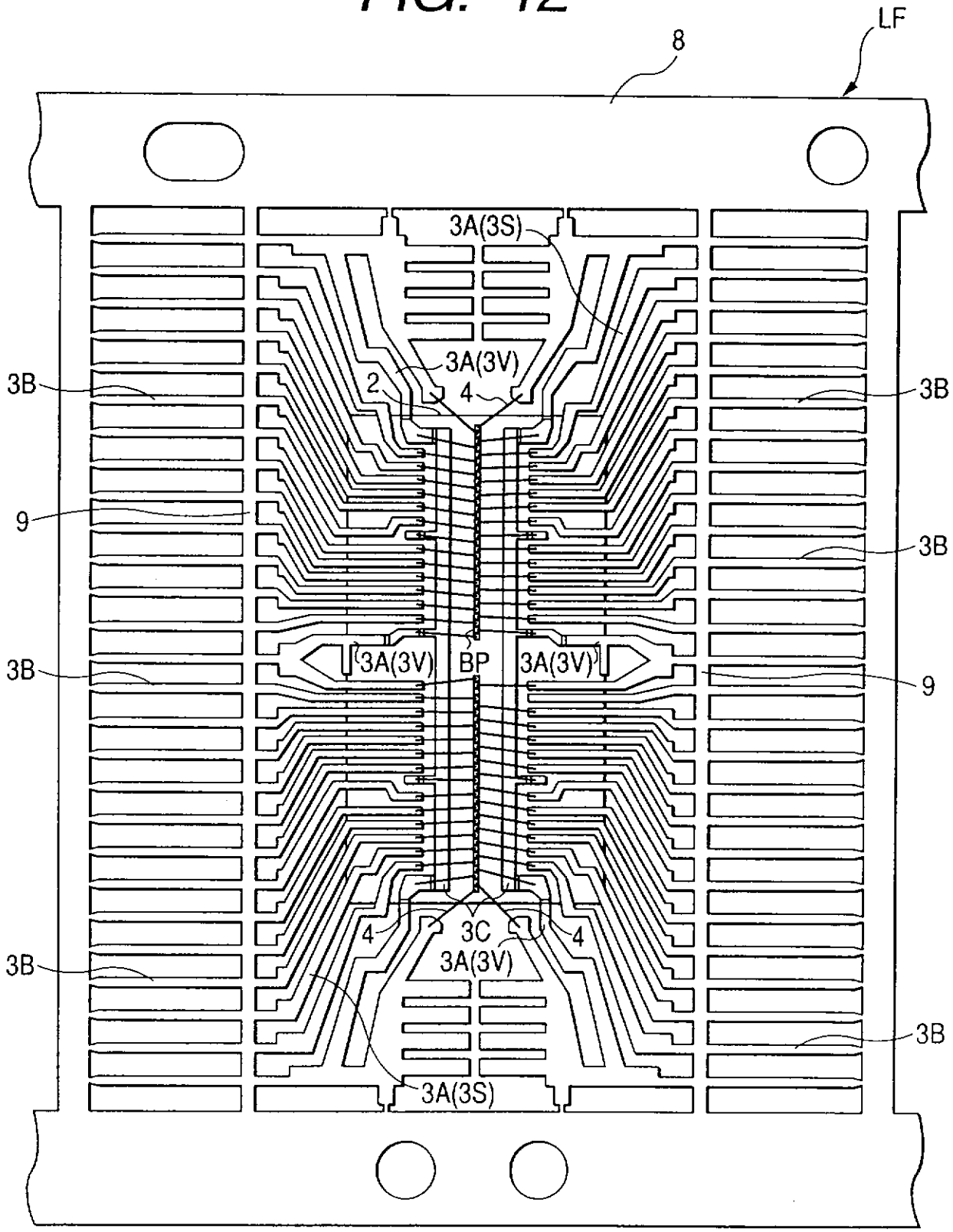


FIG. 13(a)

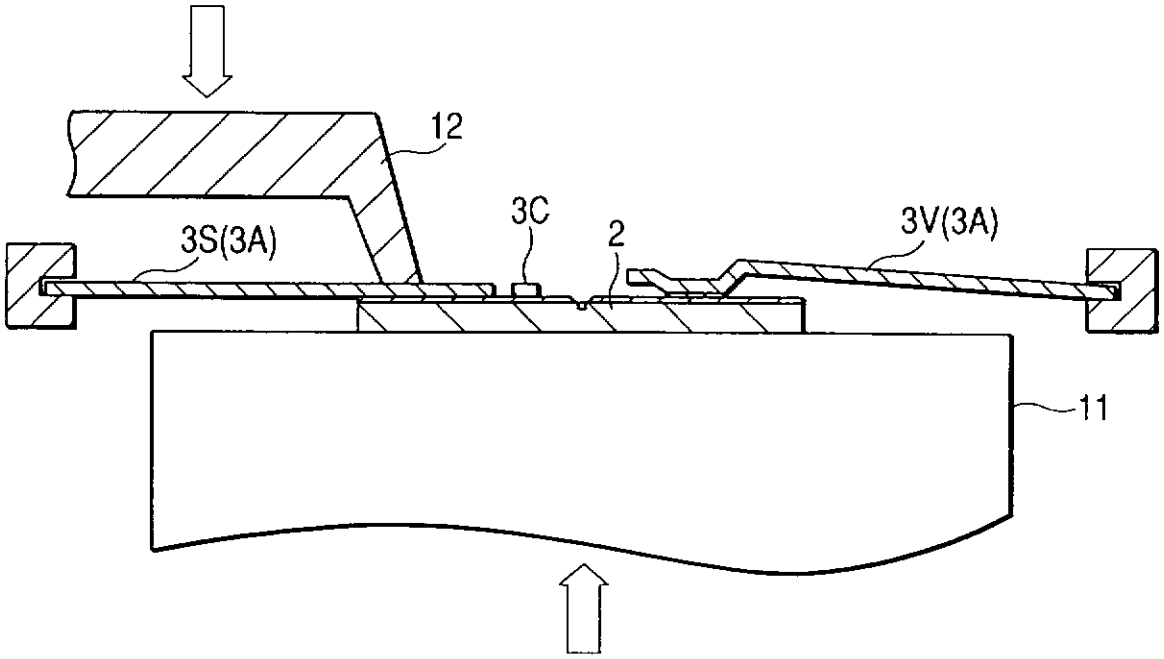


FIG. 13(b)

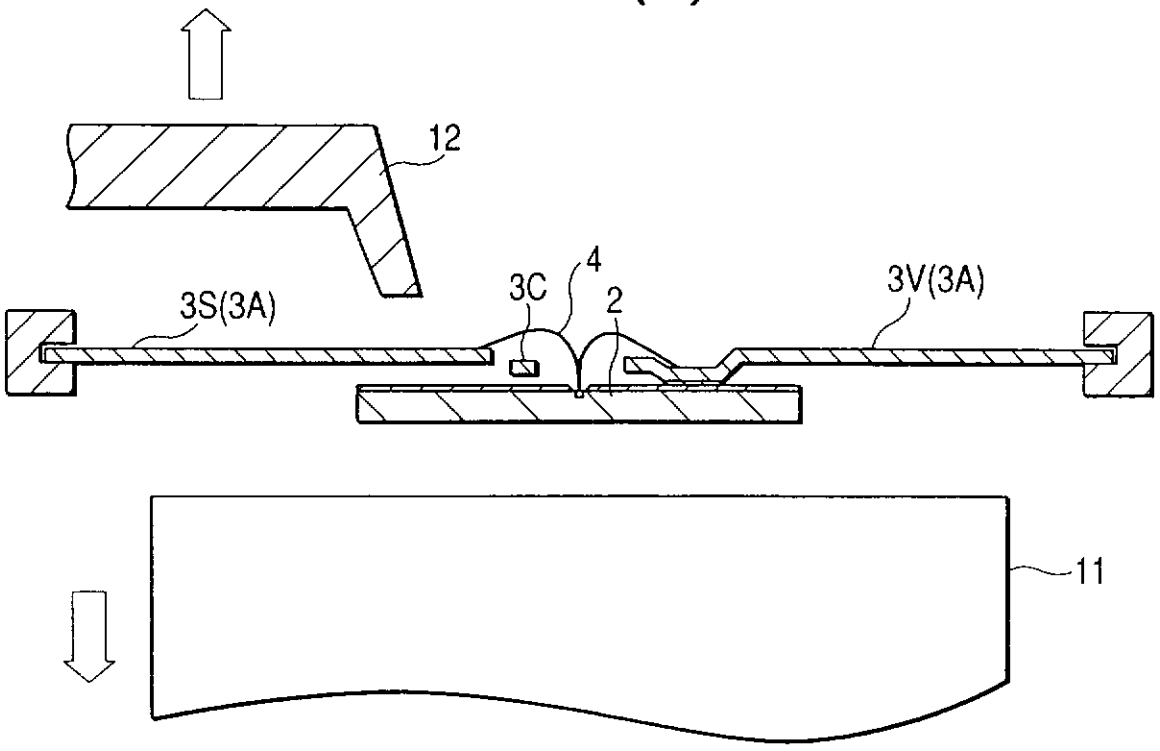


FIG. 14

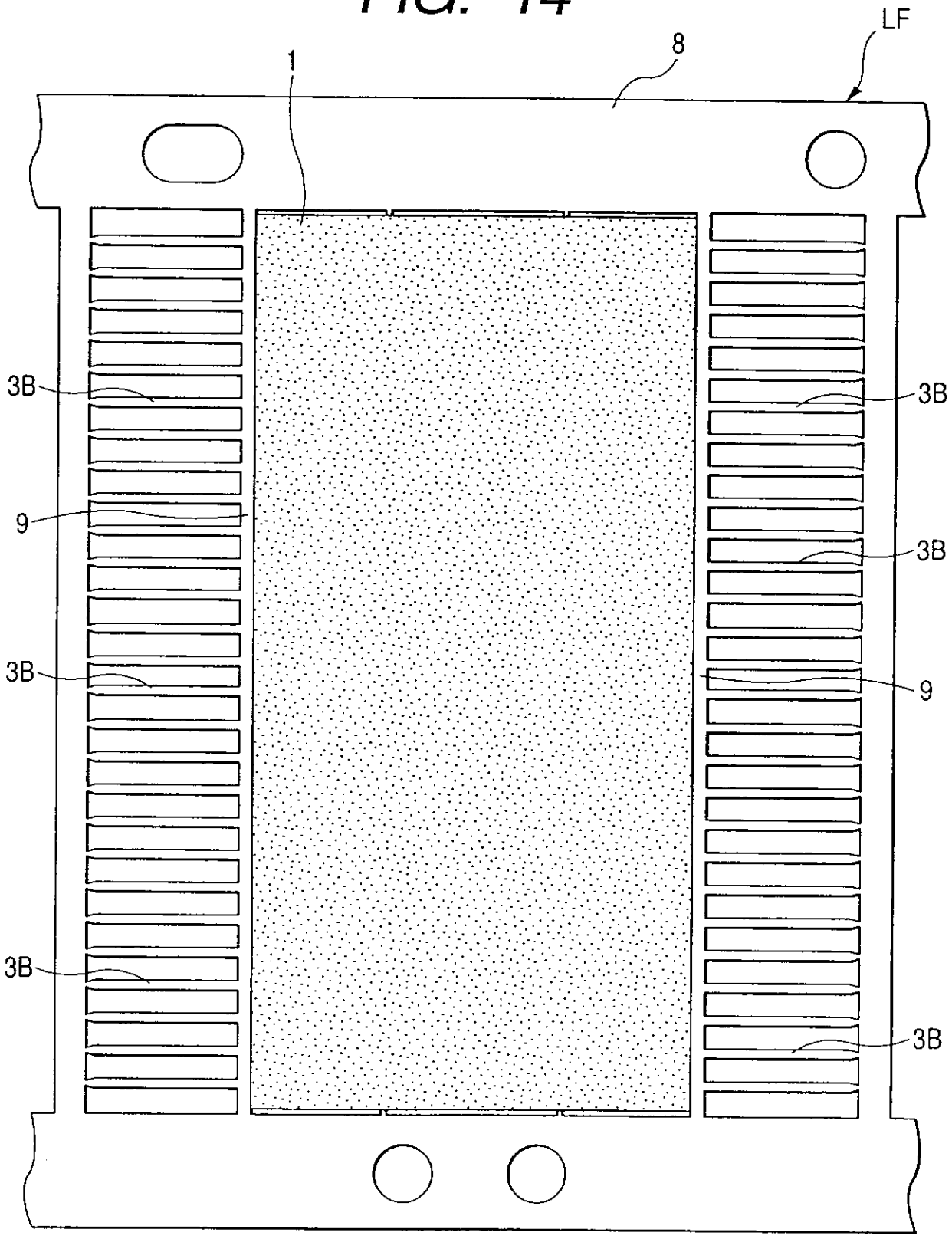


FIG. 15

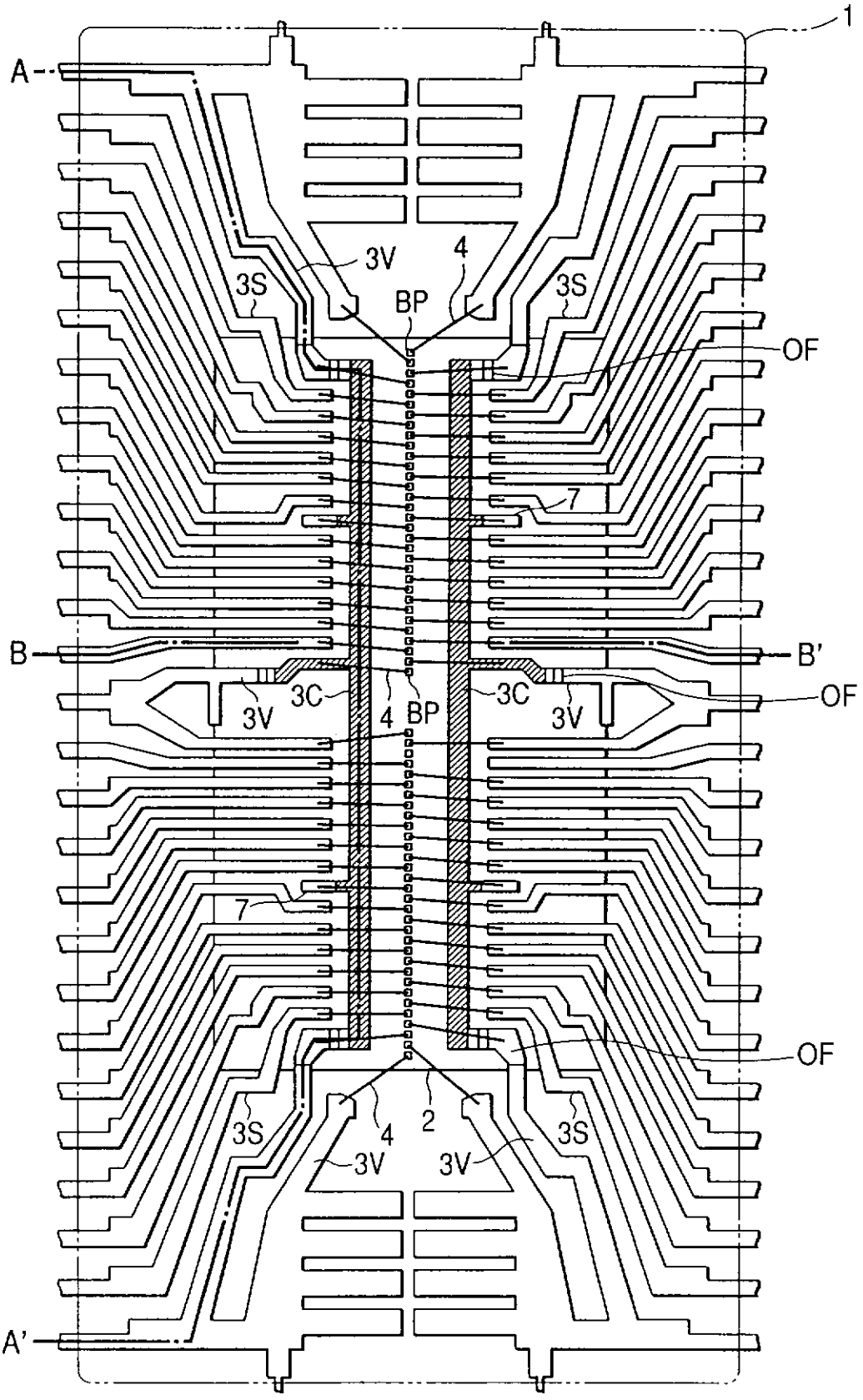


FIG. 16

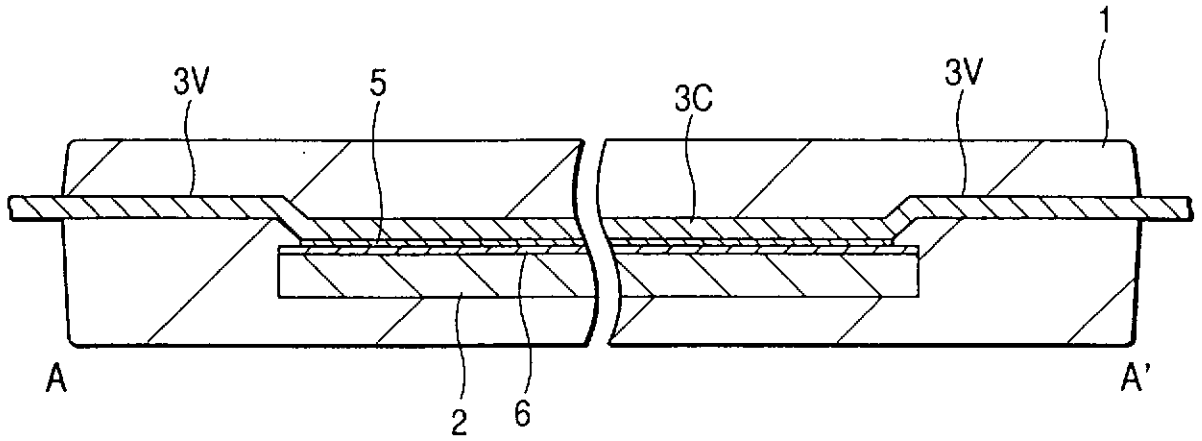


FIG. 17

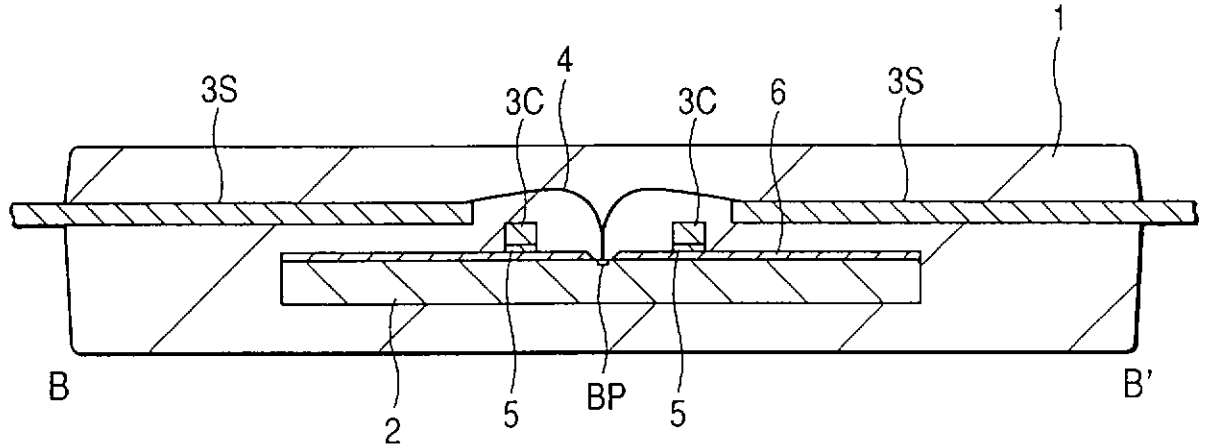


FIG. 18

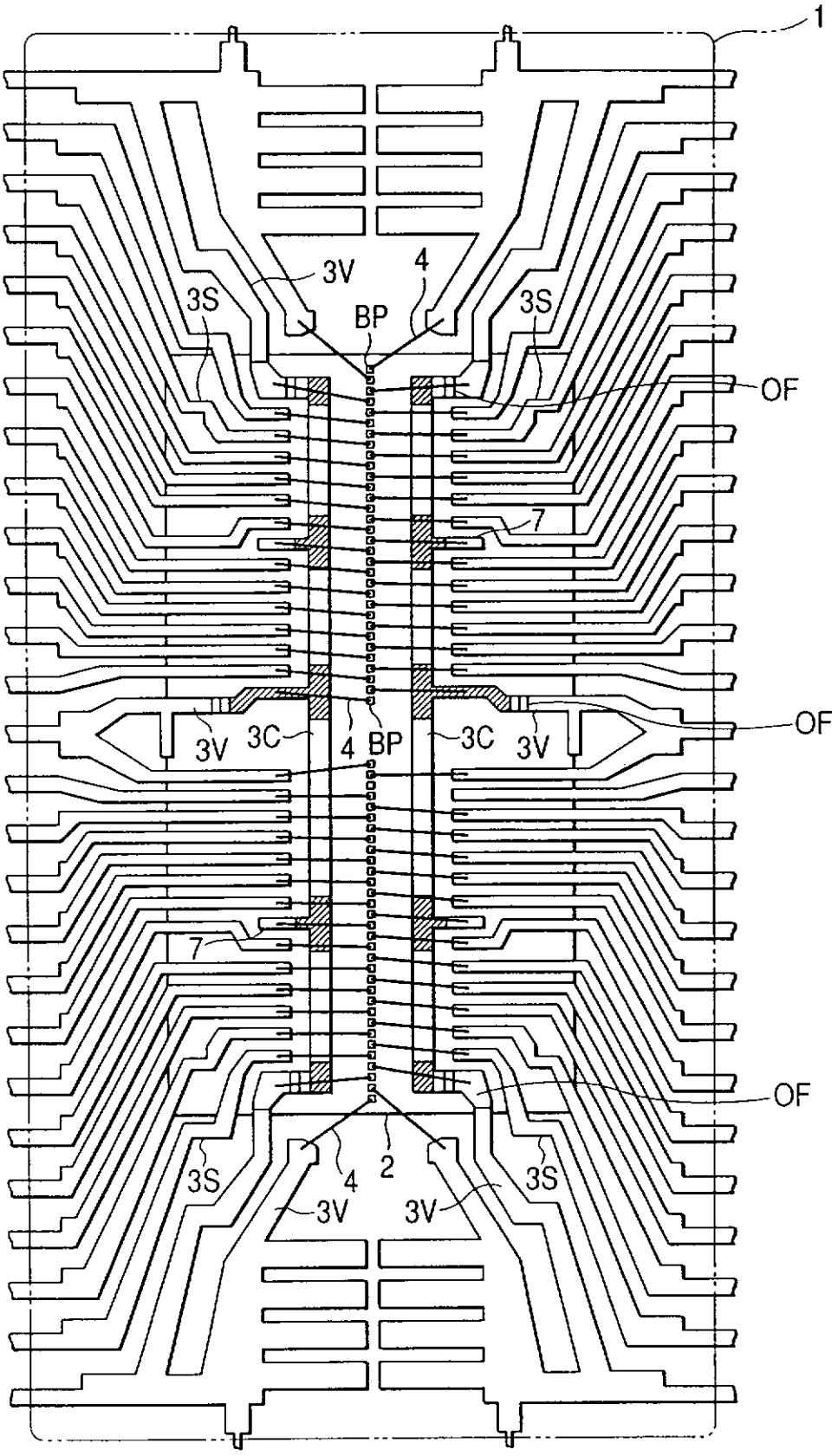


FIG. 19

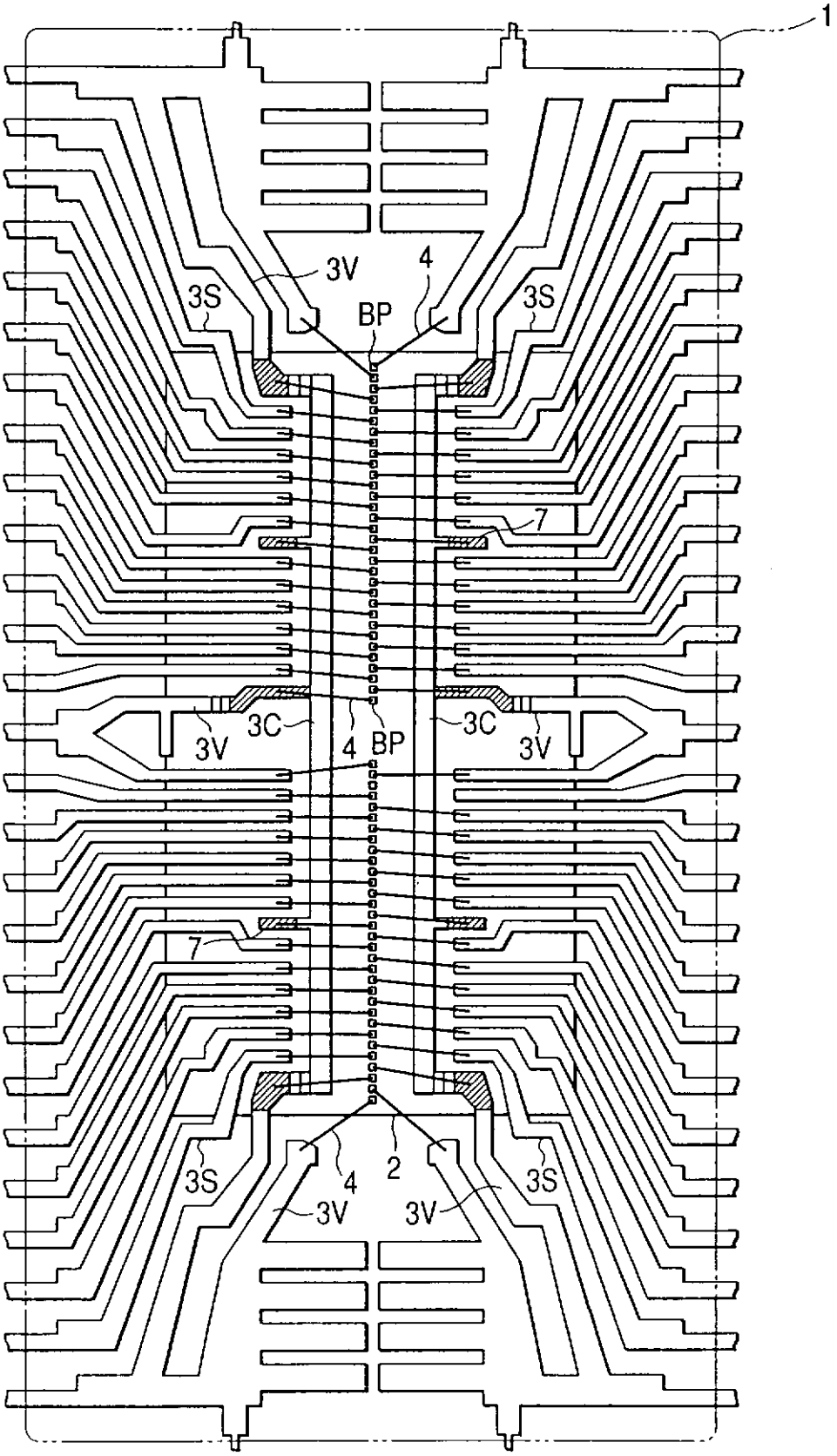


FIG. 20

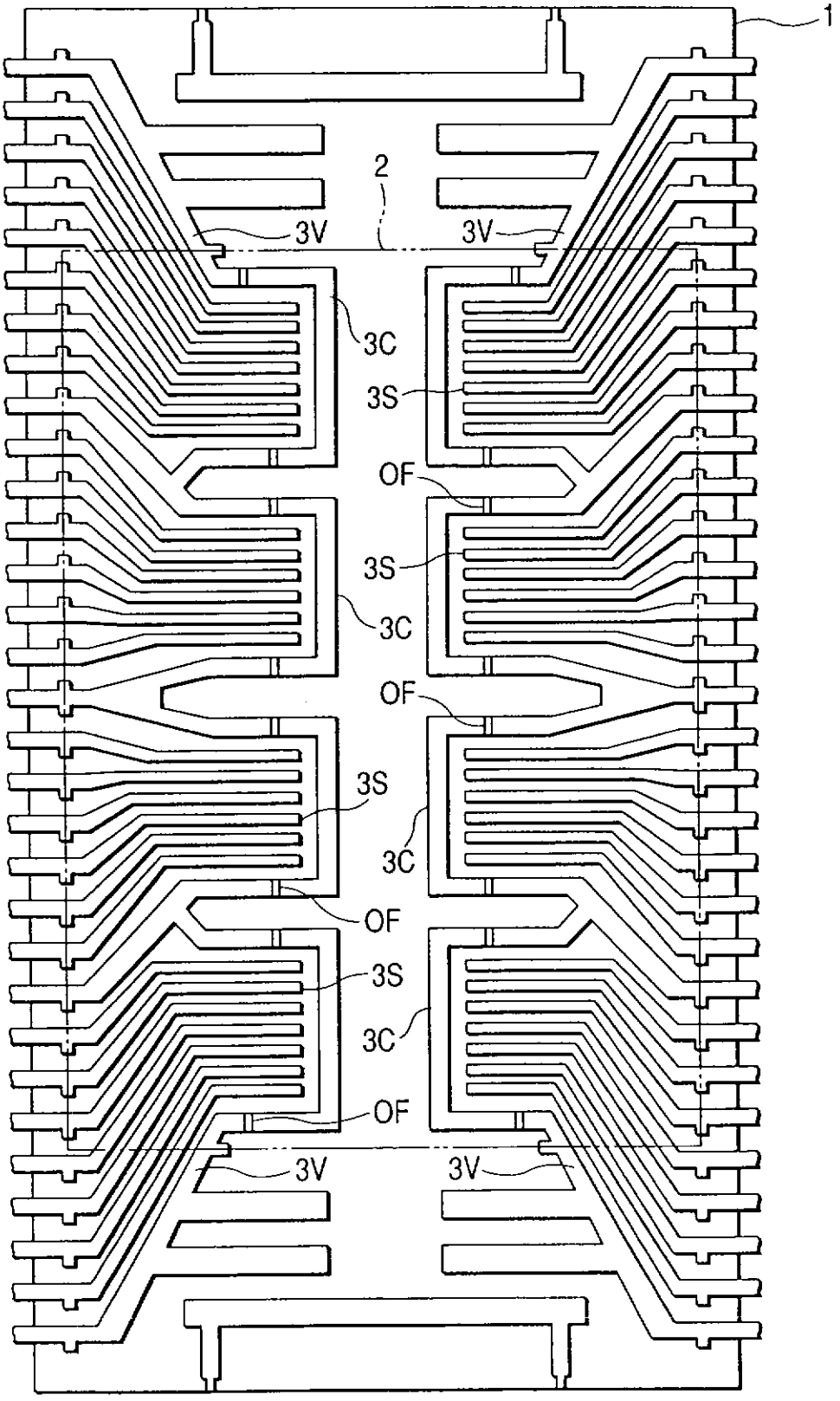


FIG. 21

