

CLAIMS

What is claimed is:

- 1.——_A method for wireless communication, comprising:
 - _receiving a time division duplexing (TDD) multi-slot slot format indicator (SFI) for a set of slots;
 - _identifying that a UE is operating in a frequency division duplexing (FDD) mode; ~~and~~
 - determining, based at least in part on the FDD mode and the TDD multi-slot SFI, a downlink slot format and an uplink slot format for a subset of the set of slots; wherein determining the downlink slot format and the uplink slot format comprises: determining a first slot format indicator for a first slot of the set and a second slot format indicator for a second slot of the set, wherein the first slot precedes the second slot; allocating the first slot format indicator as the downlink slot format for the first slot and the second slot format indicator as the uplink slot format for the first slot; determining a third slot format indicator for a third slot of the set and a fourth slot format indicator for a fourth slot of the set, wherein the third slot precedes the fourth slot; and allocating the third slot format indicator as the downlink slot format for the second slot and the fourth slot format indicator as the uplink slot format for the second slot.
- 2.——_The method of claim 1, further comprising:
 - _performing downlink communications during the subset of the set of slots in accordance with the downlink slot format; and
 - _performing uplink communications during the subset of the set of slots in accordance with the uplink slot format.
- 3.——_The method of claim 1, wherein determining the downlink slot format and the uplink slot format comprises:
 - determining the downlink slot format based at least in part on a first portion of the TDD multi-slot SFI; and
 - _determining the uplink slot format based at least in part on a second portion of the TDD multi-slot SFI.
- 4.——_The method of claim 1, wherein determining the downlink slot format and the uplink slot format comprises:
 - _determining the downlink slot format based at least in part on one or more slot format indicators for a first group of slots of the set; and
 - _determining the uplink slot format based at least in part on one or more slot format indicators for a second group of slots of the set.

5.——_The method of claim 4, wherein:
 _the first group of slots of the set comprises one or more odd-numbered slots of the set; and
 the second group of slots of the set comprises one or more even-numbered slots of the set.

6.——_The method of claim 4, wherein slots of the first group of slots and slots of the second group of slots alternate within the set of slots.

7.——_The method of claim 1, wherein ~~determining the downlink slot format and the uplink slot format comprises:~~

~~determining a first slot format indicator for a first slot of the set and a second slot format indicator for a second slot of the set, wherein the first slot precedes the second slot; and~~

~~allocating the first slot format indicator as the downlink slot format for the first slot and the second slot format indicator as the uplink slot format for the first slot.~~

8.——_The method of claim 7, wherein the first slot immediately precedes the second slot.

9.——_The method of claim 7, further comprising:

~~determining a third slot format indicator for a third slot of the set and a fourth slot format indicator for a fourth slot of the set, wherein the third slot precedes the fourth slot; and~~

~~allocating the third slot format indicator as the downlink slot format for the second slot and the fourth slot format indicator as the uplink slot format for the second slot.~~

10.——8. The method of claim 1, further comprising:
 _communicating with a base station based at least in part on the downlink slot format or the uplink slot format.

11.——9. An apparatus for wireless communication, comprising:
 _means for receiving a time division duplexing (TDD) multi-slot slot format indicator (SFI) for a set of slots;
 _means for identifying that a UE is operating in a frequency division duplexing (FDD) mode; ~~and~~

means for determining, based at least in part on the FDD mode, a downlink slot format and an uplink slot format for a subset of the set of slots from the TDD multi-slot SFI; wherein the means for determining the downlink slot format and the uplink slot format comprises: means for determining a first slot format indicator for a first slot of the set and a second slot format indicator for a second slot of the set, wherein the first slot precedes the second slot; means for allocating the first slot format indicator as the downlink slot format for the first slot and the second slot format indicator as the uplink slot format for the first slot; means for determining a third slot format indicator for a third slot of the set and a fourth slot format indicator for a fourth slot of the set, wherein the third slot precedes the fourth slot; and means for allocating the third slot format indicator as the downlink slot format for the second slot and the fourth slot format indicator as the uplink slot format for the second slot.

~~12.~~—10. The apparatus of claim ~~11~~9, further comprising:

_means for performing downlink communications during the subset of the set of slots in accordance with the downlink slot format; and

_means for performing uplink communications during the subset of the set of slots in accordance with the uplink slot format.

~~13.~~—11. The apparatus of claim ~~12~~9, wherein the means for determining the downlink slot format and the uplink slot format comprises:

_means for determining the downlink slot format based at least in part on a first portion of the TDD multi-slot SFI; and

_means for determining the uplink slot format based at least in part on a second portion of the TDD multi-slot SFI.

~~14.~~—12. The apparatus of claim ~~13~~9, wherein the means for determining the downlink slot format and the uplink slot format comprises:

_means for determining the downlink slot format based at least in part on one or more slot format indicators for a first group of slots of the set; and

_means for determining the uplink slot format based at least in part on one or more slot format indicators for a second group of slots of the set.

~~15.~~—13. The apparatus of claim ~~14~~12, wherein:

_the first group of slots of the set comprises one or more odd-numbered slots of the set; and

_the second group of slots of the set comprises one or more even-numbered slots of the set.

~~16.~~—14. The apparatus of claim ~~14~~12, wherein slots of the first group of slots and slots of the second group of slots alternate within the set of slots.

~~17.~~—15. The apparatus of claim ~~11~~, wherein the means for determining the downlink slot format and the uplink slot format comprises:

~~means for determining a first slot format indicator for a first slot of the set and a second slot format indicator for a second slot of the set, wherein the first slot precedes the second slot; and~~

~~means for allocating the first slot format indicator as the downlink slot format for the first slot and the second slot format indicator as the uplink slot format for the first slot.~~

~~18.~~—The apparatus of claim ~~17~~9, wherein the first slot immediately precedes the second slot.

~~19.~~—16. The apparatus of claim ~~17~~9, further comprising:

~~means for determining a third slot format indicator for a third slot of the set and a fourth slot format indicator for a fourth slot of the set, wherein the third slot precedes the fourth slot; and~~

~~means for allocating the third slot format indicator as the downlink slot format for the second slot and the fourth slot format indicator as the uplink slot format for the second slot.~~

~~20.~~—The apparatus of claim ~~11~~, further comprising:

~~means for communicating with a base station based at least in part on the downlink slot format or the uplink slot format.~~

~~21.~~—17. An apparatus for wireless communication, comprising:

_____a processor;

_____memory in electronic communication with the processor; and

_____instructions stored in the memory and executable by the processor to cause the apparatus to:

_____receive a time division duplexing (TDD) multi-slot slot format indicator (SFI) for a set of slots;

_____identify that a UE is operating in a frequency division duplexing (FDD) mode; and

determine, based at least in part on the FDD mode, a downlink slot format and an uplink slot format for a subset of the set of slots from the TDD multi-slot SFI; wherein the instructions to determine the downlink slot format and the uplink slot format are executable by the processor to cause the apparatus to: determine a first slot format indicator for a first slot of the set and a second slot format indicator for a second slot of the set, wherein the first slot precedes the second slot; allocate the first slot format indicator as the downlink slot format for the first slot and the second slot format indicator as the uplink slot format for the first slot; determine a third slot format indicator for a third slot of the set and a fourth slot format indicator for a fourth slot of the set, wherein the third slot precedes the fourth slot; and allocate the third slot format indicator as the downlink slot format for the second slot and the fourth slot format indicator as the uplink slot format for the second slot.

22.—18. The apparatus of claim ~~24~~17, wherein the instructions are further executable by the processor to cause the apparatus to:

 _perform downlink communications during the subset of the set of slots in accordance with the downlink slot format; and

 _perform uplink communications during the subset of the set of slots in accordance with the uplink slot format.

23.—19. The apparatus of claim ~~24~~17, wherein the instructions to determine the downlink slot format and the uplink slot format are executable by the processor to cause the apparatus to:

 _determine the downlink slot format based at least in part on a first portion of the TDD multi-slot SFI; and

 _determine the uplink slot format based at least in part on a second portion of the TDD multi-slot SFI.

24.—20. The apparatus of claim ~~24~~17, wherein the instructions to determine the downlink slot format and the uplink slot format are executable by the processor to cause the apparatus to:

 _determine the downlink slot format based at least in part on one or more slot format indicators for a first group of slots of the set; and

 _determine the uplink slot format based at least in part on one or more slot format indicators for a second group of slots of the set.

25.—21. The apparatus of claim ~~24~~20, wherein:

 _the first group of slots of the set comprises one or more odd-numbered slots of the set; and

the second group of slots of the set comprises one or more even-numbered slots of the set.

26.—~~22.~~ The apparatus of claim ~~24~~20, wherein slots of the first group of slots and slots of the second group of slots alternate within the set of slots.

27.—~~23.~~ The apparatus of claim ~~21~~, wherein the instructions to determine the downlink slot format and the uplink slot format are executable by the processor to cause the apparatus to:

~~determine a first slot format indicator for a first slot of the set and a second slot format indicator for a second slot of the set, wherein the first slot precedes the second slot; and~~

~~allocate the first slot format indicator as the downlink slot format for the first slot and the second slot format indicator as the uplink slot format for the first slot.~~

28.—~~The apparatus of claim 27~~17, wherein the first slot immediately precedes the second slot.

29.—~~24.~~ The apparatus of claim ~~27~~17, wherein the instructions are further executable by the processor to cause the apparatus to:

~~determine a third slot format indicator for a third slot of the set and a fourth slot format indicator for a fourth slot of the set, wherein the third slot precedes the fourth slot; and~~

~~allocate the third slot format indicator as the downlink slot format for the second slot and the fourth slot format indicator as the uplink slot format for the second slot.~~

30.—~~The apparatus of claim 21, wherein the instructions are further executable by the processor to cause the apparatus to:~~

~~_communicate with a base station based at least in part on the downlink slot format or the uplink slot format.~~

31.—~~25.~~ A non-transitory computer-readable medium storing code for wireless communication, the code comprising instructions executable by a processor to:

~~_receive a time division duplexing (TDD) multi-slot slot format indicator (SFI) for a set of slots;~~

identify that a UE is operating in a frequency division duplexing (FDD) mode; and

determine, based at least in part on the FDD mode, a downlink slot format and an uplink slot format for a subset of the set of slots from the TDD multi-slot SFI; wherein the instructions to determine the downlink slot format and the uplink slot format are executable by the processor to: determine a first slot format indicator for a first slot of the set and a second slot format indicator for a second slot of the set, wherein the first slot precedes the second slot; allocate the first slot format indicator as the downlink slot format for the first slot and the second slot format indicator as the uplink slot format for the first slot; determine a third slot format indicator for a third slot of the set and a fourth slot format indicator for a fourth slot of the set, wherein the third slot precedes the fourth slot; and allocate the third slot format indicator as the downlink slot format for the second slot and the fourth slot format indicator as the uplink slot format for the second slot.

~~32.~~—26. The non-transitory computer-readable medium of claim ~~34~~25, wherein the instructions are further executable by the processor to:

perform downlink communications during the subset of the set of slots in accordance with the downlink slot format; and

perform uplink communications during the subset of the set of slots in accordance with the uplink slot format.

~~33.~~—27. The non-transitory computer-readable medium of claim ~~34~~25, wherein the instructions to determine the downlink slot format and the uplink slot format are executable by the processor to:

determine the downlink slot format based at least in part on a first portion of the TDD multi-slot SFI; and

determine the uplink slot format based at least in part on a second portion of the TDD multi-slot SFI.

~~34.~~—28. The non-transitory computer-readable medium of claim ~~34~~25, wherein the instructions to determine the downlink slot format and the uplink slot format are executable by the processor to:

determine the downlink slot format based at least in part on one or more slot format indicators for a first group of slots of the set; and

determine the uplink slot format based at least in part on one or more slot format indicators for a second group of slots of the set.

~~35.~~—29. The non-transitory computer-readable medium of claim ~~34~~28, wherein:

~~_~~the first group of slots of the set comprises one or more odd-numbered slots of the set; and

the second group of slots of the set comprises one or more even-numbered slots of the set.

~~36.~~—30. The non-transitory computer-readable medium of claim ~~34~~28, wherein slots of the first group of slots and slots of the second group of slots alternate within the set of slots.

~~37.~~—31. The non-transitory computer-readable medium of claim ~~31~~, wherein the instructions to determine the downlink slot format and the uplink slot format are executable by the processor to:

~~determine a first slot format indicator for a first slot of the set and a second slot format indicator for a second slot of the set, wherein the first slot precedes the second slot; and~~

~~allocate the first slot format indicator as the downlink slot format for the first slot and the second slot format indicator as the uplink slot format for the first slot.~~

~~38.~~—The non-transitory computer-readable medium of claim ~~37~~25, wherein the first slot immediately precedes the second slot.

~~39.~~—32. The non-transitory computer-readable medium of claim ~~37~~25, wherein the instructions are further executable by the processor to:

~~determine a third slot format indicator for a third slot of the set and a fourth slot format indicator for a fourth slot of the set, wherein the third slot precedes the fourth slot; and~~

~~allocate the third slot format indicator as the downlink slot format for the second slot and the fourth slot format indicator as the uplink slot format for the second slot.~~

~~40.~~—The non-transitory computer-readable medium of claim ~~31~~, wherein the instructions are further executable by the processor to:

communicate with a base station based at least in part on the downlink slot format or the uplink slot format.