

ABSTRACT OF THE DISCLOSURE**D/A CONVERTER AND DELTA-SIGMA D/A CONVERTER**

A D/A converter for converting a given digital signal into an analog signal includes a plurality of capacitors ($C_1, C_2 \dots, C_i$) for storing an electric charge corresponding to a
5 predetermined reference voltage ($V_r +$ or $V_r -$). The reference voltage is selected depending on the digital signal during a period when a clock ϕ_1 is at a high level. A switch selection ($SUG_1 - SUG_i, SB$) is used to connect each of the plurality of capacitors between an input terminal and an output terminal of an operational amplifier 100 during a period when a clock ϕ_2 is at a high level.
(Figure 5)