

CLAIMS

What is claimed is:

1. An interface parameter update method, comprising:

updating, by a multi-link, ML, device based on a status change of each of a plurality of
5 network interfaces comprised in the ML device, parameters of enabled network interfaces in the
plurality of network interfaces, wherein the parameters comprise at least one of capability
information or an operation parameter, the plurality of network interfaces work on different
frequency bands, and the plurality of network interfaces share antennas configured in the ML
device.

10 2. The method according to claim 1, wherein the updating, by a multi-link ML device based
on a status change of each of a plurality of network interfaces comprised in the multi-link ML
device, parameters of enabled network interfaces in the plurality of network interfaces comprises:

after enabling or disabling one of the plurality of network interfaces, configuring, by the ML
device, parameters of each of all the enabled network interfaces in the plurality of network
15 interfaces, and sending the parameters of all the enabled network interfaces through each of the
enabled network interfaces.

3. The method according to claim 1 or 2, wherein the parameters comprise at least one of a
configured quantity of transmit and receive antennas, a maximum supported quantity of to-be-sent
or to-be-received streams, a parameter that indicates whether simultaneous sending and receiving
20 with another network interface is supported, a highest modulation and coding scheme that is
allowed to be used by devices at two ends of a link when adjacent channel interference exists on
the link, a security margin that needs to be reserved at a transmit end of the link, channel bandwidth,
or transmit power.

4. A multi-link, ML, device, wherein the ML device comprises a module configured to
25 implement the method according to any one of claims 1 to 3.

5. A multi-link, ML, device, wherein the ML device comprises a memory and a processor, the
memory stores a program used to perform the method according to any one of claims 1 to 3, and
the processor invokes the program stored in the memory to perform the method according to any
one of claims 1 to 3.

30 6. A computer-readable storage medium, wherein the computer-readable storage medium

stores instructions; and when the instructions are run on a computer, the computer is enabled to perform the method according to any one of claims 1 to 3.

7. A computer program product comprising instructions, wherein when the computer program product runs on a computer, the computer is enabled to perform the method according to any one
5 of claims 1 to 3.

8. A chip, wherein the chip comprises a processing circuit and an interface circuit, the interface circuit is configured to receive instructions and transmit the instructions to the processing circuit, and the processing circuit is configured to perform the method according to any one of
claims 1 to 3.

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