

ABSTRACT

LIQUID CRYSTAL DISPLAY DEVICE

When a first source bus line (SA) or a second source bus line (SB) which is associated with a pixel of one column is electrically connected with a spare wire (AL1), a control circuit (15B) of a liquid crystal display device (100B) according to an embodiment of the present invention is capable of controlling the amplitudes of the oscillation of a first display signal voltage and a second display signal voltage output to the first source bus line (SA) and the second source bus line (SB) so as to be smaller than that achieved when the first source bus line (SA) or the second source bus line (SB) is not electrically connected with the spare wire (AL1).

Figure 1